

LM120QML

www.ti.com

SNVS368A – DECEMBER 2010 – REVISED APRIL 2013

LM120QML Series 3-Terminal Negative Regulators

Check for Samples: LM120QML

FEATURES

- Preset Output Voltage Error Less Than ±3%
- Preset Current Limit
- Internal Thermal Shutdown
- Operates with Input-Output Voltage Differential
 Down to 1V
- Excellent Ripple Rejection
- Low Temperature Drift
- Easily Adjustable to Higher Output Voltage

DESCRIPTION

The LM120 series are three-terminal negative regulators with a fixed output voltage of -5V, -12V, and -15V, and up to 1.5A load current capability. Where other voltages are required, the LM137 and LM137HV series provide an output voltage range of -1.2V to -47V.

The LM120 needs only one external component—a compensation capacitor at the output, making them easy to apply. Worst case specifies on output voltage deviation due to any combination of line, load or temperature variation assure satisfactory system operation.

Exceptional effort has been made to make the LM120 Series immune to overload conditions. The regulators have current limiting which is independent of temperature, combined with thermal overload protection. Internal current limiting protects against momentary faults while thermal shutdown prevents junction temperatures from exceeding safe limits during prolonged overloads.

Although primarily intended for fixed output voltage applications, the LM120 Series may be programmed for higher output voltages with a simple resistive divider. The low quiescent drain current of the devices allows this technique to be used with good regulation.

	• •	
	Rated	Design
Package	Power	Load
	Dissipation	Current
ТО (К)	20W	1.5A
PFM (H)	2W	0.5A

Table 1. LM120 Series Packages and Power Capability

Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet. All trademarks are the property of their respective owners.

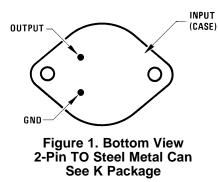
LM120QML

SNVS368A - DECEMBER 2010 - REVISED APRIL 2013



www.ti.com

Connection Diagram



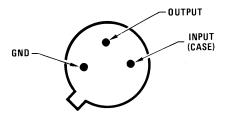
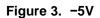
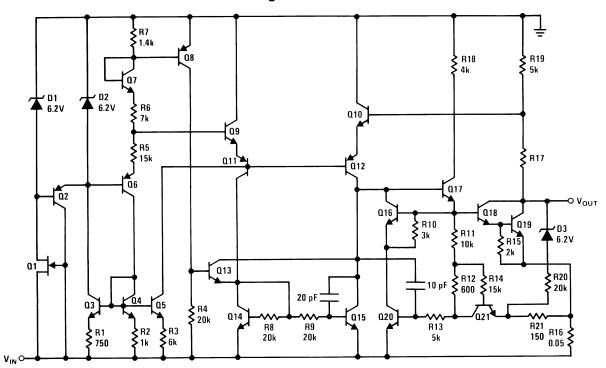


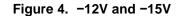
Figure 2. Bottom View 3-Pin PFM Metal Can See NDT0003A Package

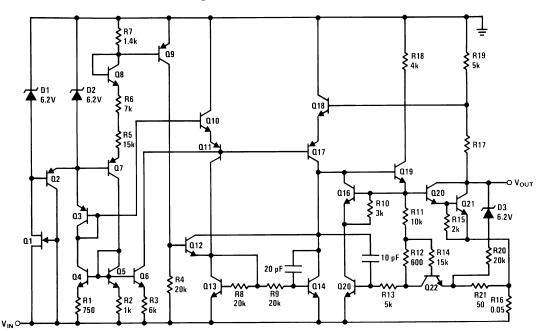
Schematic Diagrams













These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

ABSOLUTE MAXIMUM RATINGS⁽¹⁾

			LM120-5	LM120-12	LM120-15	
Power Dissipation			Internally Limited			
Input Voltage			-25V	-35V	-40V	
Input-Output Voltage Differential			25V	30V	30V	
Junction Temperatures				150°C		
Storage Temperature Range			-	65°C ≤ T _A ≤ +150	°C	
Operating Temperature Range			$-55^{\circ}C \le T_{A} \le +125^{\circ}C$			
Lead Temperature (Soldering, 10 sec.)			300°C			
Thermal Resistance	θ _{JA}	PFM-Pkg (Still Air @ 0.5W)	191°C/W			
		PFM-Pkg (500LF/Min Air flow @ 0.5W)		70°C/W		
		TO-Pkg (Still Air @ 0.5W)	35°C/W			
		TO-Pkg (500LF/Min Air flow @ 0.5W)		TBD		
	θ _{JC} PFM-Pkg			29°C/W		
		TO-Pkg		3°C/W		
ESD Tolerance ⁽²⁾				4000V		

(1) Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not ensure specific performance limits. For ensured specifications and test conditions, see the Electrical Characteristics. The ensured specifications apply only for the test conditions listed. Some performance characteristics may degrade when the device is not operated under the listed test conditions.

(2) Human body model, $1.5 \text{ k}\Omega$ in seriew with 100 pF.

SNVS368A - DECEMBER 2010-REVISED APRIL 2013

	Mil-Std-883, Method 5005 - Group A	
Subgroup	Description	Temp (°C)
1	Static tests at	+25
2	Static tests at	+125
3	Static tests at	-55
4	Dynamic tests at	+25
5	Dynamic tests at	+125
6	Dynamic tests at	-55
7	Functional tests at	+25
8A	Functional tests at	+125
8B	Functional tests at	-55
9	Switching tests at	+25
10	Switching tests at	+125
11	Switching tests at	-55
12	Settling time at	+25
13	Settling time at	+125
14	Settling time at	-55

LM120H-5.0 DC PARAMETERS

The following conditions apply, unless otherwise specified. $V_{IN} = -10V$, $I_L = 5mA$

Symbol	Parameter	Conditions	Notes	Min	Max	Unit	Sub- groups
	Ouissesst Current	$V_{IN} = -7V$			2.0	mA	1, 2, 3
l _Q	Quiescent Current	V _{IN} = -25V			2.0	mA	1, 2, 3
				-0.4	0.4	mA	1
A.I.	Outer and Outer at Ohen an	$5\text{mA} \le \text{I}_{\text{L}} \le 0.5\text{A}$		-0.5	0.5	mA	2, 3
Δl _Q	Quiescent Current Change			-0.4	0.4	mA	1
		$-25V \le V_{IN} \le -7V$		-0.5	0.5	mA	2, 3
				-5.1	-4.9	V	1
		V _{IN} = -7.5V		-5.2	-4.8	V	1, 2, 3
V _{OUT}	Output Voltage	$V_{IN} = -7.5V, I_L = 0.5A$		-5.2	-4.8	V	1, 2, 3
		V _{IN} = -25V		-5.2	-4.8	V	1, 2, 3
		$V_{IN} = -25V, I_{L} = 100mA$		-5.2	-4.8	V	1, 2, 3
D	Line Deculation			-25	25	mV	1
R _{Line}	Line Regulation	$-25V \le V_{IN} \le -7V$		-50	50	mV	2, 3
Р	Lood Degulation	$F_{m} \wedge c \downarrow c \wedge c$		-50	50	mV	1
R _{Load}	Load Regulation	$5\text{mA} \le \text{I}_{\text{L}} \le 0.5\text{A}$		-100	100	mV	2, 3
I _{OS}	Short Circuit Current	V _{IN} = -25V		0.1	1.5	А	1
RR	Ripple Rejection	f = 120Hz, I _L = 125mA, e _I = 1V _{RMS}		54		dB	4

Table 2. QUALITY CONFORMANCE INSPECTION

www.ti.com



SNVS368A – DECEMBER 2010 – REVISED APRIL 2013

www.ti.com

LM120K-5.0 DC PARAMETERS

The following conditions apply, unless otherwise specified. $V_{IN} = -10V$, $I_L = 5mA$

Symbol	Parameter	Conditions	Notes	Min	Мах	Unit	Sub- groups
	Quiescent Current	$V_{IN} = -7V$			2.0	mA	1, 2, 3
l _Q	Quescent Current	V _{IN} = -25V			2.0	mA	1, 2, 3
				-0.4	0.4	mA	1
		$5\text{mA} \le \text{I}_{\text{L}} \le 1.5\text{A}$		-0.5	0.5	mA	2, 3
ΔI _Q	Quiescent Current Change			-0.4	0.4	mA	1
	$-25V \le V_{IN} \le -7V$		-0.5	0.5	mA	2, 3	
			-5.1	-4.9	V	1	
		V _{IN} = -7.5V		-5.2	-4.8	V	1, 2, 3
V _{OUT}	Output Voltage	V _{IN} = -7.5V, I _L = 1.5A		-5.2	-4.8	V	1, 2, 3
		V _{IN} = -25V		-5.2	-4.8	V	1, 2, 3
		V _{IN} = -25V, I _L = 1A		-5.2	-4.8	V	1, 2, 3
D				-25	25	mV	1
R _{Line}	Line Regulation	$-25V \le V_{IN} \le -7V$		-50	50	mV	2, 3
2		5 4 44 54 54		-75	75	mV	1
R _{Load}	Load Regulation	$5\text{mA} \le \text{I}_{\text{L}} \le 1.5\text{A}$		-100	100	mV	2, 3
l _{os}	Short Circuit Current	V _{IN} = -25V		0.4	3.0	А	1
RR	Ripple Rejection	f = 120Hz, I _L = 350mA, e _I = 1V _{RMS}		54		dB	4

LM120H-12 DC PARAMETERS

The following conditions apply, unless otherwise specified. $V_{IN} = -17V$, $I_L = 5mA$

Symbol	Parameter	Conditions	Notes	Min	Мах	Unit	Sub- groups
1	Quiessent Current	V _{IN} = -14V			4.0	mA	1, 2, 3
l _Q	Quiescent Current	$V_{IN} = -32V$			4.0	mA	1, 2, 3
		V _{IN} = -17V,			0.4	mA	1
A.I.	Quiescent Current Change	$5mA \le I_L \le 200mA$			0.5	mA	2, 3
Δl _Q	Quiescent Current Change				0.4	mA	1
		$-32V \le V_{IN} \le -14V$			0.5	mA	2, 3
C		$V_{IN} = -17V, 5mA \le I_L \le 200mA$		-25	25	mV	1
R _{Load}	Load Regulation			-50	50		2, 3
D	Line Degulation	201/51/5514		-10	10	mV	1
R _{Line}	Line Regulation	$-32V \le V_{IN} \le -14V$		-20	20	mV	2, 3
I _{OS}	Short Circuit Current	V _{IN} = -32V		0.1	1.5	А	1
		V _{IN} = -17V		-12.3	-11.7	V	1
		$V_{IN} = -32V$		-12.5	-11.5	V	1, 2, 3
V _{OUT}	Output Voltage	$V_{IN} = -32V, I_{L} = 100mA$		-12.5	-11.5	V	1, 2, 3
		V _{IN} = -14.5V		-12.5	-11.5	V	1, 2, 3
		V _{IN} = -14.5V, I _L = 200mA		-12.5	-11.5	V	1, 2, 3
RR	Ripple Rejection	f = 120Hz, I _L = 125mA, e _i = 1V _{RMS}		56		dB	4

SNVS368A - DECEMBER 2010 - REVISED APRIL 2013



www.ti.com

LM120K-12 DC PARAMETERS

The following conditions apply to all the following parameters, unless otherwise specified. $V_{IN} = -17V$, $I_L = 5mA$

Symbol	Parameter	Conditions	Notes	Min	Мах	Unit	Sub- groups
1	Quiescent Current	V _{IN} = -14V			4.0	mA	1, 2, 3
l _Q	Quiescent Current	V _{IN} = -32V			4.0	mA	1, 2, 3
		$1/-17/5m^{2}/5m^{2}$			0.4	mA	1
A 1	Quiescent Current Change	$V_{IN} = -17V$, 5mA $\leq I_L \leq 1A$			0.5	mA	2, 3
Δl _Q	Quiescent Current Change	221/(-1)/(0.4	mA	1
		$-32V \le V_{IN} \le -14V$			0.5	mA	2, 3
R _{Load}	Load Regulation	$V_{IN} = -17V, 5mA \le I_L \le 1A$		-80	80	mV	1, 2, 3
Р	Line Degulation			-10	10	mV	1
R _{Line}	Line Regulation	$-32V \le V_{IN} \le -14V$		-20	20	mV	2, 3
I _{OS}	Short Circuit Current	$V_{IN} = -32V$		0.4	3.0	А	1
		V _{IN} = -17V		-12.3	-11.7	V	1
		V _{IN} = -32V		-12.5	-11.5	V	1, 2, 3
V _{OUT}	Output Voltage	$V_{IN} = -32V, I_{L} = 1A$		-12.5	-11.5	V	1, 2, 3
		V _{IN} = -14.5V		-12.5	-11.5	V	1, 2, 3
		$V_{IN} = -14.5V, I_{L} = 1A$		-12.5	-11.5	V	1, 2, 3
RR	Ripple Rejection	f = 120Hz, I _L = 350mA, e _i = 1V _{RMS}		56		dB	4

LM120H-15 DC PARAMETERS

The following conditions apply to all the following parameters, unless otherwise specified. $V_{IN} = 20V$, $I_L = 5mA$

Symbol	Parameter	Conditions	Notes	Min	Max	Unit	Sub- groups
1	Outercoart Current	V _{IN} = -17V			4.0	mA	1, 2, 3
l _Q	Quiescent Current	V _{IN} = -35V			4.0	mA	1, 2, 3
		V _{IN} = -17V,			0.4	mA	1
A.I.	Quisseent Current Change	$5mA \le I_L \le 200mA$			0.5	mA	2, 3
Δl _Q	Quiescent Current Change	25 (1 - 5) (1 - 5 - 47) (1 - 5 - 47) (1 - 5 - 5 - 5) (1 - 5 - 5) (1 - 5 - 5) (1 - 5)			0.4	mA	1
		-35V ≤ V _{IN} ≤ -17V			0.5	mA	2, 3
D	Land Derivletion	V _{IN} = -20V,		-25	25	mV	1
R _{Load}	Load Regulation	$5mA \le I_L \le 200mA$		-50	50	mV	2, 3
D				-10	10	mV	1
R _{Line}	Line Regulation	$-35V \le V_{IN} \le -17V$		-20	20	mV	2, 3
l _{os}	Short Circuit Current	V _{IN} = -35V		0.1	1.5	А	1
		$V_{IN} = -20V$		-15.3	-14.7	V	1
		V _{IN} = -35V		-15.5	-14.5	V	1, 2, 3
V _{OUT}	Output Voltage	V _{IN} = -35V, I _L = 100mA		-15.5	-14.5	V	1, 2, 3
		V _{IN} = -17.5V		-15.5	-14.5	V	1, 2, 3
		V _{IN} = -17.5V, I _L = 200mA		-15.5	-14.5	V	1, 2, 3
RR	Ripple Rejection	f = 120Hz, I _L = 125mA, e _i = 1V _{RMS}		56		dB	4



LM120K-15 DC PARAMETERS

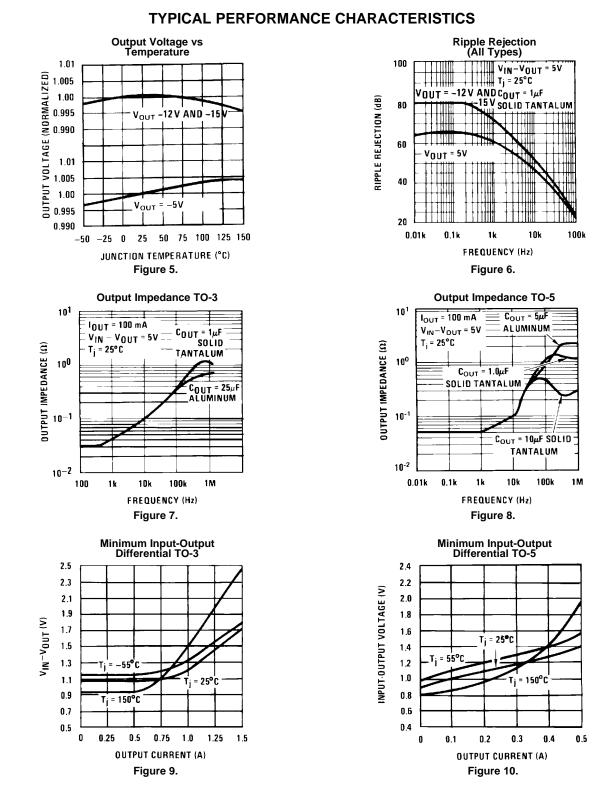
The following conditions apply, unless otherwise specified. $V_{IN} = 20V$, $I_{L} = 5mA$

Symbol	Parameter	Conditions	Notes	Min	Max	Unit	Sub- groups
	Quieseent Current	V _{IN} = -17V			4.0	mA	1, 2, 3
l _Q	Quiescent Current	V _{IN} = -35V			4.0	mA	1, 2, 3
		V _{IN} = -17V,			0.4	mA	1
		$5mA \le I_L \le 1A$			0.5	mA	2, 3
Δl _Q	Quiescent Current Change				0.4	mA	1
		$-35V \le V_{IN} \le -17V$			0.5	mA	2, 3
R _{Load}	Load Regulation	$V_{IN} = -20V,$ 5mA $\leq I_L \leq 1A$		-80	80	mV	1, 2, 3
D	Line Develotion			-10	10	mV	1
R _{Line}	Line Regulation	$-35V \le V_{IN} \le -17V$		-20	20	mV	2, 3
I _{OS}	Short Circuit Current	V _{IN} = -35V		0.4	3.0	А	1
		V _{IN} = -20V		-15.3	-14.7	V	1
		V _{IN} = -35V		-15.5	-14.5	V	1, 2, 3
V _{OUT}	Output Voltage	V _{IN} = -35V, I _L = 1A		-15.5	-14.5	V	1, 2, 3
		V _{IN} = -17.5V		-15.5	-14.5	V	1, 2, 3
		V _{IN} = -17.5V, I _L = 1.5A		-15.5	-14.5	V	1, 2, 3
ΔV _O / Δt	Long Term Stability		See ⁽¹⁾		150	mV	1
RR	Ripple Rejection	f = 120Hz, I _L = 350mA, e _I = 1V _{RMS}		56		dB	4

(1) Specified parameter, not tested



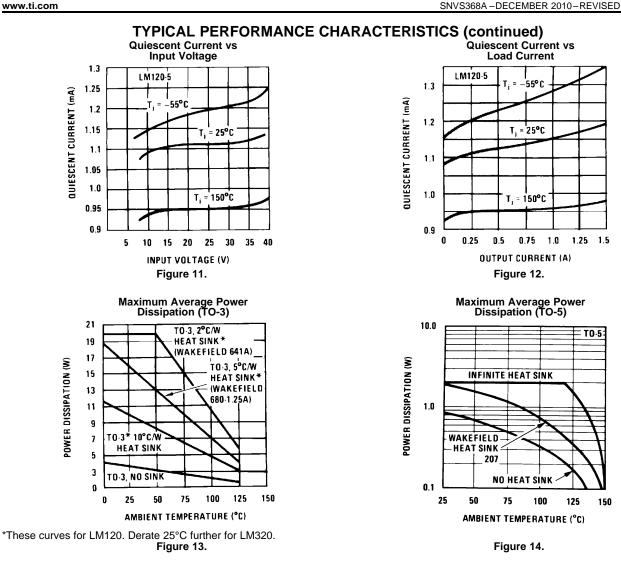
SNVS368A - DECEMBER 2010 - REVISED APRIL 2013

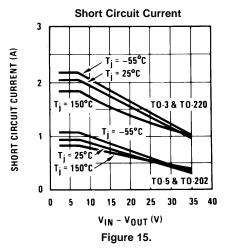


8





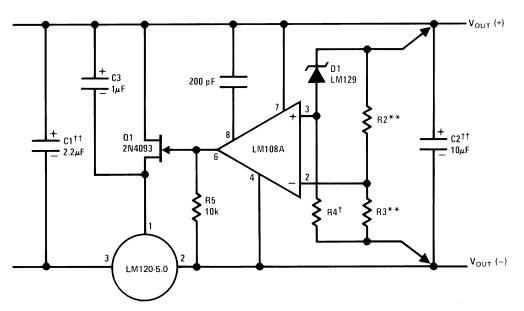




SNVS368A – DECEMBER 2010 – REVISED APRIL 2013

TEXAS INSTRUMENTS

www.ti.com



TYPICAL APPLICATIONS

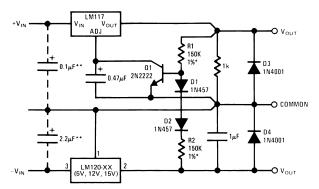
Lead and line regulation — 0.01% temperature stability — 0.2% †Determines Zener current.

. ††Solid tantalum.

An LM120-12 or LM120-15 may be used to permit higher input voltages, but the regulated output voltage must be at least -15V when using the LM120-12 and -18V for the LM120-15.

**Select resistors to set output voltage. 2 ppm/°C tracking suggested.





* Resistor tolerance of R1 and R2 determine matching of (+) and (-) inputs.

**Necessary only if raw supply capacitors are more than 3" from regulators

An LM3086N array may substitute for Q1, D1 and D2 for better stability and tracking. In the array diode transistors Q5 and Q4 (in parallel) make up D2; similarly, Q1 and Q2 become D1 and Q3 replaces the 2N2222.

Figure 17. Wide Range Tracking Regulator



SNVS368A - DECEMBER 2010 - REVISED APRIL 2013

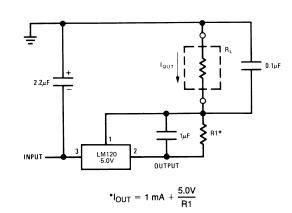
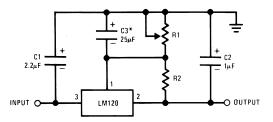


Figure 18. Current Source



*Optional. Improves transient response and ripple rejection.

$$V_{OUT} = V_{SET} \frac{R1 + R2}{R2}$$

 SELECT R2 AS FOLLOWS:

 LM120-5
 -300Ω

 LM120-12
 -750Ω

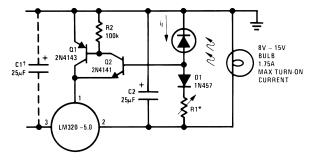
 LM120-15
 -1k

Figure 19. Variable Output

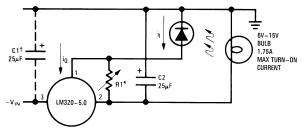
www.ti.com



SNVS368A-DECEMBER 2010-REVISED APRIL 2013

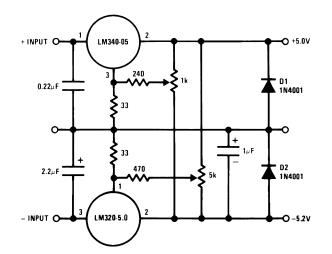


*Lamp brightness increases until $i_l = 5V/R1$ (i_l can be set as low as 1 µA). †Necessary only if raw supply filter capacitor is more than 2" from LM320MP.

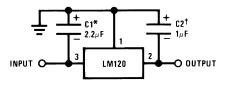


*Lamp brightness increases until $i_1 = i_Q$ (1 mA) + 5V/R1. +Necessary only if raw supply filter capacitor is more than 2" from LM320.









*Required if regulator is separated from filter capacitor by more than 3". For value given, capacitor must be solid tantalum. 25 µF aluminum electrolytic may be substituted.

†Required for stability. For value given, capacitor must be solid tantalum. 25 μF aluminum electrolytic may be substituted. Values given may be increased without limit.

For output capacitance in excess of 100 μ F, a high current diode from input to output (1N4001, etc.) will protect the regulator from momentary input shorts.

Figure 22. Fixed Regulator



SNVS368A - DECEMBER 2010 - REVISED APRIL 2013

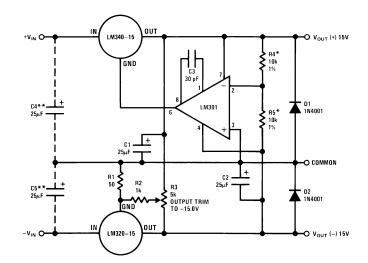


Figure 23. ±15V, 1 Amp Tracking Regulators

Table 3. Performance (Typical) ⁽¹⁾	Table 3.	Performance	(Typical) ⁽¹⁾
---	----------	-------------	--------------------------

Load Regulation at $\Delta I_L = 1A$	10 mV	1 mV
Output Ripple, $C_{IN} = 3000 \ \mu\text{F}$, $I_L = 1\text{A}$	100 µVRMS	100 μVRMS
Temperature Stability	+50 mV	+50 mV
Output Noise 10 Hz ≤ f ≤ 10 kHz	150 μVRMS	150 μVRMS

(1) *Resistor tolerance of R4 and R5 determine matching of (+) and (-) outputs. **Necessary only if raw supply filter capacitors are more than 2" from regulators.

LM120QML

SNVS368A - DECEMBER 2010 - REVISED APRIL 2013

www.ti.com

NSTRUMENTS

Texas

REVISION HISTORY

Date Released	Revision	Section	Changes
12/15/2010	A	New release to the corporate format	6 MDS datasheets were converted and merged into one datasheet compliant to corporate format. Drift endpoints removed since note used on 883 product. MDS MNLM120-5.0-K Rev OBL, MNLM120-5.0-H Rev 0BL, MNLM120-12-K Rev OBL, MNLM120-12-H Rev 0BL, MNLM120-15-K Rev OBL, & MNLM120-15- H Rev 0BL will be archived.
4/22/2013	A	All	Changed layout of National Data Sheet to TI format.



25-Oct-2016

PACKAGING INFORMATION

Orderable Device	Status	Package Type		Pins			Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Device Marking	Samples
	(1)		Drawing		Qty	(2)	(6)	(3)		(4/5)	
LM120H-12/883	ACTIVE	то	NDT	3	20	TBD	Call TI	Call TI	-55 to 150	LM120H-12/883 Q AC O LM120H-12/883 Q >T	Samples
LM120H-15/883	ACTIVE	то	NDT	3	20	TBD	Call TI	Call TI	-55 to 150	LM120H-15/883 Q AC O LM120H-15/883 Q >T	Samples
LM120H-5.0/883	ACTIVE	то	NDT	3	20	TBD	Call TI	Call TI	-55 to 150	LM120H-5.0/883 Q A CO LM120H-5.0/883 Q > T	Samples
LM120K-12/883	ACTIVE	то	К	2	50	TBD	Call TI	Call TI	-55 to 150	LM120K-12 /883 Q ACO /883 Q >T	Samples
LM120K-15/883	ACTIVE	то	К	2	50	TBD	Call TI	Call TI	-55 to 150	LM120K-15 /883 Q ACO /883 Q >T	Samples
LM120KG-12 MD8	ACTIVE	DIESALE	Y	0	221	Green (RoHS & no Sb/Br)	Call TI	Level-1-NA-UNLIM	-55 to 125		Samples
LM120KG-15 MD8	ACTIVE	DIESALE	Y	0	221	Green (RoHS & no Sb/Br)	Call TI	Level-1-NA-UNLIM	-55 to 125		Samples
LM120KG-5 MD8	ACTIVE	DIESALE	Y	0	221	Green (RoHS & no Sb/Br)	Call TI	Level-1-NA-UNLIM	-55 to 125		Samples

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes. Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.



PACKAGE OPTION ADDENDUM

25-Oct-2016

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

⁽³⁾ MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

⁽⁴⁾ There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

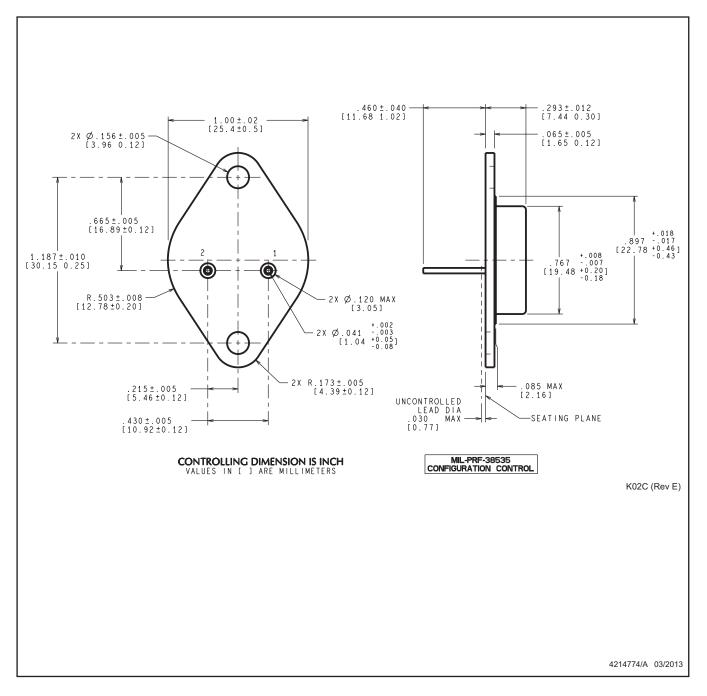
⁽⁵⁾ Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

(6) Lead/Ball Finish - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

Important Information and Disclaimer: The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

K0002C



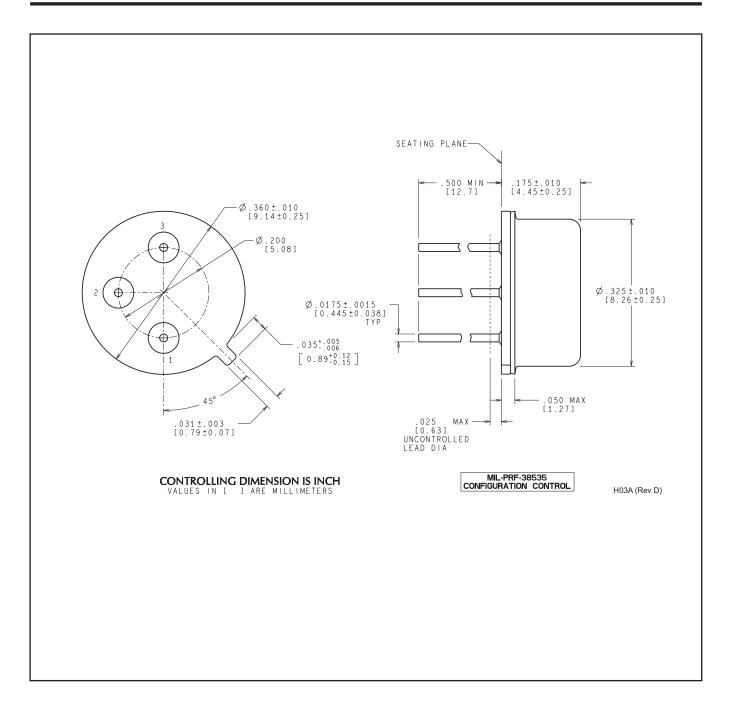
NOTES:

1. All linear dimensions are in millimeters. Dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.

2. This drawing is subject to change without notice.

3. Leads not to be bent greater than 15°







IMPORTANT NOTICE

Texas Instruments Incorporated and its subsidiaries (TI) reserve the right to make corrections, enhancements, improvements and other changes to its semiconductor products and services per JESD46, latest issue, and to discontinue any product or service per JESD48, latest issue. Buyers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All semiconductor products (also referred to herein as "components") are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its components to the specifications applicable at the time of sale, in accordance with the warranty in TI's terms and conditions of sale of semiconductor products. Testing and other quality control techniques are used to the extent TI deems necessary to support this warranty. Except where mandated by applicable law, testing of all parameters of each component is not necessarily performed.

TI assumes no liability for applications assistance or the design of Buyers' products. Buyers are responsible for their products and applications using TI components. To minimize the risks associated with Buyers' products and applications, Buyers should provide adequate design and operating safeguards.

TI does not warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right relating to any combination, machine, or process in which TI components or services are used. Information published by TI regarding third-party products or services does not constitute a license to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

Reproduction of significant portions of TI information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. TI is not responsible or liable for such altered documentation. Information of third parties may be subject to additional restrictions.

Resale of TI components or services with statements different from or beyond the parameters stated by TI for that component or service voids all express and any implied warranties for the associated TI component or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

Buyer acknowledges and agrees that it is solely responsible for compliance with all legal, regulatory and safety-related requirements concerning its products, and any use of TI components in its applications, notwithstanding any applications-related information or support that may be provided by TI. Buyer represents and agrees that it has all the necessary expertise to create and implement safeguards which anticipate dangerous consequences of failures, monitor failures and their consequences, lessen the likelihood of failures that might cause harm and take appropriate remedial actions. Buyer will fully indemnify TI and its representatives against any damages arising out of the use of any TI components in safety-critical applications.

In some cases, TI components may be promoted specifically to facilitate safety-related applications. With such components, TI's goal is to help enable customers to design and create their own end-product solutions that meet applicable functional safety standards and requirements. Nonetheless, such components are subject to these terms.

No TI components are authorized for use in FDA Class III (or similar life-critical medical equipment) unless authorized officers of the parties have executed a special agreement specifically governing such use.

Only those TI components which TI has specifically designated as military grade or "enhanced plastic" are designed and intended for use in military/aerospace applications or environments. Buyer acknowledges and agrees that any military or aerospace use of TI components which have *not* been so designated is solely at the Buyer's risk, and that Buyer is solely responsible for compliance with all legal and regulatory requirements in connection with such use.

TI has specifically designated certain components as meeting ISO/TS16949 requirements, mainly for automotive use. In any case of use of non-designated products, TI will not be responsible for any failure to meet ISO/TS16949.

Products		Applications				
Audio	www.ti.com/audio	Automotive and Transportation	www.ti.com/automotive			
Amplifiers	amplifier.ti.com	Communications and Telecom	www.ti.com/communications			
Data Converters	dataconverter.ti.com	Computers and Peripherals	www.ti.com/computers			
DLP® Products	www.dlp.com	Consumer Electronics	www.ti.com/consumer-apps			
DSP	dsp.ti.com	Energy and Lighting	www.ti.com/energy			
Clocks and Timers	www.ti.com/clocks	Industrial	www.ti.com/industrial			
Interface	interface.ti.com	Medical	www.ti.com/medical			
Logic	logic.ti.com	Security	www.ti.com/security			
Power Mgmt	power.ti.com	Space, Avionics and Defense	www.ti.com/space-avionics-defense			
Microcontrollers	microcontroller.ti.com	Video and Imaging	www.ti.com/video			
RFID	www.ti-rfid.com					
OMAP Applications Processors	www.ti.com/omap	TI E2E Community	e2e.ti.com			
Wireless Connectivity	www.ti.com/wirelessconnectivity					

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265 Copyright © 2016, Texas Instruments Incorporated