





Support &

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# LMV791-DIE Low Noise, CMOS Input Operational Amplifier with Shutdown

Technical

Documents

Sample &

Buv

#### 1 Features

- Low Noise, CMOS Input Operational Amplifier
- Unity Gain Stable Operational Amplifier
- Supply Current per Channel in Shutdown Mode
- Performance in Low Voltage and Low Noise
  System

## 2 Applications

Photodiode Amplifiers

Tools &

Software

- Active Filters and Buffers
- Low Noise Signal Processing
- Medical Instrumentation
- Sensor Interface Applications

## **3** Description

The LMV791-DIE low noise, CMOS input operational amplifier offers a low input voltage noise density. The LMV791-DIE is a unity gain stable operational amplifier and has gain bandwidth. The LMV791-DIE can operate from a single supply.

The LMV791-DIE provides optimal performance in low voltage and low noise systems. The LMV791-DIE has a built-in enable feature which can be used to optimize power dissipation in low power applications.

PRODUCT	PACKAGE DESIGNATOR	PACKAGE	ORDERABLE PART NUMBER	PACKAGE QUANTITY
	TD	Doro dia in woffle $\operatorname{pack}^{(2)}$	LMV791TDA1	360
LIVIV791		Dare die in wante pack	LMV791TDA2	10

#### Ordering Information<sup>(1)</sup>

For the most current package and ordering information, see the Package Option Addendum at the end of this document, or see the TI web site at www.ti.com.

(2) Processing is per the Texas Instruments commercial production baseline and is in compliance with the Texas Instruments Quality Control System in effect at the time of manufacture. Electrical screening consists of DC parametric and functional testing at room temperature only. Unless otherwise specified by Texas Instruments AC performance and performance over temperature is not warranted. Visual Inspection is performed in accordance with MIL-STD-883 Test Method 2010 Condition B at 75X minimum.



This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

## 4 Bare Die Information

DIE THICKNESS	BACKSIDE FINISH	BACKSIDE POTENTIAL	BOND PAD METALLIZATION COMPOSITION	BOND PAD THICKNESS	
10.5 mils.	Silicon with backgrind	Floating	AI(0.5%)Cu	700 nm	



#### **Bond Pad Coordinates in Microns**

DESCRIPTION	PAD NUMBER	X MIN	Y MIN	X MAX	Y MAX
OUTPUT	1	269.75	-276	344.8	-200.8
EN	2	-41.35	200.8	33.7	276
-IN	3	-345.45	200.8	-270.4	276
mountpad	4	-345.45	-54.85	-270.4	20.35
+IN	5	-345.45	-276	-270.4	-200.8
V+	6	271	200.8	346.05	276



27-Feb-2014

# PACKAGING INFORMATION

Orderable Device	Status	Package Type	Package	Pins	Package	Eco Plan	Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Device Marking	Samples
	(1)		Drawing		Qty	(2)	(6)	(3)		(4/5)	
LMV791TDA1	ACTIVE			0	360	TBD	Call TI	N / A for Pkg Type	25 Only		Samples
LMV791TDA2	ACTIVE			0	10	TBD	Call TI	N / A for Pkg Type	25 Only		Samples

<sup>(1)</sup> The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

**TBD:** The Pb-Free/Green conversion plan has not been defined.

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Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

<sup>(3)</sup> MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

<sup>(4)</sup> There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

<sup>(6)</sup> Lead/Ball Finish - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

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