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HMC346ALC3B

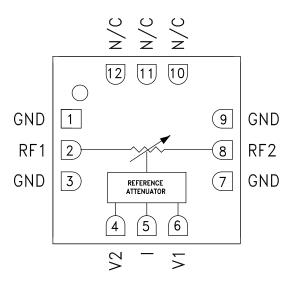
GaAs MMIC VOLTAGE-VARIABLE ATTENUATOR, DC - 18 GHz

Typical Applications

The HMC346ALC3B is ideal for:

- Test Instrumentation
- Fiber Optics & Broadband Telecom
- Microwave Radio & VSAT
- Military Radios, Radar, & ECM

Functional Diagram



Features

Wide Bandwidth: DC - 18 GHz Low Phase Shift vs. Attenuation 30 dB Attenuation Range Simplified Voltage Control RoHS Compliant 3 x 3 mm SMT Package

General Description

The HMC346ALC3B is an absorptive Voltage Variable Attenuator (VVA) in a leadless "Pb free" RoHS compliant SMT mount ceramic package operating from DC - 18 GHz. It features an on-chip reference attenuator for use with an external op-amp to provide simple single voltage attenuation control, 0 to -5V. The device is ideal in designs where an analog DC control signal must control RF signal levels over a 30 dB amplitude range. The HMC346ALC3B allows the use of surface mount manufacturing techniques.

Electrical Specifications, $T_A = +25^{\circ}$ C, 50 Ohm system

Parameter		Min	Typical	Max	Units
Insertion Loss	DC - 10 GHz DC - 14 GHz DC - 18 GHz		2.0 2.8 3.3	2.5 3.2 4.0	dB dB dB
Attenuation Range	DC - 12 GHz DC - 18 GHz	26 20	30 24		dB dB
Return Loss	DC - 18 GHz		10		dB
Input Power for 0.25 dB Compression (0.5 - 18 GHz)	Min. Atten: Atten. >2 dB:		+5 +4		dBm dBm
Input Third Order Intercept (0.5 - 18 GHz) (Two-tone Input Power = -8 dBm Each Tone)	Min. Atten: Atten. >2 dB:		+30 +8		dBm dBm
Switching Characteristics	tRISE, tFALL (10/90% RF): tON, tOFF (50% CTL to 10/90% RF):		8 16		ns ns

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HMC346ALC3B* PRODUCT PAGE QUICK LINKS

Last Content Update: 08/17/2017

View a parametric search of comparable parts.

EVALUATION KITS

HMC346ALC3B Evaluation Board

DOCUMENTATION

Data Sheet

• HMC346ALC3B: GaAs MMIC Voltage-Variable Attenuator, DC - 18 GHz Data Sheet

DESIGN RESOURCES

- HMC346ALC3B Material Declaration
- PCN-PDN Information
- Quality And Reliability
- Symbols and Footprints

DISCUSSIONS

View all HMC346ALC3B EngineerZone Discussions.

SAMPLE AND BUY

Visit the product page to see pricing options.

TECHNICAL SUPPORT

Submit a technical question or find your regional support number.

DOCUMENT FEEDBACK

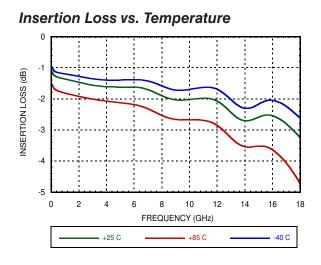
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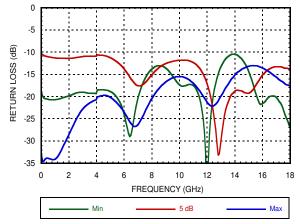
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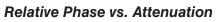
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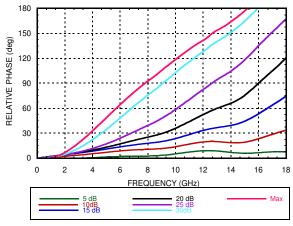
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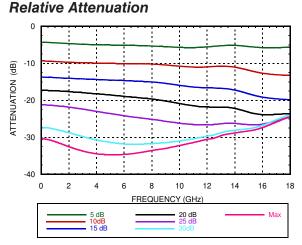
Return Loss vs. Attenuation



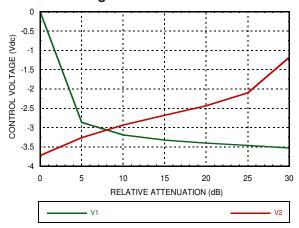




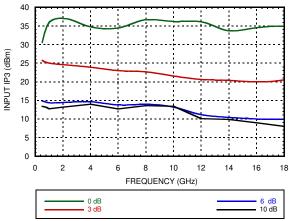
*Two-tone input power = -8 dBm each tone, 1 MHz spacing.



Relative Attenuation vs. Control Voltage @ 10 GHz





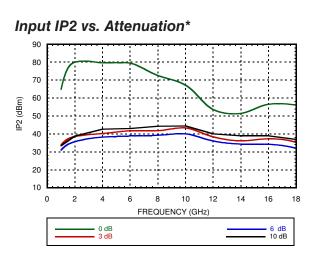


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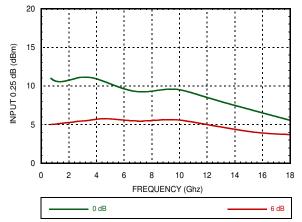
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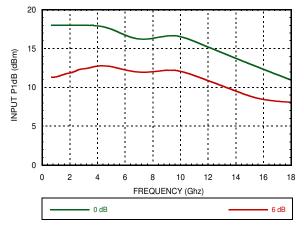
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0.25 dB Compression vs. Attenuation



Second Harmonic vs. Attenuation, Pin = -8 dBm 90 80 SECOND HARMONIC (dBc) 70 60 50 40 30 20 10 0 2 4 6 8 10 12 14 16 18 FREQUENCY (GHz) 0 dB 3 dB 6 dB 10 dB

1 dB Compression vs. Attenuation



*Two-tone input power = -8 dBm each tone, 1 MHz spacing.

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Absolute Maximum Ratings

RF Input Power	+18 dBm	
DC Voltage on I Pin	+/- 0.8V	
Control Voltage Range	0.3 to -6V	
Storage Temperature	-65 to +150 °C	
Operating Temperature	-40 to +85 °C	
Junction Temperature	+175 °C	
Junction to Case Thermal Resistance	10 °C/W	
ESD Sensitivity (HBM)	Class 1A	

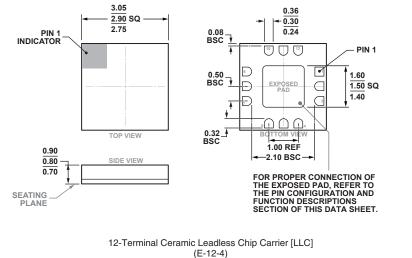


ELECTROSTATIC SENSITIVE DEVICE OBSERVE HANDLING PRECAUTIONS

State	Bias Condition
V1	-5 to 0V @ 9 mA Typical.
V2	-5 to 0V @ 9 mA Typical.

13-02-2017-A

Outline Drawing



(E-12-4) Dimensions shown in millimeters

Package Information

Part Number	Package Body Material	Lead Finish	MSL Rating	Package Marking ^[2]
HMC346ALC3B	Alumina, White	Gold over Nickel	MSL3 ^[1]	346A XXXX

[[1] Max peak reflow temperature of 260 °C

[2] 4-Digit lot number XXXX

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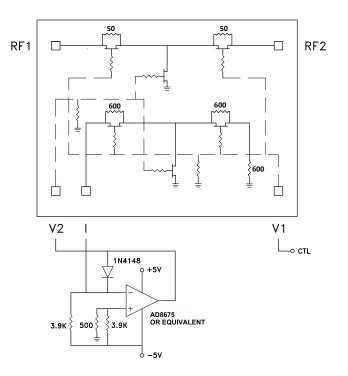
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Pin Descriptions

Pin Number	Function	Description	Interface Schematic
1, 3, 7, 9	GND	Package bottom has exposed metal paddle that must also be connected to PCB RF ground.	
2, 8	RF1 RF2	This pin is DC coupled and matched to 50 Ohm. Blocking capacitors are required if the RF line potential is not equal to 0V.	
4, 6	V2, V1	Control input (master).	500
5	I	Control input (slave).	600
10, 11, 12	N/C	This pin may be connected to PCB RF/DC ground. Performance will not be affected.	

Single-Line Control Driver



External op-amp control circuit maintains impedance match while attenuation is varied. Input control ranges from 0 Volts (min. attenuation) to -5.0 Volts (max. attenuation.)

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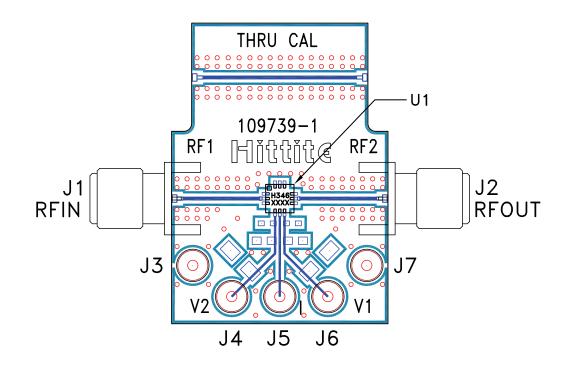


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Evaluation PCB



List of Materials for Evaluation EV1HMC346ALC3B^[1]

Item	Description
J1 - J2	PCB Mount SMA RF Connector
J3 - J7	DC Pin
U1	HMC346ALC3B VVA
PCB [2]	109739-1 Evaluation PCB

Reference this number when ordering complete evaluation PCB
Circuit Board Material: Rogers 4350

The circuit board used in the application should be generated with proper RF circuit design techniques. Signal lines at the RF ports should be 50 Ohm impedance and the package ground leads and package bottom should be connected directly to the PCB RF ground plane, similar to that shown above. The evaluation circuit board shown above is available from Analog Devices Inc. upon request.