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August 2009

# FSA6157 Low- $R_{ON}$ SPDT (0.8 $\Omega$ ) Negative-Swing Audio or Video Switch

#### **Features**

- 0.8Ω Typical On Resistance (R<sub>ON</sub>) for +2.7V Supply
- 0.45Ω Maximum R<sub>ON</sub> Flatness for +2.7V Supply
- -3db Bandwidth: > 50MHz
- Low I<sub>CCT</sub> Current Over an Expanded Control Input Range
- Packaged in Pb-free 6-Lead MicroPak™ (1.0 x 1.4mm)
- Power-Off Protection on All I/O Ports
- Broad V<sub>CC</sub> Operating Range: 1.65 to 4.3V
- HBM JEDEC: JESD22-A114
- I/O to GND: 12kVPower to GND: 16kV

# **Applications**

- Cell Phone, PDA, Digital Camera, and Notebook
- LCD Monitor, TV, and Set-top Box

# Description

The FSA6157 is a high-performance, Single Pole Double Throw (SPDT) analog switch that features a low  $R_{\text{ON}}$  of  $0.8\Omega$  (typical) at 2.7V supply. The FSA6157 operates over a wide  $V_{\text{CC}}$  range of 1.65V to 4.3V and is designed for break-before-make operation. The select input is TTL-level compatible.

The FSA6157 features very low quiescent current even when the control voltage is lower than the  $V_{\rm CC}$  supply. This feature suits mobile handset applications by allowing direct interface with baseband processor general-purpose I/Os with minimal battery consumption.

#### **IMPORTANT NOTE:**

For additional performance information, please contact <a href="mailto:analogswitch@fairchildsemi.com">analogswitch@fairchildsemi.com</a>.

# **Ordering Information**

Part Number	Top Mark	Eco Status	Package Description
FSA6157L6X	GT	Green	6-Lead, MicroPak™, 1.0mm wide, JEDEC MO-255

Por Fairchild's definition of Eco Status, please visit: <a href="http://www.fairchildsemi.com/company/green/rohs\_green.html">http://www.fairchildsemi.com/company/green/rohs\_green.html</a>.

# **Analog Symbol**

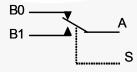


Figure 1. FSA6157

# **Pin Assignments**

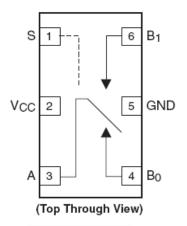


Figure 2. Pin Assignments for 6-Lead MicroPak™

# **Pin Descriptions**

Name	Description
A, B <sub>0</sub> , B <sub>1</sub>	Data Ports
S	Switch Select Pin

# **Truth Table**

Control Input, S	Function
LOW	B0 connected to A
HIGH	B1 connected to A

# **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter	Min.	Max.	Units			
V <sub>CC</sub>	Supply Voltage			-0.5	4.6	V	
V <sub>SW</sub>	Switch I/O Voltage <sup>(1)</sup>	В0,	B1, A Pins	V <sub>CC</sub> – 5.5V	4.6	V	
V <sub>SW-SW</sub>	Switch I/O to Switch I/O Voltage Delta (Off State) <sup>(1)</sup>	В0,	B1, A Pins		5.5	٧	
V <sub>CNTRL</sub>	Control Input Voltage <sup>(1)</sup>		-0.5	4.6	V		
I <sub>IK</sub>	Input Clamp Diode Current				-50	mA	
I <sub>SW</sub>	Switch I/O Current (Continuous)				350	mA	
I <sub>SWPEAK</sub>	Peak Switch Current (Pulsed at 1ms Duration, <10	)% Dı	uty Cycle)		500	mA	
$T_{STG}$	Storage Temperature Range			-65	+150	°C	
TJ	Maximum Junction Temperature				+150	°C	
TL	Lead Temperature (Soldering, 10 seconds)			V.	+260	°C	
		I/O	to GND		12		
ESD	Human Body Model (JEDEC: JESD22-A114)		wer to GND		16	kV	
LSD		All (	Other Pins		8	ΝV	
	Charge Device Model (JEDEC: JESD22-C101)				2		

#### Note

1. Input and output negative ratings may be exceeded if input and output diode current ratings are observed.

# **Recommended Operating Conditions**

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to absolute maximum ratings.

Symbol	Parameter	Min.	Max.	Units
V <sub>CC</sub>	Supply Voltage	1.65	4.3	V
V <sub>CNTRL</sub> <sup>(2)</sup>	Control Input Voltage – Select Pin	0	V <sub>CC</sub>	V
$V_{SW}$	Switch I/O Voltage	V <sub>CC</sub> – 4.3V	4.3	V
V <sub>SW-SW</sub>	Switch I/O Voltage to Switch I/O Voltage (Off-State)	7	4.6	V
T <sub>A</sub>	Operating Temperature	-40	85	°C

#### Note:

2. Input and output negative ratings may be exceeded if input and output diode current ratings are observed.

#### **DC Electrical Characteristics**

All typical values are at 25°C unless otherwise specified.

Symbol	Parameter	Conditions	V <sub>cc</sub> (V)	-	Γ <sub>A</sub> =+25°	С	T <sub>A</sub> =-40 to +85°C		Unit
				Min.	Тур.	Max.	Min.	Max.	
	Analog Signal Range			V <sub>CC</sub> - 4.3V		V <sub>cc</sub>			V
V <sub>IK</sub>	Clamp Diode Voltage		3.00					-1.2	V
			3.60 to 4.30				1.4		
$V_{IH}$	Input Voltage High		2.70 to 3.60				1.3		V
VIН	input voitage riigii		2.30 to 2.70				1.3		
			1.65 to 1.95				0.9		
			3.60 to 4.30					0.7	
V <sub>II</sub>	Input Voltage Low		2.70 to 3.60					0.4	V
V IL	input voitage Low		2.30 to 2.70					0.4	
			1.65 to 1.95					0.4	
I <sub>IN</sub>	Control Input Leakage (S)	V <sub>IN=</sub> 0 to V <sub>CC</sub>	4.30				-1	1	μA
I <sub>NO(0FF)</sub> , I <sub>NC(0FF)</sub>	Off Leakage Current of Port B0 and B1	$\begin{array}{l} \text{A=0.5V,V}_{\text{CC}} - 0.5\text{V} \\ \text{B0 or B1=V}_{\text{CC}} - 0.5\text{V}, \\ \text{0.5V, or Floating;} \\ \text{Figure 4} \end{array}$	1.95 to 4.30	-100		100	-500	500	nA
I <sub>A(ON)</sub>	On Leakage Current of Port A	A=0.5V, $V_{CC}$ – 0.5V B0 or B1= $V_{CC}$ -0.5V, 0.5V, or Floating; Figure 5	4.30	-100		100	-250	250	nA
I <sub>OFF</sub>	Power-Off Leakage Current (All I/O Ports)	V <sub>A,BN</sub> =0.3V to 4.3V or Floating,	0V or Floating				-40	40	μA
		I <sub>ON=</sub> 100mA, B0 or B1= 0, 0.7V, 3.6V, 4.3V; Figure 3	4.30		0.4			0.8	
Ron	Switch On Resistance <sup>(3,6)</sup>	I <sub>ON=</sub> 100mA, B0 or B1= 0, 0.7V, 2.0V, 2.7V; Figure 3	2.70		0.8			1.0	Ω
K <sub>ON</sub>	Switch On Resistance	I <sub>ON=</sub> 100mA, B0 or B1= 0, 0.7V, 1.6V, 2.3V; Figure 3	2.30					1.5	1 12
		I <sub>ON=</sub> 100mA, B0 or B1= 0, 0.7V, 1.65V; Figure 3	1.65		1.3			2.0	
$\Delta R_{ON}$	On Resistance Matching Between Channels <sup>(4)</sup>	I <sub>ON</sub> =100mA, B0 or B1=0.7V	2.30 to 4.30		0.050			0.130	Ω
R <sub>FLAT(ON)</sub>	On Resistance Flatness <sup>(5)</sup>	I <sub>OUT</sub> =100mA, B0 or B1=0V to V <sub>CC</sub>	2.70 to 4.30					0.45	Ω
Icc	Quiescent Supply Current	V <sub>SW=</sub> 0 or V <sub>CC</sub> , I <sub>OUT</sub> =0	4.30	-100		100	-500	500	nA
I <sub>CCT</sub>	Increase in I <sub>cc</sub> per Input	Input at 2.6V	4.30		3.0			10.0	μA
•001	Sado iii igg poi iiipat	Input at 1.8V	1.00		7.0			13.0	μ, ,

#### Notes:

- On resistance is determined by the voltage drop between A and B pins at the indicated current through the switch.
- $\Delta$  R<sub>ON</sub>=R<sub>ON max</sub> R<sub>ON min</sub> measured at identical Vcc, temperature, and voltage. Flatness is defined as the difference between the maximum and minimum value of on resistance (R<sub>ON</sub>) over the specified range of conditions.
- Guaranteed by characterization, not production tested.

## **AC Electrical Characteristics**

All typical value are for  $V_{CC}$ =1.8V, 2.5V, 3.3V, and 4.0V at 25°C unless otherwise specified.

Symbol	Parameter	Conditions	V <sub>CC</sub> (V)		T <sub>A</sub> =+25%	C		T <sub>A</sub> =-40 to +85°C		Figure
•				Min.	Тур.	Max.	Min.	Max.		
			3.60 to 4.30	5		65	3	70		
		B0 or B1=1.0V,	2.70 to 3.60	5		65	3	70		l
t <sub>ON</sub>	Turn-On Time	$R_L=50\Omega$ , $C_L=35pF$	2.30 to 2.70	5		70	3	80	ns	
			1.65 to 1.95	10		100	10	150		Figure 6
			3.60 to 4.30	1		35	1	45		Figure 7
		B0 or B1=1.0V,	2.70 to 3.60	1		35	1	45		
t <sub>OFF</sub>	Turn-Off Time	$R_L=50\Omega$ , $C_L=35pF$	2.30 to 2.70	2		45	2	50	ns	
			1.65 to 1.95	2		70	2	95		
			3.60 to 4.30				2			
	Break-Before-	B0 or B1=1.0V,	2.70 to 3.60	2.70 to 3.60			2			
t <sub>BBM</sub>	Make Time	$R_L=50\Omega$ , $C_L=35pF$	2.30 to 2.70				2	, n	ns	Figure 8
			1.65 to 1.95				2			
			3.60 to 4.30		25					
Q	Charge	C <sub>L</sub> =1.0nF, V <sub>S</sub> =0V,	2.70 to 3.60		15				рC	Figure
Q	Injection	R <sub>S</sub> =0Ω	Ω 2.30 to 2.70 12			рС	12			
			1.65 to 1.95		5					
OIRR	Off Isolation	f=20kHz, $R_L$ =50 $\Omega$ , $C_L$ =0pF	1.65 to 4.30		-60				dB	Figure 10
Xtalk	Crosstalk	f=20kHz, $R_L$ =50Ω, $C_L$ =0pF	1.65 to 4.30		-60				dB	Figure 11
BW	-3db Bandwidth	$R_L$ =50 $\Omega$ , $C_L$ =0pF	1.65 to 4.30		>50		1		MHz	Figure 9
THD	Total Harmonic Distortion	f=20Hz to 20kHz, R <sub>L</sub> =32 $\Omega$ , V <sub>IN</sub> =2V <sub>PP</sub>	1.65 to 4.30		0.1				%	Figure 15
SNR	Signal to Noise Ratio	$ \begin{array}{l} \text{f=1kHz, } R_\text{L}\text{=}32\Omega, \\ V_\text{IN}\text{=}0\text{dBmw,} \\ V_\text{BIAS}\text{=}0\text{V} \end{array} $	4.30		-70				dB	

# Capacitance

Symphol	Dovometer	Conditions	V 00	T <sub>A</sub> =+25°C			l lmi4	Ciaura
Symbol	Parameter	Conditions	V <sub>CC</sub> (V)	Min.	Тур.	Max.	Unit	Figure
C <sub>IN</sub>	Control Pin Input Capacitance	f=1MHz	0		3		pF	Figure 13
C <sub>OFF</sub>	B Port Off Capacitance	f=1MHz	3.30			30	pF	Figure 13
C <sub>ON</sub>	A Port On Capacitance	f=1MHz	3.30			150	pF	Figure 14

# **Test Diagrams**

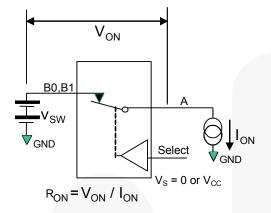


Figure 3. On Resistance

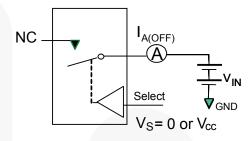


Figure 4. Off Leakage (Ports Tested Separately)

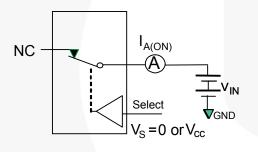


Figure 5. On Leakage

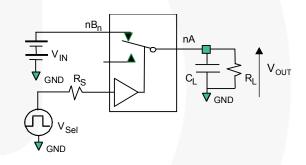


Figure 6. Test Circuit Load

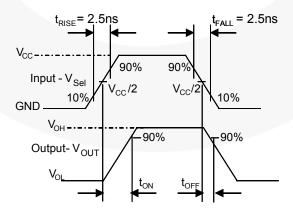


Figure 7. Turn-On / Turn-Off Waveforms

# Test Diagrams (Continued)

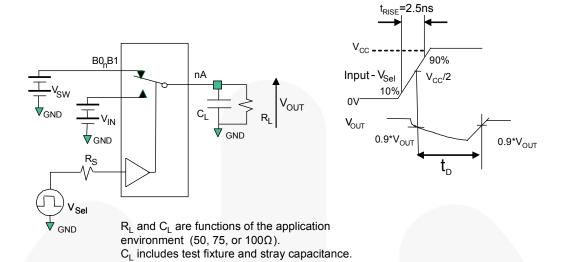


Figure 8. Break-Before-Make Interval Timing

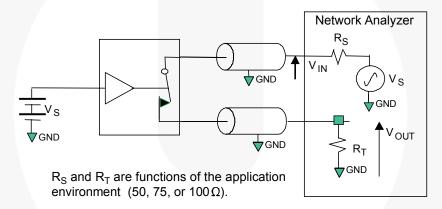


Figure 9. Bandwidth

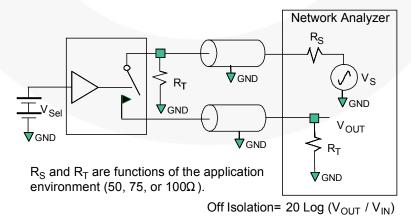
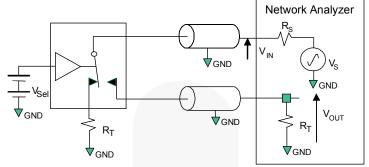


Figure 10. Channel Off Isolation

## **Test Diagrams** (Continued)



 $R_S$  and  $R_T$  are functions of the application environment (50, 75, or 100 $\Omega$ ).

Crosstalk = 20 Log  $(V_{OUT} / V_{IN})$ 

Figure 11. Adjacent Channel Crosstalk

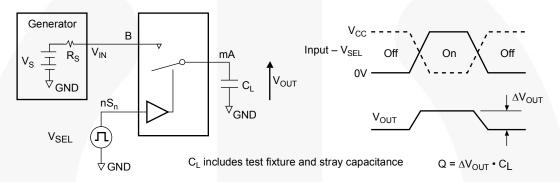


Figure 12. Charge Injection Test

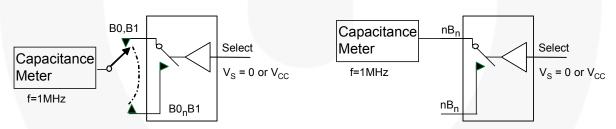


Figure 13. Channel Off Capacitance

Figure 14. Channel On Capacitance

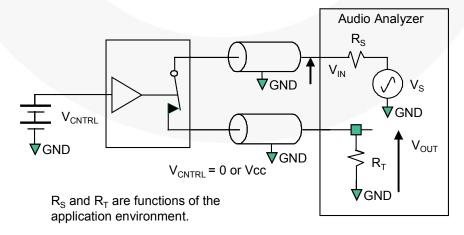
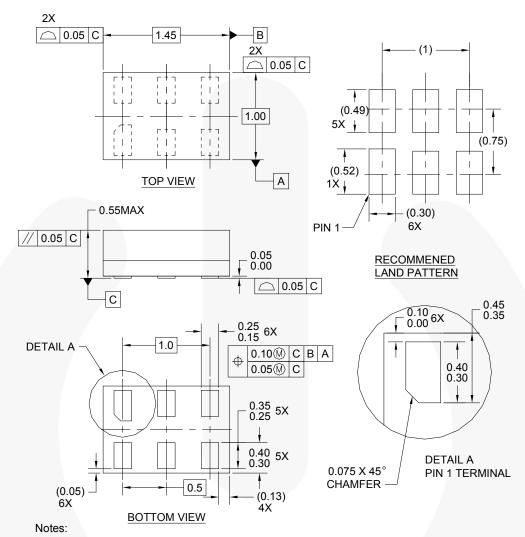


Figure 15. Total Harmonic Distortion

# **Physical Dimensions**



- 1. CONFORMS TO JEDEC STANDARD M0-252 VARIATION UAAD
- 2. DIMENSIONS ARE IN MILLIMETERS
- 3. DRAWING CONFORMS TO ASME Y14.5M-1994

MAC06AREVC

#### Figure 16. 6-Lead MicroPak™, 1.0mm Wide

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Datasheet Identification	Product Status	Definition
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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
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