




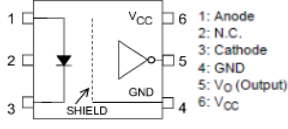
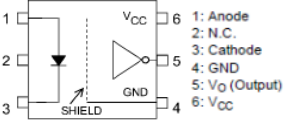
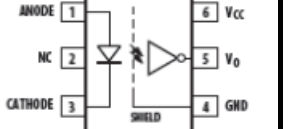
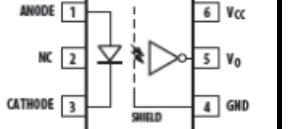
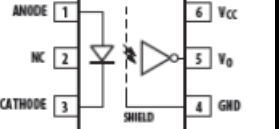


20M Open-Collector (SDIP6)

Spec.	 TOSHIBA Leading Innovation >>>	 TOSHIBA Leading Innovation >>>	 AVAGO TECHNOLOGIES	 AVAGO TECHNOLOGIES	 AVAGO TECHNOLOGIES
	TLP2768	TLP2768F	ACPL-P611	ACPL-W611	ACPL-W60L
PKG	SDIP6	SDIP6 (Wide type)	SDIP6	SDIP6 (Wide type)	SDIP6 (Wide type)
Schematic	 <p>1: Anode 2: N.C. 3: Cathode 4: GND 5: V_O (Output) 6: V_{CC}</p>	 <p>1: Anode 2: N.C. 3: Cathode 4: GND 5: V_O (Output) 6: V_{CC}</p>	 <p>ANODE 1 NC 2 CATHODE 3 SHIELD 6 V_{CC} 5 V_O 4 GND</p>	 <p>ANODE 1 NC 2 CATHODE 3 SHIELD 6 V_{CC} 5 V_O 4 GND</p>	 <p>ANODE 1 NC 2 CATHODE 3 SHIELD 6 V_{CC} 5 V_O 4 GND</p>
Output Stage	Open-collector	Open-collector	Open-collector	Open-collector	Open-collector
Supply Voltage [V _{CC}] Recommend Operating conditions	2.7 to 3.6 V / 4.5 to 5.5 V	2.7 to 3.6 V / 4.5 to 5.5 V	4.5 to 5.5 V	4.5 to 5.5 V	2.7 to 3.6 V / 4.5 to 5.5 V
Operating Temperature Range Topr	-40~125°C	-40~125°C	-40~85°C	-40~85°C	-40~85°C
Input threshold current IFH (max)	5 mA	5 mA	5 mA	5 mA	5 mA
Logic Low/High Supply Current I _{CC} L/I _{CC} H (max)	ICCL / ICCH: 4 mA	ICCL / ICCH: 4 mA	ICCL : 10.5 mA ICCH : 7.5 mA	ICCL : 10.5 mA ICCH : 7.5 mA	ICCL : 13 mA ICCH : 10 mA
Propagation Delay Time tpLH/tpHL (max.)	60 ns	60 ns	100 ns	100 ns	100 ns
Pulse Width Distortion tpHL-tpLH (max)	35 ns	35 ns	35 ns	35 ns	35 ns
Propagation Delay Skew tpsk (max.)	40 ns	40 ns	40 ns	40 ns	40 ns
Common Mode Transient Immunity CMH/CML (min.) (Ta=25°C)	20 kV/ms	20 kV/ms	10 kV/ms	10 kV/ms	15 kV/ms
Isolation Voltage BV _s (min) (Ta=25°C)	5000 Vrms	5000 Vrms	3750 Vrms	3750 Vrms	3750 Vrms