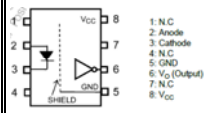
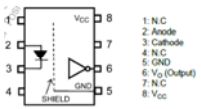
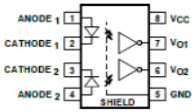
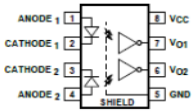
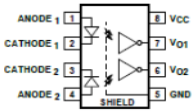
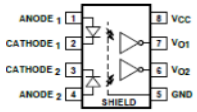
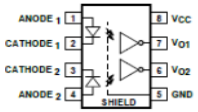
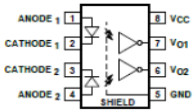
20M Open-Collector（SO8(Dual)）

Spec.	TOSHIBA Leading Innovation >>> TLP2118E	TOSHIBA Leading Innovation >>> TLP2168	AVAGO TECHNOLOGIES HCPL-0630	AVAGO TECHNOLOGIES HCPL-0631	AVAGO TECHNOLOGIES HCPL-0661	AVAGO TECHNOLOGIES HCPL-063A	AVAGO TECHNOLOGIES HCPL-063N	AVAGO TECHNOLOGIES HCPL-063L
PKG	SO8(Dual)	SO8(Dual)	SO8(Dual)	SO8(Dual)	SO8(Dual)	SO8(Dual)	SO8(Dual)	SO8(Dual)
Schematic								
Output Stage	Open-collector	Open-collector	Open-collector	Open-collector	Open-collector	Open-collector	Open-collector	Open-collector
Supply Voltage [Vcc] Recommend Operating conditions	4.5 to 5.5 V	**2.7 to 3.6 V / 4.5 to 5.5 V**	4.5 to 5.5 V	4.5 to 5.5 V	4.5 to 5.5 V	4.5 to 5.5 V	4.5 to 5.5 V	**2.7 to 3.6 V / 4.5 to 5.5 V**
Operating Temperature Range Topr	**-40~100°C**	**-40~125°C**	-40~85°C	-40~85°C	-40~85°C	-40~85°C	-40~85°C	-40~85°C
Input threshold current IFH (max)	5 mA	5 mA	5 mA	5 mA	5 mA	3 mA	3 mA	5 mA
Logic Low/High Supply Current IccL/ICCH (max)	**ICCL / ICCH:10 mA**	**ICCL / ICCH:8 mA**	ICCL / ICCH:21 mA / 15 mA	ICCL / ICCH:21 mA / 15 mA	ICCL / ICCH:21 mA / 15 mA	ICCL / ICCH:21 mA / 15 mA	ICCL / ICCH:21 mA / 15 mA	ICCL / ICCH:21 mA / 15 mA
Propagation Delay Time tpLH/tpHL (max.)	**75 ns**	**60 ns**	100 ns	100 ns	100 ns	100 ns	100 ns	100 ns
Pulse Width Distortion |tpHL-tpLH| (max)	35 ns	35 ns	35 ns	35 ns	35 ns	45 ns	45 ns	35 ns
Propagation Delay Skew tpsk (max.)	50 ns	40 ns	40 ns	40 ns	40 ns	60 ns	60 ns	40 ns
Common Mode Transient Immunity CMH/CML (min.) (Ta=25°C)	**15 kV/µs**	**15 kV/ms**	5 kV/ms	10 kV/ms	**15 kV/ms**	1 kV/ms	**15 kV/ms**	**15 kV/ms**
Isolation Voltage BVs (min) (Ta=25°C)	2500 Vrms	2500 Vrms	3750 Vrms	3750 Vrms	3750 Vrms	3750 Vrms	3750 Vrms	3750 Vrms