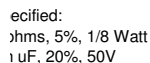




Contents	
1	OPENING INTERFACE
5	I/O Headers and Power Supply

Revisions & Change Log			
Rev	Description	Date	Approved
X1	Initial Draft	Feb/12/13	Alistair
X2	* MCU Pin assignments changed for Pins 57-64	Feb/18/13	Alistair
X3	* J1 & J4 Signal mapping changed * RGB led connections changed	Feb/19/13	Alistair
X4	* SPI FLASH MEMORY deleted * CAD Notes added	Feb/19/13	Alistair
A	* BACK ANNOTATION DONE * DNP Assy option removed for some jumpers & tablutd in Page2 * A085 RELEASE	Mar/07/13	Alistair
A1	* Some jumpers are commented as DNP for production build as tabulated in page 2	Apr/16/13	V J Ramon
	* R36 Populated	Apr/26/13	V J Ramon
	* C1 Made DNP	May/03/13	V J Ramon
	* A085 Release		
B	* Changing C13 from 1uF to 0.1uF to fix yield issue	Jun/20/13	V J Ramon

FREEDOM KL26Z

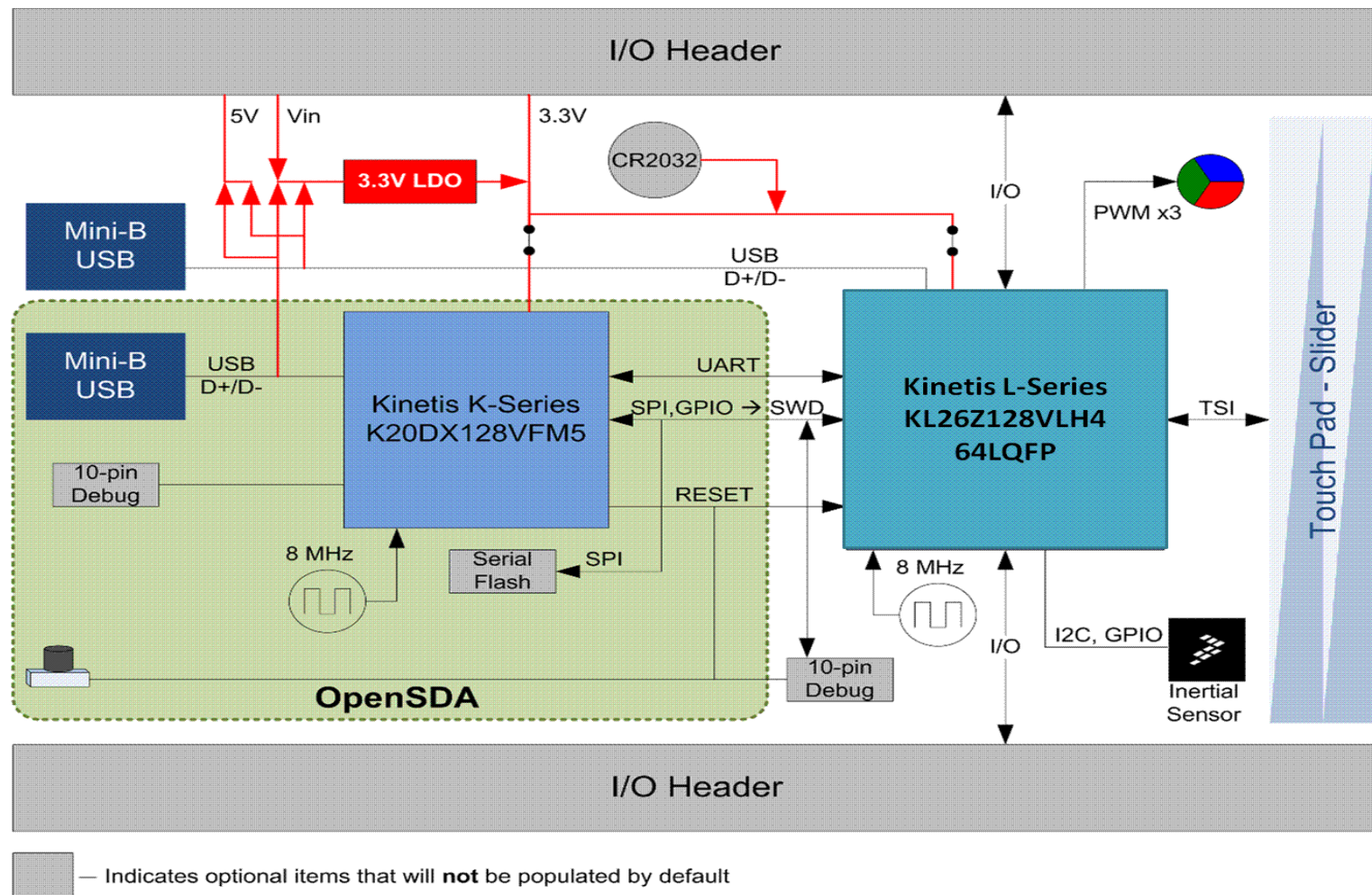


2. Interrupted lines coded with the same letter or letter combinations are electrically connected.
3. Device type number is for reference only. The number varies with the manufacturer.

4. Special signal usage:
 _B Denotes - Active-Low Signal
 <> or [] Denotes - Vectored Signals

5. Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.

S.No	REF DES	DESCRIPTION	PROTOTYPE BUILD (Rev A)	PRODUCTION BUILD (Rev A1)
1.	J1,J3	CON 2X8 SKT TH 100MIL SP 335H AU 104L	LOAD	DNP
2.	J2	CON 2X10 SKT TH 100MIL CTR 340H AU 104L	LOAD	DNP
3.	J8,J5,J15,J13,J14,J9	HDR 1X2 TH 100MIL SP 339H AU 98L	LOAD	DNP
4.	J4	CON 2X6 SKT TH 100MIL CTR 340H AU 104L	LOAD	DNP
5.	J7,J12	HDR 2X5 TH 50MIL CTR 167H AU 91L	LOAD	DNP
6.	J11	HDR 1X3 TH 100MIL SP 330H AU 98L	LOAD	DNP
7.	BT1	HOLDER BATTERY 20MM DIA TH	LOAD	DNP



ICAP Classification: FCP: FIUO: PUBI: X

Drawing Title:

FRDM-KL26Z

Page Title

BLOCK DIAGRAM

Size

Document Number

C

1000

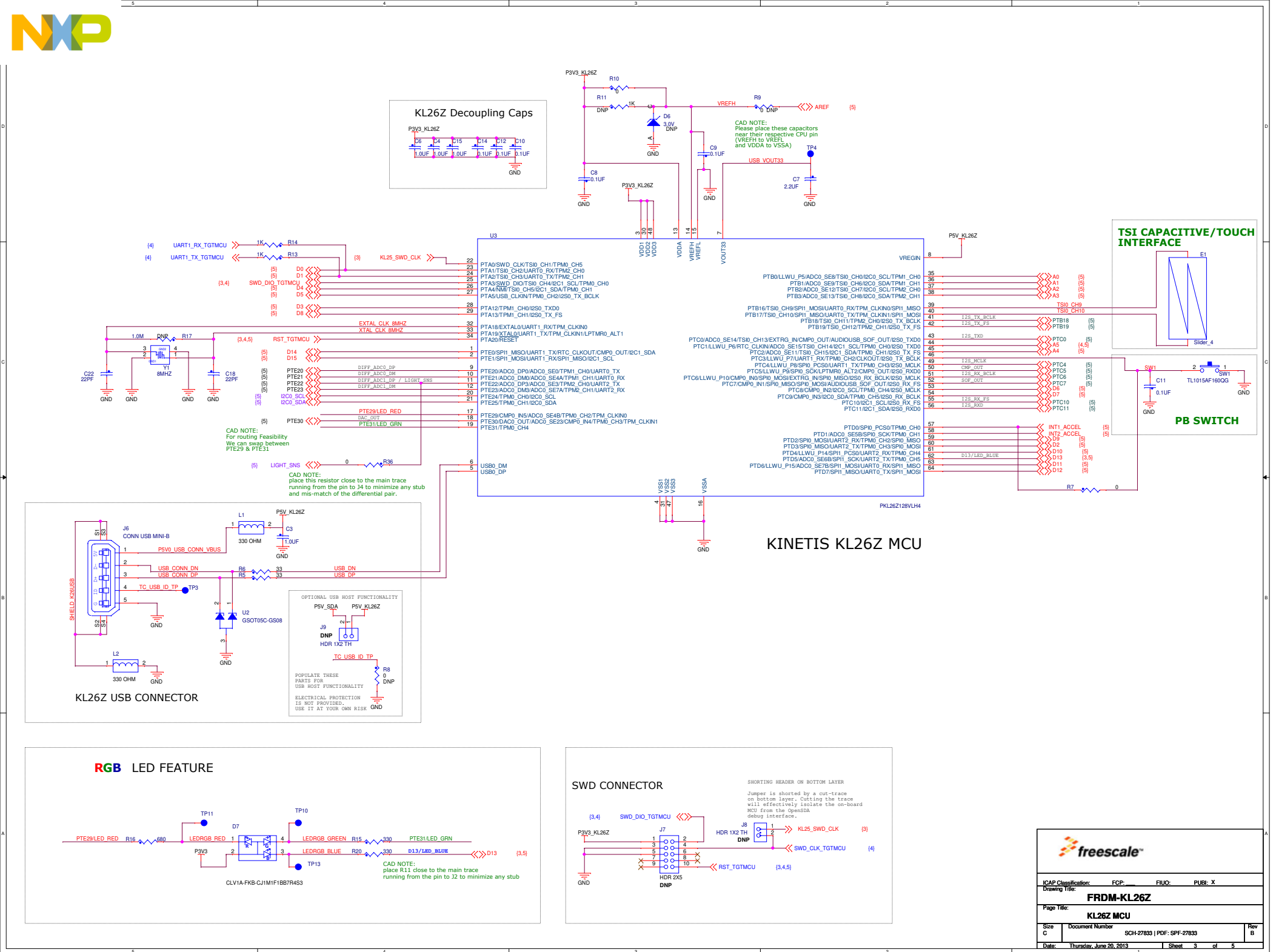
SCH-27833 | PDF: SPF-27833

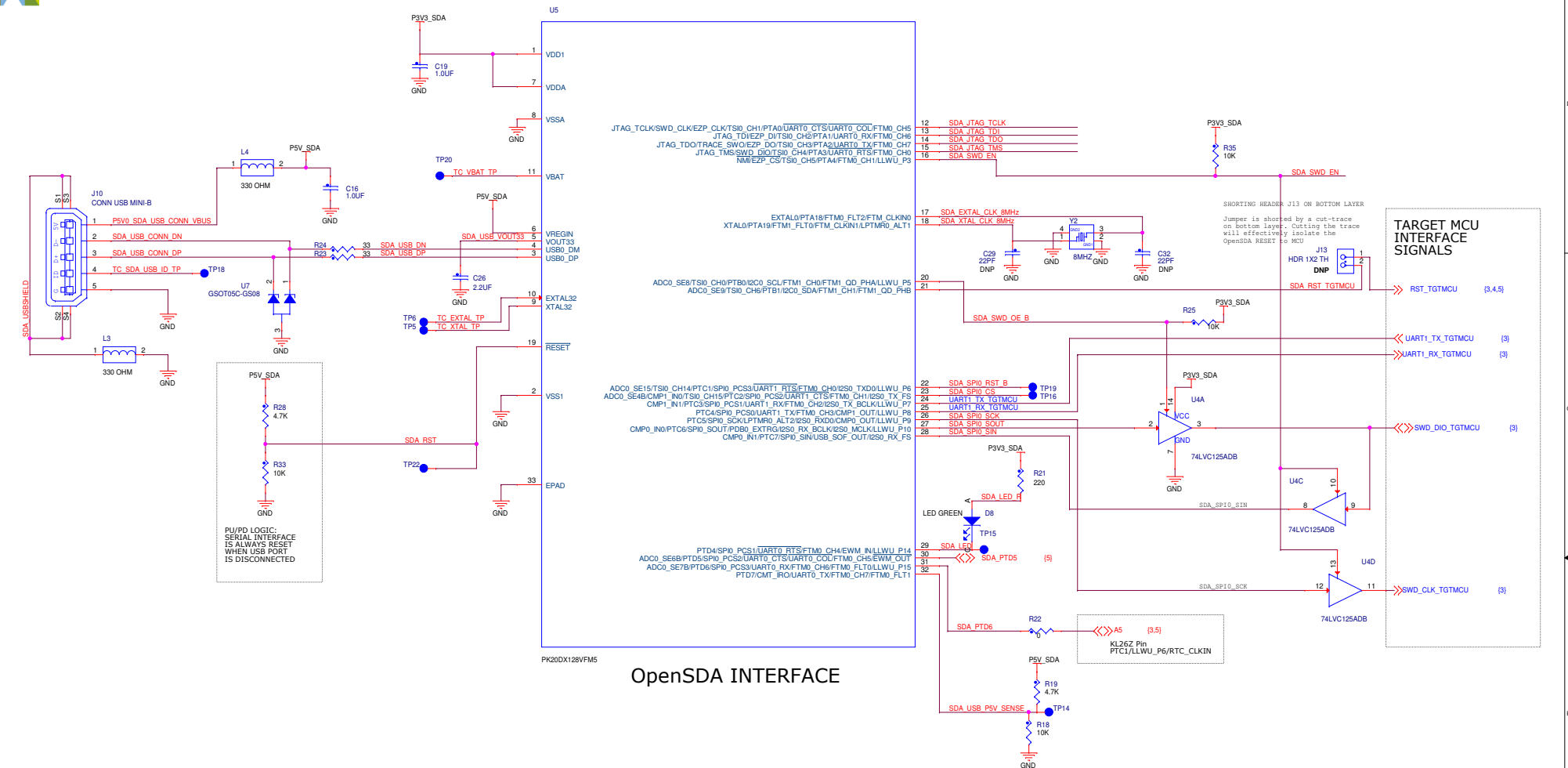
Date _____

Thursday, June 20

013	Sheet 2
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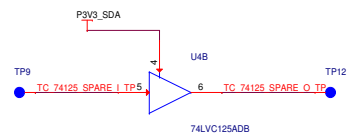
1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
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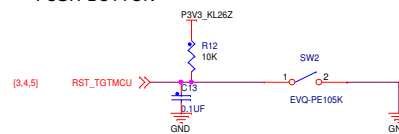


OpenSDA INTERFACE

SPARE 74HC125 buffer



TARGET RESET AND BOOTLOADER PUSH BUTTON



OpenSDA INTERFACE JTAG CONNECTOR

