

SS32F THRU SS320F

Surface Mount Schottky Barrier Rectifier

Reverse Voltage - 20 to 200 V Forward Current - 3.0A

FEATURES

- Metal silicon junction, majority carrier conduction
- For surface mounted applications
- Low power loss, high efficiency
- High forward surge current capability
- For use in low voltage, high frequency inverters, free wheeling, and polarity protection applications

MECHANICAL DATA

• Case: SMAF

• Terminals: Solderable per MIL-STD-750, Method 2026

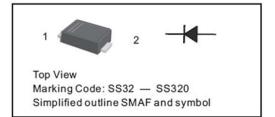
• Approx. Weight: 27mg 0.00086oz

Absolute Maximum Ratings and Electrical characteristics

Ratings at 25 $^{\circ}$ C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz resistive or inductive load, for capacitive load, derate by 20 $^{\circ}$ C

PINNING

PIN	DESCRIPTION
1	Cathode
2	Anode



Parameter	Symbols	SS32F	SS34F	SS36F	SS38F	SS310F	SS312F	SS315F	SS320F	Units
Maximum Repetitive Peak Reverse Voltage	V _{RRM}	20	40	60	80	100	120	150	200	٧
Maximum RMS voltage	V _{RMS}	14	28	42	56	70	84	105	140	٧
Maximum DC Blocking Voltage	V _{DC}	20	40	60	80	100	120	150	200	٧
Maximum Average Forward Rectified Current	I _{F(AV)}	3.0							Α	
Peak Forward Surge Current,8.3ms Single Half Sine-wave Superimposed on Rated Load (JEDEC method)	I _{FSM}	80				70				А
Max Instantaneous Forward Voltage at 3 A	V _F	0.55		0.70		0.85		0.95		٧
Maximum DC Reverse Current T _a = 25°C at Rated DC Reverse Voltage T _a =100°C	IR		.5 0	0.3 5					mA	
Typical Junction Capacitance 1)	Ci	2	50	160					pF	
Typical Thermal Resistance 2)	Reua	40						°C/W		
Operating Junction Temperature Range	Tj	-55 ~ +125							°C	
Storage Temperature Range	T _{stg}	-55 ~ + 150						,	°C	

1) Measured at 1MHz and applied reverse voltage of 4 V D.C. 2) P.C.B. mounted with 0.2 X 0.2" (5 X 5 mm) copper pad areas.

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Fig. 1 Forward Current Derating Curve

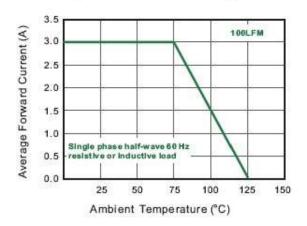


Fig.3 Typical Forward Characteristic

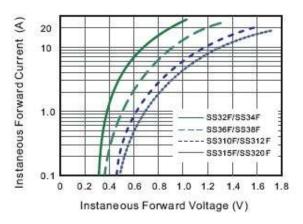


Fig.5 Maximum Non-Repetitive Peak Forward Surage Current

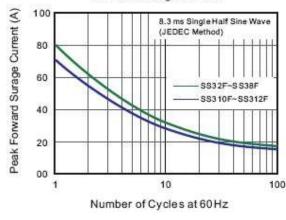


Fig. 2 Typical Reverse Characteristics

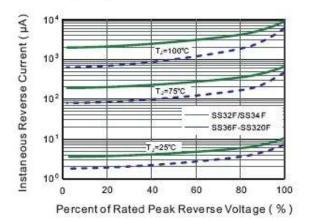


Fig.4 Typical Junction Capacitance

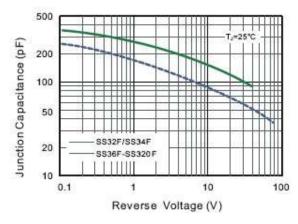
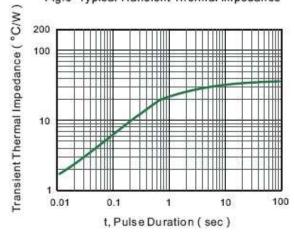


Fig.6- Typical Transient Thermal Impedance



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REV.07



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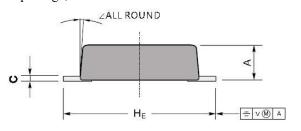
∠ALL ROUND

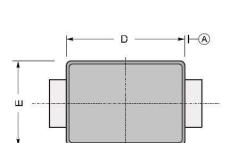
E -

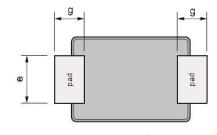
PACKAGE OUTLINE

Plastic surface mounted package; 2 leads







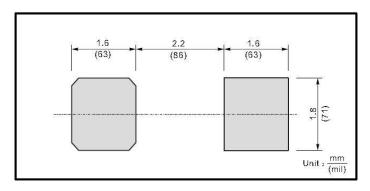


Top View

Bottom View

UNIT		Α	С	D	Е	e	9	H∈	2
mm	max	1.1	0.20	3.7	2.7	1.6	1.2	4.9	
	min	0.9	0.12	3.3	2.4	1.3	8.0	4.4	7°
mil	max	43	7.9	146	106	63	47	193	(
	min	35	4.7	130	94	51	31	173	

The recommended mounting pad size



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