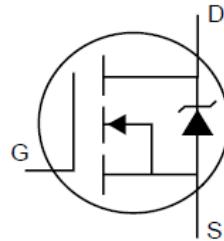
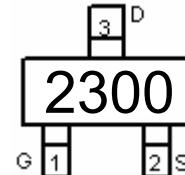


Description

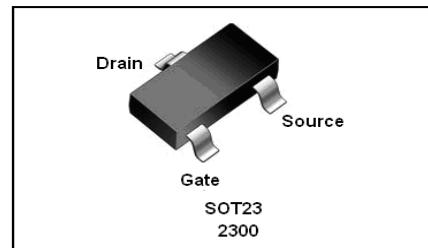
The 2300 designed by the trench processing techniques to achieve extremely low on-resistance. And fast switching speed and improved transfer effective . These features combine to make this design an extremely efficient and reliable device for variety of DC-DC applications.



Schematic diagram



Marking and pin Assignment



Features

- ◆ Ron(typ.)=25 mΩ @VGS=2.5V
- ◆ Ron(typ.)=21 mΩ @VGS=4.5V
- ◆ Low On-Resistance
- ◆ 150°C Operating Temperature
- ◆ Fast Switching
- ◆ Lead-Free, RoHS Compliant

Application

- Battery protection
- Load switch
- Power management

| Symbol | Parameter | Rating | Unit |
|---|-------------------------------------|--|------|
| Common Ratings (T_c=25°C Unless Otherwise Noted) | | | |
| V _{GS} | Gate-Source Voltage | ±12 | V |
| V _{(BR)DSS} | Drain-Source Breakdown Voltage | 20 | V |
| T _J | Maximum Junction Temperature | 150 | °C |
| T _{STG} | Storage Temperature Range | -50 to 155 | °C |
| I _S | Diode Continuous Forward Current | T _c =25°C 5.2 ^① | A |
| Mounted on Large Heat Sink | | | |
| I _{DM} | Pulse Drain Current Tested | T _c =25°C 1 | A |
| I _D | Continuous Drain Current(VGS=10V) | T _c =25°C 5.2 ^① | A |
| | | T _c =100°C 4.0 | |
| P _D | Maximum Power Dissipation | T _c =25°C 1.25 | W |
| R _{θJA} | Thermal Resistance Junction-Ambient | 135 | °C/W |

| Symbol | Parameter | Condition | Min | Typ | Max | Unit |
|---|--|--|-----|------|-------------------------|------------------|
| Static Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated) | | | | | | |
| $V_{(\text{BR})\text{DSS}}$ | Drain-Source Breakdown Voltage | $V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$ | 20 | -- | -- | V |
| I_{DSS} | Zero Gate Voltage Drain Current ($T_c=25^\circ\text{C}$) | $V_{\text{DS}}=20\text{V}, V_{\text{GS}}=0\text{V}$ | -- | -- | 1 | μA |
| | Zero Gate Voltage Drain Current ($T_c=125^\circ\text{C}$) | $V_{\text{DS}}=20\text{V}, V_{\text{GS}}=0\text{V}$ | -- | -- | 100 | μA |
| I_{GSS} | Gate-Body Leakage Current | $V_{\text{GS}}=\pm 12\text{V}, V_{\text{DS}}=0\text{V}$ | -- | -- | ± 100 | nA |
| $V_{\text{GS}(\text{TH})}$ $m\Omega$ | Gate Threshold Voltage | $V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$ | 0.5 | 0.7 | 1.5 | V |
| $R_{\text{DS}(\text{ON})}$ | Drain-Source On-State Resistance | $V_{\text{GS}}=2.5\text{V}, I_{\text{D}}=5.2\text{A}$ | -- | 25 | 35 | $\text{m}\Omega$ |
| $R_{\text{DS}(\text{ON})}$ | Drain-Source On-State Resistance | $V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=6\text{A}$ | -- | 21 | 25 | $\text{m}\Omega$ |
| Dynamic Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated) | | | | | | |
| C_{iss} | Input Capacitance | $V_{\text{DS}}=10\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$ | -- | 630 | -- | pF |
| C_{oss} | Output Capacitance | | -- | 150 | -- | pF |
| C_{rss} | Reverse Transfer Capacitance | | -- | 60 | -- | pF |
| Q_g | Total Gate Charge | $V_{\text{DS}}=10\text{V}, I_{\text{D}}=2.8\text{A}, V_{\text{GS}}=4.5\text{V}$ | -- | 11 | -- | nC |
| Q_{gs} | Gate-Source Charge | | -- | 1.6 | -- | nC |
| Q_{gd} | Gate-Drain Charge | | -- | 2.7 | -- | nC |
| Switching Characteristics | | | | | | |
| $t_{\text{d(on)}}$ | Turn-on Delay Time | $V_{\text{DD}}=10\text{V}, I_{\text{D}}=1\text{A}, R_{\text{G}}=6\Omega, V_{\text{GS}}=4.5\text{V}, RL=5\Omega,$ | -- | 14.5 | -- | nS |
| t_r | Turn-on Rise Time | | -- | 46 | -- | nS |
| $t_{\text{d(off)}}$ | Turn-Off Delay Time | | -- | 52 | -- | nS |
| t_f | Turn-Off Fall Time | | -- | 39 | -- | nS |
| Source- Drain Diode Characteristics | | | | | | |
| I_{SD} | Source-drain current(Body Diode) | $T_c=25^\circ\text{C}$ | -- | -- | $5.2^{\textcircled{1}}$ | A |
| I_{SDM} | Pulsed Source-drain current (Body Diode) | | -- | -- | $20^{\textcircled{1}}$ | A |
| V_{SD} | Forward on voltage | $T_j=25^\circ\text{C}, I_{\text{SD}}=2.8\text{A}, V_{\text{GS}}=0\text{V}$ | -- | 0.85 | 1.3 | V |

Typical Characteristics

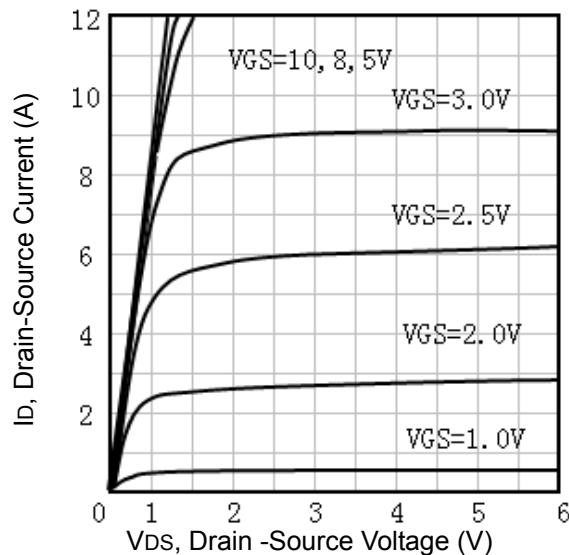


Fig1. Typical Output Characteristics

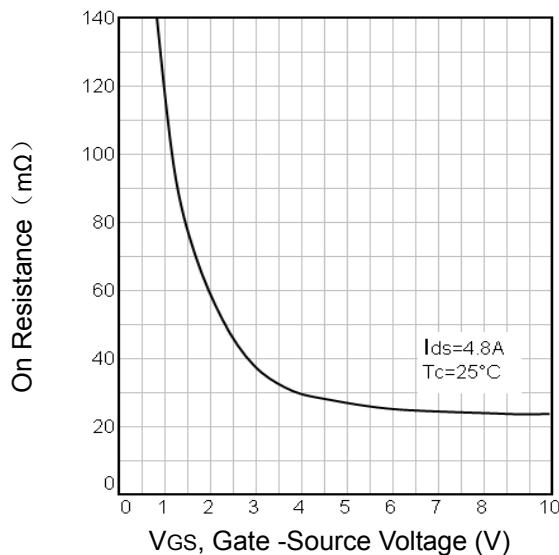


Fig2. Typical Transfer Characteristics

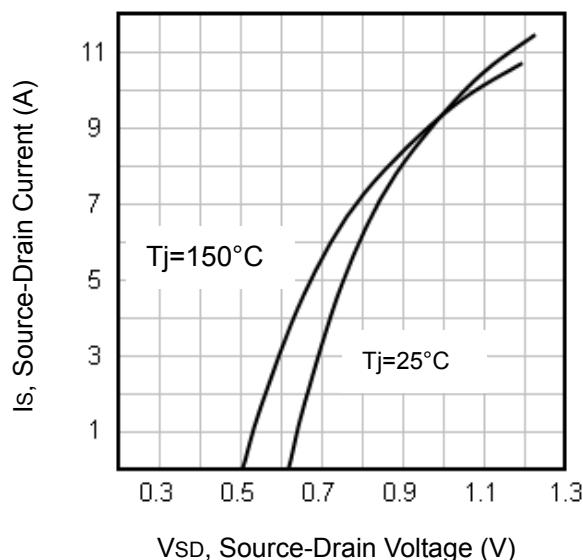


Fig7. Typical Source-Drain Diode Forward Voltage

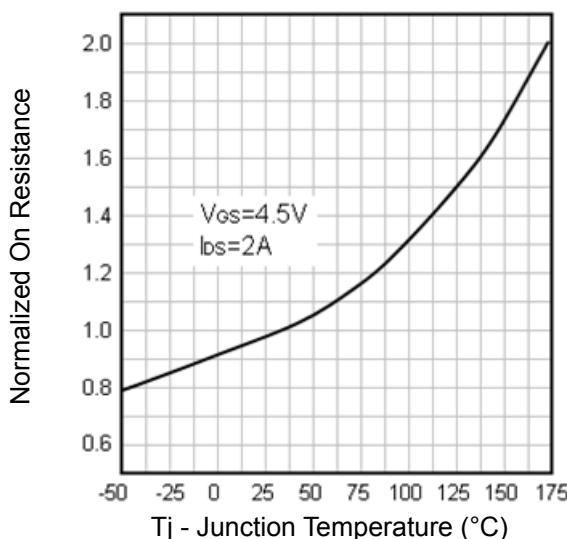


Fig4. Normalized On-Resistance Vs. Temperature

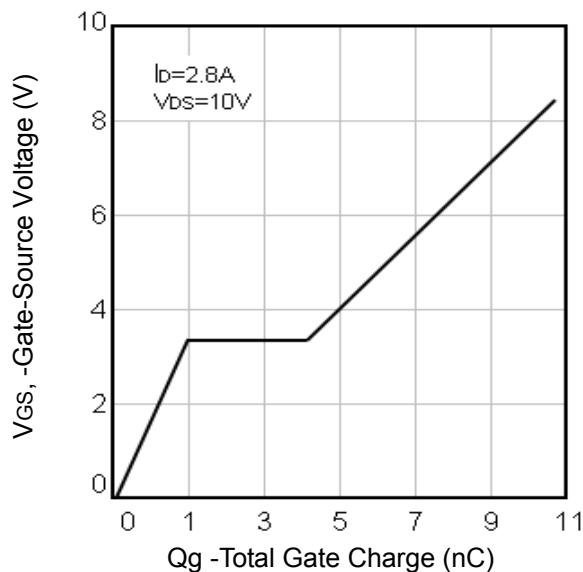


Fig5. Typical Gate Charge Vs. Gate-Source Voltage

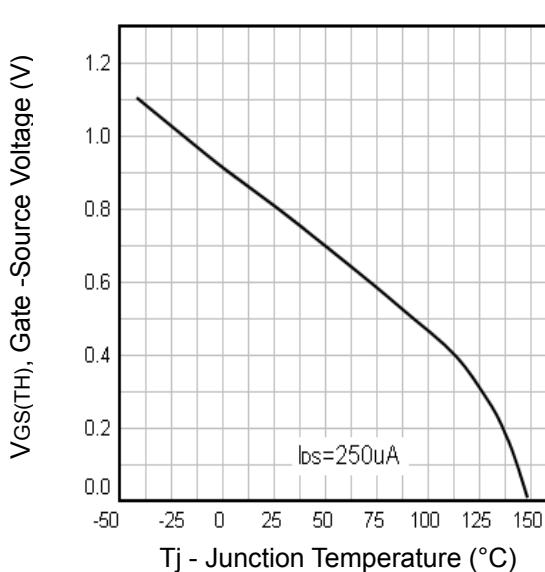


Fig6. Threshold Voltage Vs. Temperature