

LM4871

3W Audio Power Amplifier with Shutdown Mode

FEATURES

- No Output Coupling Capacitors, Bootstrap Capacitors, or Snubber Circuits Required
- Unity-gain Stable
- WSON, VSSOP, SOIC, or PDIP Packaging
- External Gain Configuration Capability
- Pin Compatible with the LM4861 APPLICATIONS
- Portable Computers
- Desktop Computers
- Low Voltage Audio Systems

KEY SPECIFICATIONS

- PO at 10% THD+N, 1kHz
- LM4871LD: 3Ω , 4Ω Loads; 3W (typ), 2.5 W (typ)
- All other LM4871 Packages: 8Ω load
 - 1.5 W (typ)
 - Shutdown Current 0.6µA (typ)
 - Supply Voltage Range 2.0V to 5.5 V
 - THD at 1kHz at 1W Continuous Average

Output Power into 8Ω 0.5% (max)

Connection Diagrams

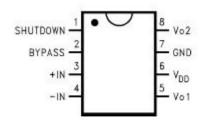


Figure 1. VSSOP, Small Outline, and PDIP
Package
Top View
See Package Number DGK0008A, D0008A, or

DESCRIPTION

The LM4871 is a mono bridged audio power amplifier capable of delivering 3W of continuous average power into a 3 Ω load with less than 10% THD when powered by a 5V power supply (see Note). To conserve power in portable applications, the LM4871's micropower shutdown mode (IQ = 0.6 μ A, typ) is activated when VDD is applied to the SHUTDOWN pin.

Boomer audio power amplifiers are designed specifically to provide high power, high fidelity audio output. They require few external components and operate on low supply voltages from 2.0V to 5.5V. Since the LM4871 does not require output coupling capacitors, bootstrap capacitors, or snubber networks, it is ideally suited for low-power portable systems that require minimum volume and weight. Additional LM4871 features include thermal shutdown

protection, unity-gain stability, and external gain set.

Note: An LM4871LD that has been properly mounted to a circuit board will deliver 3W into 3 Ω (at 10%)

THD). The other package options for the LM4871 will deliver 1.5W into 8 Ω (at 10% THD). See the Application Information section for further information concerning the LM4871LD, LM4871MM, LM4871M, and the LM4871N.

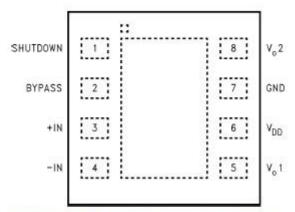


Figure 2. WSON Package (Top View) See Package Number NGN0008A



Typical Application

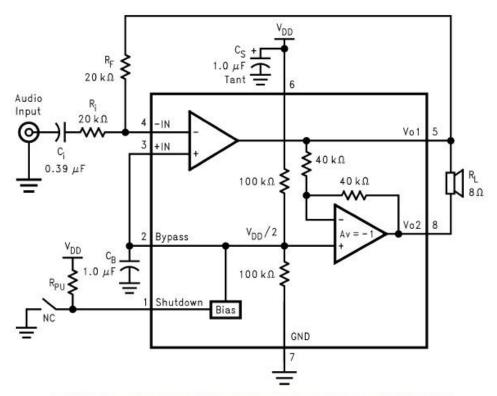


Figure 3. Typical Audio Amplifier Application Circuit



Absolute Maximum Ratings (1)(2)

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Supply Voltage		6.0\	
Supply Temperature		-65°C to +150°C	
Input Voltage		-0.3V to V _{DD} to +0.3V	
Power Dissipation (3)			Internally Limited
ESD Susceptibility ⁽⁴⁾			5000V
ESD Susceptibility ⁽⁵⁾			250V
Junction Temperature	78		150°C
Soldering Information	Small Outline Package	Vapor Phase (60 sec.)	215°C
5		Infrared (15 sec.)	220°C
θ _{JC} (typ)—D0008A		2	35°C/W
θ _{JA} (typ)—D0008A			140°C/W
θ _{JC} (typ)—D0008E			37°C/W
θ _{JA} (typ)—D0008E			107°C/W
θ _{JC} (typ)—DGK0008A			56°C/W
θ _{JA} (typ)—DGK0008A			210°C/W
θ _{JC} (typ)—NGN0008A			4.3°C/W
θ _{JA} (typ)—NGN0008A			56°C/W ⁽⁶⁾

- (1) Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not ensure specific performance limits. Electrical Characteristics state DC and AC electrical specifications under particular test conditions which ensure specific performance limits. This assumes that the device is within the Operating Ratings. Specifications are not ensured for parameters where no limit is given, however, the typical value is a good indication of device performance.
- (2) If Military/Aerospace specified devices are required, please contact the Texas Instruments Sales Office/Distributors for availability and specifications.
- 3) The maximum power dissipation must be derated at elevated temperatures and is dictated by T_{JMAX}, θ_{JA}, and the ambient temperature T_A. The maximum allowable power dissipation is P_{DMAX} = (T_{JMAX}-T_A)/θ_{JA} or the number given in Absolute Maximum Ratings, whichever is lower. For the LM4871, T_{JMAX} = 150°C. For the θ_{JA}'s for different packages, please see the Application Information section or the absolute maximum ratings section.
- (4) Human body model, 100pF discharged through a 1.5kΩ resistor.
- Machine Model, 220pF–240pF discharged through all pins.
- (6) The given θ_{JA} is for an LM4871 packaged in an NGN0008A with the Exposed–DAP soldered to an exposed 1in² area of 1oz printed circuit board copper.

Operating Ratings

Temperature Range T _{MIN} ≤ T _A ≤ T _{MAX}	-40°C ≤ T _A ≤ 85°C
Supply Voltage	$2.0V \le V_{DD} \le 5.5V$

Electrical Characteristics (1)(2)

The following specifications apply for V_{DD} = 5V and R_L = 8 Ω unless otherwise specified. Limits apply for T_A = 25°C.

	Parameter	Conditions	LM4871			1
Symbol			Min ⁽³⁾	Typical (4)	Limit ⁽³⁾	Units (Limits)
V _{DD}	Supply Voltage		2.0		5.5	V
I _{DD}	Quiescent Power Supply Current	$V_{IN} = 0V$, $I_o = 0A$		6.5	10.0	mA
I _{SD}	Shutdown Current	$V_{PIN1} = V_{DD}$		0.6	2	μА
Vos	Output Offset Voltage	V _{IN} = 0V		5.0	50	mV

- (1) Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not ensure specific performance limits. Electrical Characteristics state DC and AC electrical specifications under particular test conditions which ensure specific performance limits. This assumes that the device is within the Operating Ratings. Specifications are not ensured for parameters where no limit is given, however, the typical value is a good indication of device performance.
- (2) All voltages are measured with respect to the ground pin, unless otherwise specified.
- Typicals are specified at 25°C and represent the parametric norm.
- Limits are specified to TI's AOQL (Average Outgoing Quality Level).



Electrical Characteristics⁽¹⁾⁽²⁾ (continued)

The following specifications apply for V_{DD} = 5V and R_L = 8 Ω unless otherwise specified. Limits apply for T_A = 25°C.

Symbol	Parameter	Conditions	LM4871			
			Min ⁽³⁾	Typical ⁽⁴⁾	Limit ⁽³⁾	Units (Limits)
P _o C	Output Power	THD = 1%, f = 1kHz				
		LM4871LD, $R_L = 3\Omega^{(5)}$ LM4871LD, $R_L = 4\Omega^{(5)}$ LM4871, $R_L = 8\Omega^{(5)}$	20	2.38 2 1.2		W
		THD+N = 10%, f = 1kHz LM4871LD, $R_L = 3\Omega^{(5)}$ LM4871LD, $R_L = 4\Omega^{(5)}$ LM4871, $R_L = 8\Omega^{(5)}$		3 2.5 1.5		W
THD+N	Total Harmonic Distortion+Noise	$20Hz \le f \le 20kHz$, $A_{VD} = 2$ $LM4871LD$, $R_{L} = 4Ω$, $P_{O} = 1.6W$ $LM4871$, $R_{L} = 8Ω$, $P_{O} = 1W$		0.13 0.25		%
PSRR	Power Supply Rejection Ratio	V _{DD} = 4.9V to 5.1V		60		dB

⁽⁵⁾ When driving 3Ω or 4Ω loads from a 5V supply, the LM4871LD must be mounted to a circuit board.