

N-Ch MOSFET

General Description

The WSF28N06 is the highest performance trench N-Ch MOSFET with extreme high cell density , which provide excellent RDSON and gate charge for most of the synchronous buck converter applications .

The WSF28N06 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

Product Summery

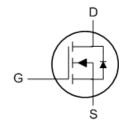
BVDSS	RDSON	ID
60V	28mΩ	28A

Applications

- High Frequency Point-of-Load Synchronous Buck Converter for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- LCD/LED back light

TO-252 Pin Configuration





Absolute Maximum Ratings

Symbol	Parameter	Rating	Units	
V_{DS}	Drain-Source Voltage 60		V	
V_{GS}	Gate-Source Voltage	±20	V	
I _D @T _A =25℃	Continuous Drain Current, V _{GS} @ 10V ¹	28	А	
I _D @T _A =70℃	Continuous Drain Current, V _{GS} @ 10V ¹	17	Α	
I _{DP}	Pulsed Drain Current ²	96	Α	
EAS	Single Pulse Avalanche Energy ³	22	mJ	
I _{AS}	Avalanche Current 28		Α	
P _D @T _C =25°C	Total Power Dissipation⁴	60	W	
P _D @T _c =100℃	Total Power Dissipation⁴	30	W	
T _{STG}	Storage Temperature Range -55 to 175		$^{\circ}$	
T _J	Operating Junction Temperature Range -55 to 175		$^{\circ}$ C	

Thermal Data

Symbol	Parameter	Тур.	Max.	Unit	
$R_{ heta JA}$	Thermal Resistance Junction-Ambient ¹		50	°C/W	
$R_{ heta JC}$	Thermal Resistance Junction-Case ¹		2.5	°C/W	



Electrical Characteristics (T_J=25 ℃, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	60			V
$\triangle BV_{DSS}/\triangle T_{J}$	BV _{DSS} Temperature Coefficient	Reference to 25℃ , I _D =1mA		0.057		V/°C
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V , I _D =12A		28	40	mΩ
		V _{GS} =5.0V , I _D =11A		38	50	
$V_{GS(th)}$	Gate Threshold Voltage	\/ -\/ -250uA	1.0	2.0	3.0	V
$\triangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	$V_{GS}=V_{DS}$, $I_D=250uA$		-5.68		mV/℃
	Drain-Source Leakage Current	V_{DS} =48V , V_{GS} =0V , T_J =25 $^{\circ}\mathrm{C}$			1	
I _{DSS}		V _{DS} =48V , V _{GS} =0V , T _J =55℃			5	· uA
I _{GSS}	Gate-Source Leakage Current	V_{GS} = $\pm 20V$, V_{DS} = $0V$			±100	nA
gfs	Forward Transconductance	V _{DS} =5V , I _D =15A		33		S
Rg	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f=1MHz		1.7	3.4	Ω
Q_g	Total Gate Charge (4.5V)	V _{DS} =30V , V _{GS} =10V , I _D =12A		12	36	nC
Q_{gs}	Gate-Source Charge			3.0	5.2	
Q_{gd}	Gate-Drain Charge			3.2	4.5	
T _{d(on)}	Turn-On Delay Time			8.9	16	
Tr	Rise Time	VDD=30V, RL=30Ω , IDS=1A,		8	15	20
T _{d(off)}	Turn-Off Delay Time	VGEN=10V, RG=6Ω		28	51	ns
T _f	Fall Time			22	41	
Ciss	Input Capacitance	VGS=0V, VDS=30V, Frequency=1.0MHz		830		
C _{oss}	Output Capacitance			85		pF
C _{rss}	Reverse Transfer Capacitance			64		

Diode Characteristics

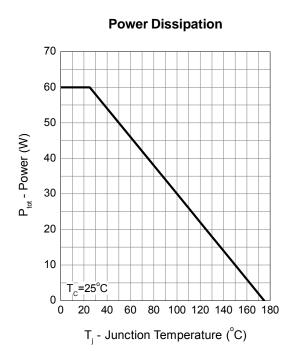
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
I _S	Continuous Source Current ^{1,6}	V_G = V_D = $0V$, Force Current			12	Α
I _{SM}	Pulsed Source Current ^{2,6}				70	Α
V _{SD}	Diode Forward Voltage ²	V_{GS} =0V , I_{S} =12A , T_{J} =25 $^{\circ}$ C			1.3	V
t _{rr}	Reverse Recovery Time	IF=1A ,dl/dt=100A/µs,TJ=25℃		30		nS
Q _{rr}	Reverse Recovery Charge			35		nC

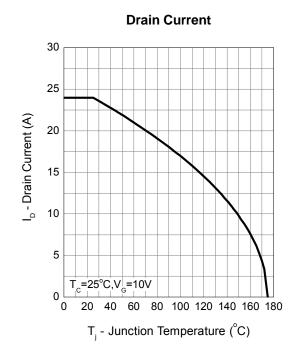
Note a : Pulse test ; pulse width $\leq 300 \, \mu s$, duty cycle $\leq 2\%$.

Note b: Guaranteed by design, not subject to production testing.

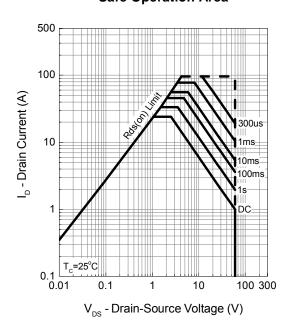


Typical Operating Characteristics

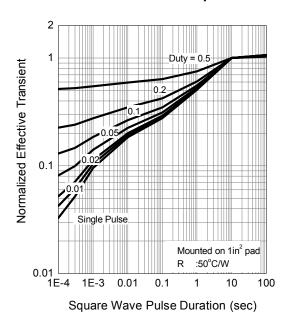




Safe Operation Area

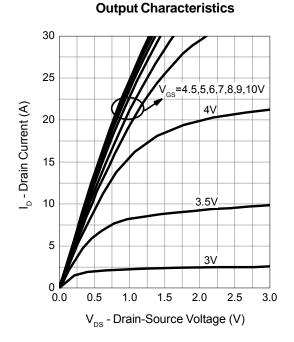


Thermal Transient Impedance

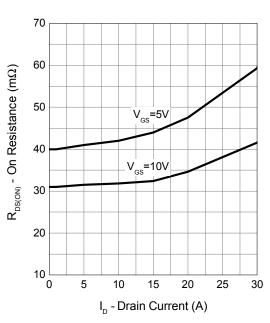




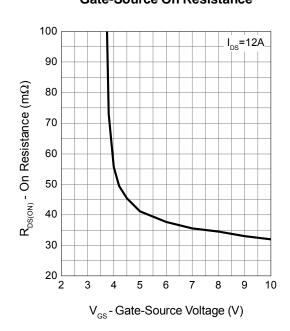
Typical Operating Characteristics (Cont.)



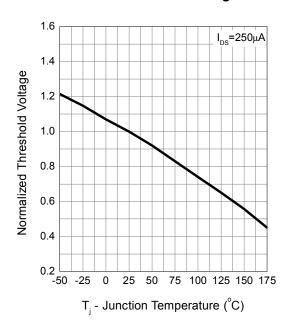
Drain-Source On Resistance



Gate-Source On Resistance



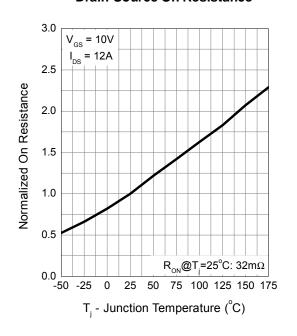
Gate Threshold Voltage



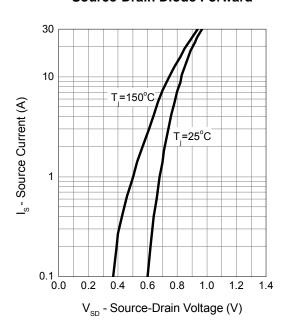


Typical Operating Characteristics (Cont.)

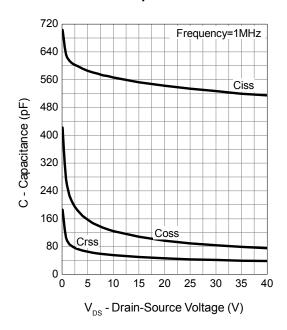
Drain-Source On Resistance



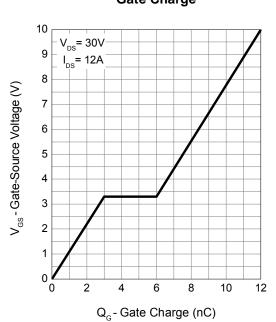
Source-Drain Diode Forward



Capacitance



Gate Charge





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