

**N-Ch MOSFET** 

### **General Description**

The WSD3070DN is the highest performance trench N-ch MOSFETs with extreme high cell density, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

The WSD3070DN meet the RoHS and Green Product requirement , 100% EAS guaranteed with full function reliability approved.

#### Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

### **Product Summery**

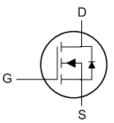
BVDSS	RDSON	ID
25V	2.5mΩ	70A

### Applications

- High Frequency Point-of-Load Synchronous
  Buck Converter for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- Load Switch

#### DFN3.3X3.3 Pin Configuration





Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	25	V
V <sub>GS</sub>	Gate-Source Voltage	±12	V
I₀@T₀=25℃	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	70	A
I <sub>D</sub> @T <sub>C</sub> =100℃	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	55	А
I <sub>D</sub> @T <sub>A</sub> =25℃	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	20	A
I <sub>D</sub> @T <sub>A</sub> =70℃	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	16	А
I <sub>DM</sub> @Тс=25℃	Pulsed Drain Current <sup>2</sup>	200	A
EAS	Avalanche Energy ,Single Pulse (L=0.5mH) <sup>3</sup>	100	mJ
I <sub>AS</sub>	Avalanche Current ,Single pulse(L=0.5mH) <sup>3</sup>	20	A
P <sub>D</sub> @T <sub>C</sub> =25℃	Total Power Dissipation <sup>4</sup>	62.5	W
P <sub>D</sub> @T <sub>A</sub> =25℃	Total Power Dissipation <sup>4</sup>	1.78	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	°C

### **Thermal Data**

Symbol	Parameter	Тур.	Max.	Unit
R <sub>eja</sub>	Thermal Resistance Junction-Ambient <sup>1</sup>		70	°C/W
R <sub>θJC</sub>	Thermal Resistance Junction-Case <sup>1</sup>		2.5	°C/W

### **Absolute Maximum Ratings**



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### Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	25			V
$\triangle BV_{DSS} / \triangle T_J$	BVDSS Temperature Coefficient	Reference to 25 $^\circ\!\mathrm{C}$ , I_D=1mA		0.028		V/℃
Б	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =4.5V , I <sub>D</sub> =20A		2.5	3.4	<b>m</b> 0
R <sub>DS(ON)</sub>		V <sub>GS</sub> =2.5V , I <sub>D</sub> =20A		3.0	4.0	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage		0.5	0.8	1.1	V
$ riangle V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient	$V_{GS}=V_{DS}$ , $I_{D}=250$ uA		-6.16		mV/℃
		V <sub>DS</sub> =24V , V <sub>GS</sub> =0V , T <sub>J</sub> =25℃			1	
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =24V , V <sub>GS</sub> =0V , T <sub>J</sub> =55℃			5	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}=\pm20V$ , $V_{DS}=0V$			±100	nA
gfs	Forward Transconductance	V <sub>DS</sub> =5V , I <sub>D</sub> =40A		74		S
R <sub>g</sub>	Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz		0.85	1.1	Ω
Qg	Total Gate Charge (4.5V)			96	134	
Q <sub>gs</sub>	Gate-Source Charge	VDS=15V, VGS=10V, IDS=20A		5.5	7.7	nC
Q <sub>gd</sub>	Gate-Drain Charge			16	22	
T <sub>d(on)</sub>	Turn-On Delay Time			16.6	31	
Tr	Rise Time	VDD=15V, RL=15Ω ,		12.2	24	
T <sub>d(off)</sub>	Turn-Off Delay Time	IDS=1A, VGEN=10V,		135	244	ns
T <sub>f</sub>	Fall Time	RG=6Ω		48	87	
C <sub>iss</sub>	Input Capacitance			4920		
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> =15V , V <sub>GS</sub> =0V , f=1MHz		510		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			350		

### **Diode Characteristics**

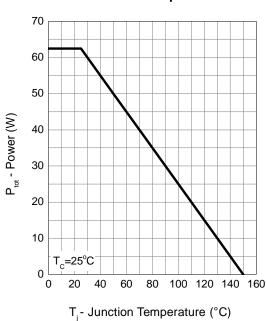
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current <sup>1,6</sup>				20	А
I <sub>SM</sub>	Pulsed Source Current <sup>2,6</sup>	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			70	А
V <sub>SD</sub>	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =1A , TJ=25℃			1.1	V
t <sub>rr</sub>	Reverse Recovery Time			14.8		nS
Qrr	Reverse Recovery Charge	IF=40A,dI/dt=100A/µs,TJ=25℃		3.9		nC

Note d : Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2%. Note e : Guaranteed by design, not subject to production testing.

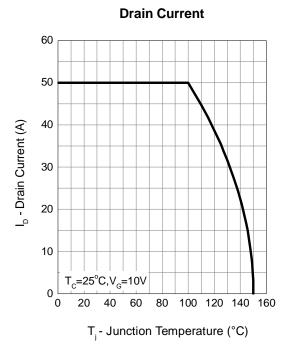


**N-Ch MOSFET** 

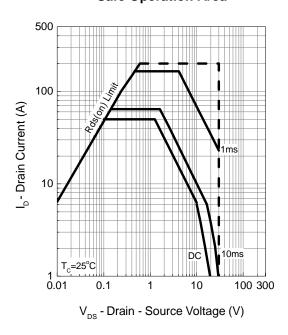
# **Typical Operating Characteristics**



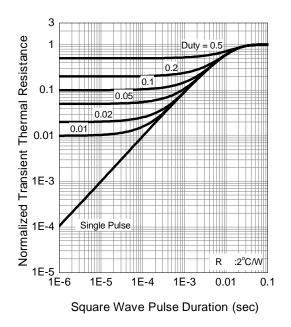
Power Dissipation



Safe Operation Area

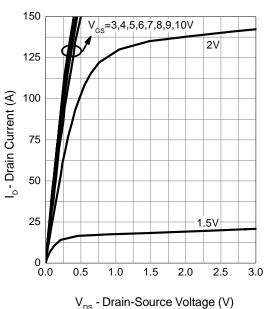


Thermal Transient Impedance





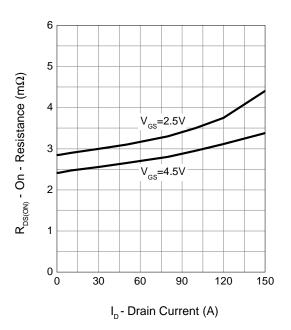
## Typical Operating Characteristics (Cont.)



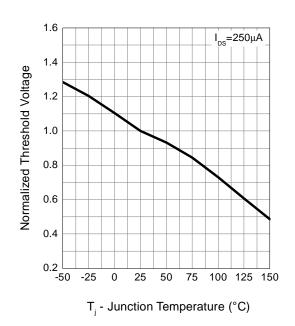
**Output Characteristics** 

3.0

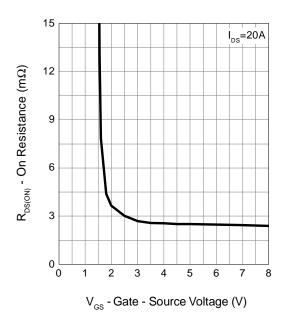
**Drain-Source On Resistance** 



**Gate Threshold Voltage** 



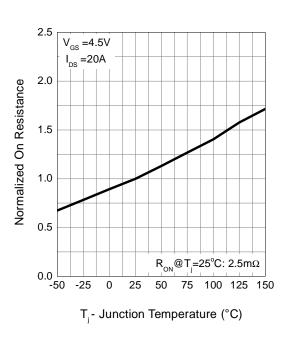
**Gate-Source On Resistance** 



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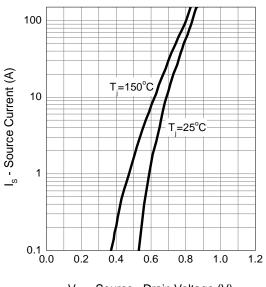


## Typical Operating Characteristics (Cont.)

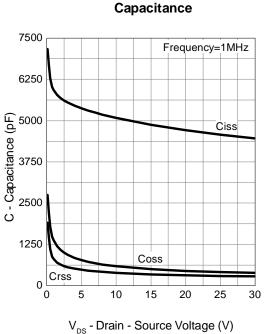


#### **Drain-Source On Resistance**

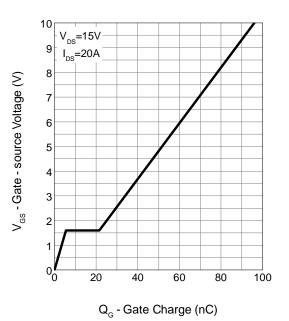
Source-Drain Diode Forward



 $V_{SD}$  - Source - Drain Voltage (V)



**Gate Charge** 

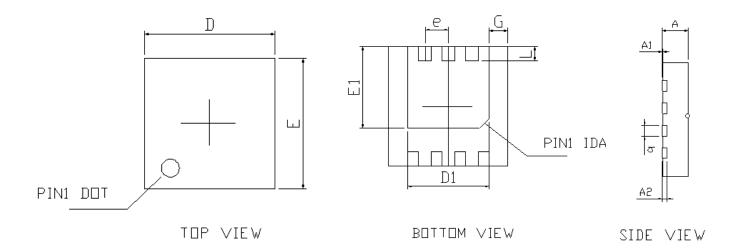




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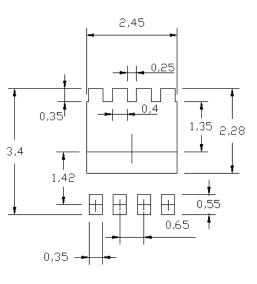
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## DFN3.3x3.3



		DFN3.3x3.3	3	
SYMBOLS	MILLIMETERS		INCHES	HES
	MIN.	MAX.	MIN.	MAX.
A	0.700	0.800	0.028	0.032
A1	0.000	0.050	0.000	0.002
A2	0.100	0.250	0.004	0.010
b	0.240	0.350	0.009	0.014
D	3.150	3.400	0.124	0.134
D1	2.100	2.350	0.083	0.093
Е	3.150	3.400	0.124	0.134
E1	2.150	2.350	0.850	0.093
e	0.600	0.700	0.024	0.028
G	0.475	0.575	0.019	0.023
L	0.350	0.450	0.014	0.018

### RECOMMENDED LAND PATTERN

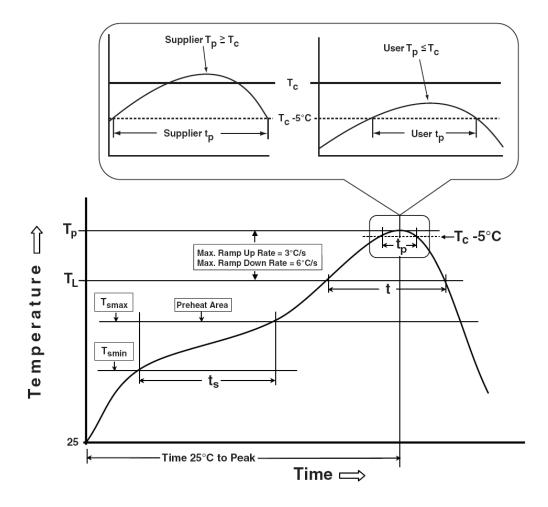




WSD3070DN

**N-Ch MOSFET** 

### **Classification Profile**





### **Classification Reflow Profiles**

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly		
Preheat & Soak Temperature min (T <sub>smin</sub> ) Temperature max (T <sub>smax</sub> ) Time (T <sub>smin</sub> to T <sub>smax</sub> ) (t <sub>s</sub> )	100 °C 150 °C 60-120 seconds	150 °C 200 °C 60-120 seconds		
Average ramp-up rate (T <sub>smax</sub> to T <sub>P</sub> )	3 °C/second max.	3°C/second max.		
Liquidous temperature (T <sub>L</sub> ) Time at liquidous (t <sub>L</sub> )	183 °C 60-150 seconds	217 °C 60-150 seconds		
Peak package body Temperature (T <sub>p</sub> )*	See Classification Temp in table 1	See Classification Temp in table 2		
Time $(t_P)^{**}$ within 5°C of the specified classification temperature $(T_c)$	20** seconds	30** seconds		
Average ramp-down rate ( $T_p$ to $T_{smax}$ )	6 °C/second max.	6 °C/second max.		
Time 25°C to peak temperature	6 minutes max.	8 minutes max.		
* Tolerance for peak profile Temperature ( $T_p$ ) is defined as a supplier minimum and a user maximum. ** Tolerance for time at peak profile temperature ( $t_p$ ) is defined as a supplier minimum and a user maximum.				

Table 1. SnPb Eutectic Process – Classification Temperatures (Tc)

Package Thickness	Volume mm <sup>3</sup> <350	Volume mm <sup>3</sup> ³350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2. Pb-free Process – Classification Temperatures (Tc)

Package Thickness	Volume mm <sup>3</sup> <350	Volume mm <sup>3</sup> 350-2000	Volume mm <sup>3</sup> >2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

## Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	1000 Hrs, 80% of VDS max @ Tjmax
HTGB	JESD-22, A108	1000 Hrs, 100% of VGS max @ Tjmax
PCT	JESD-22, A102	168 Hrs, 100%RH, 2atm, 121°C
тст	JESD-22, A104	500 Cycles, -65°C~150°C



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