

General Description

The WSD2012DN25 is the highest performance trench N-ch MOSFETs with extreme high cell density, which provide excellent RDSON and gate charge for most of the small power switching and load switch applications.

The WSD2012DN25 meet the RoHS and Green Product requirement with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent Cdv/dt effect decline
- Green Device Available

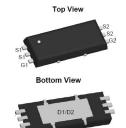
Product Summery

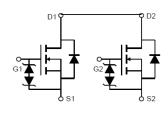
BVDSS	RDSON	ID
20V	$9.5 m\Omega_{(max)}$	11A

Applications

- Power management in portable and battery operated products
- DC-DC Power System
- ESD:2KV

DFN2X5 Pin Configuration





Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage 20		V
V_{GS}	Gate-Source Voltage	±12	V
I _D @T _A =25℃	Continuous Drain Current, V _{GS} @ 4.5V ¹	11	Α
I _D @T _A =70°C	Continuous Drain Current, V _{GS} @ 4.5V ¹ 9		Α
I _{DM}	Pulsed Drain Current ²	95	Α
P _D @T _A =25℃	Total Power Dissipation ³	1.7	W
P _D @T _A =70℃	Total Power Dissipation ³ 1.0		W
T _{STG}	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	$^{\circ}$ C

Thermal Data

Symbol	Parameter	Тур.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient ¹ (Steady State)		75	°C/W
R _{θJA}	Thermal Resistance Junction-ambient ¹ (t<10S)		40	°C/W



Electrical Characteristics (T_J=25 C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	20			V
$\triangle BV_{DSS}/\triangle T_{J}$	BVDSS Temperature Coefficient	Reference to 25°C , I _D =1mA		0.022		V/°C
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =4.5V , I _D =5.5A	5.0	7.0	9.5	mΩ
		V _{GS} =2.5V , I _D =5.5A	6.8	9.0	13	
$V_{GS(th)}$	Gate Threshold Voltage	\/ -\/ -250uA	0.3	0.65	1.0	V
$\triangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	$-V_{GS}=V_{DS}$, $I_D=250uA$		-2.32		mV/℃
	Drain-Source Leakage Current	V _{DS} =16V , V _{GS} =0V , T _J =25℃			1	
I _{DSS}		V _{DS} =16V , V _{GS} =0V , T _J =55℃			5	- uA
I _{GSS}	Gate-Source Leakage Current	V_{GS} = $\pm 12V$, V_{DS} = $0V$			±100	nA
gfs	Forward Transconductance	V _{DS} =5V , I _D =10A		65		S
R_g	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f=1MHz		2500		Ω
Q_g	Total Gate Charge (4.5V)		10	12.5	15	nC
Q_{gs}	Gate-Source Charge	V _{DS} =10V , V _{GS} =4.5V , I _D =5.5A		5.5		
Q_{gd}	Gate-Drain Charge			6.5		
T _{d(on)}	Turn-On Delay Time			1.1		
T _r	Rise Time	V_{DD} =10V , V_{GS} =10V , R_{G} =1 Ω ,		2.6		- - μs
T _{d(off)}	Turn-Off Delay Time	I _D =1A ,RL=10Ω		7		
T _f	Fall Time			7.4		
C _{iss}	Input Capacitance	V _{DS} =10V , V _{GS} =0V , f=1MHz		1256		
Coss	Output Capacitance			220		pF
C _{rss}	Reverse Transfer Capacitance			168		

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
I _S	Continuous Source Current ^{1,4}	V _G =V _D =0V , Force Current			3	Α
I _{SM}	Pulsed Source Current ^{2,4}				45	Α
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V , I _S =1A , T _J =25℃		0.58	1.0	V
t _{rr}	Reverse Recovery Time	IF=10A,dI/dt=500A/µs , Tյ=25℃		11		nS
Q _{rr}	Reverse Recovery Charge			15		nC

Note:

- 1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper, t<10sec.
- 2.The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%
- 3.The power dissipation is limited by 150 ℃ junction temperature
- $\textbf{4.The data is theoretically the same as } \textbf{I}_{D} \text{ and } \textbf{I}_{DM} \text{ , in real applications , should be limited by total power dissipation.}$



Typical Characteristics

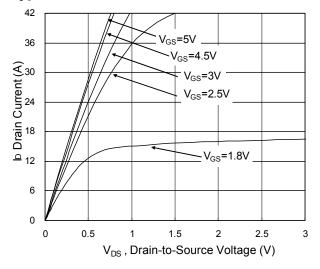


Fig.1 Typical Output Characteristics

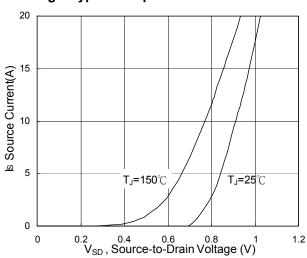


Fig.3 Forward Characteristics Of Reverse

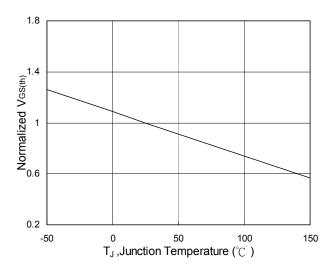


Fig.5 V_{GS(th)} vs. T_J

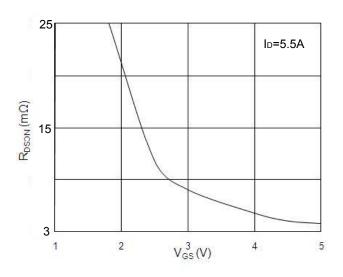


Fig.2 On-Resistance vs. Gate-Source

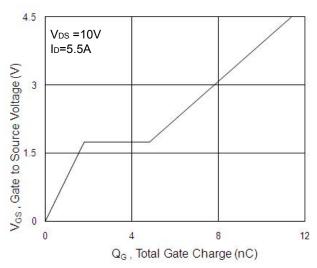


Fig.4 Gate-Charge Characteristics

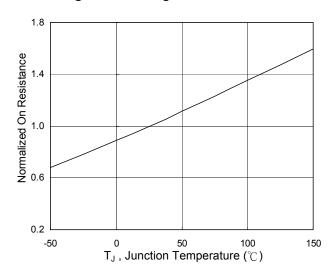
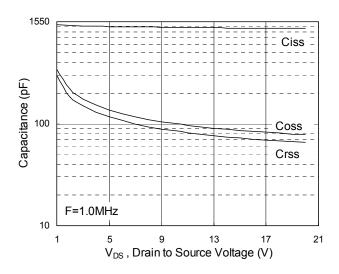


Fig.6 Normalized R_{DSON} vs. T_J





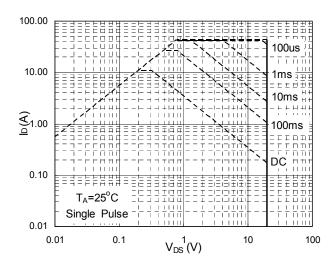


Fig.7 Capacitance

Fig.8 Safe Operating Area

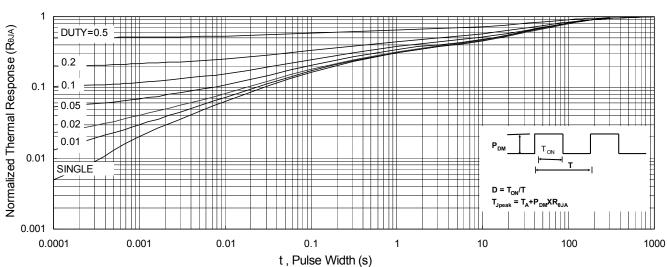


Fig.9 Normalized Maximum Transient Thermal Impedance

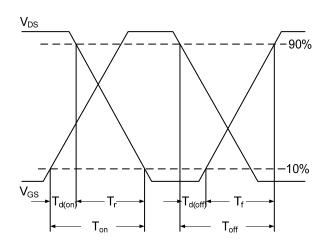


Fig.10 Switching Time Waveform

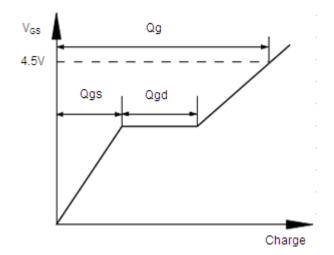


Fig.11 Gate Charge Waveform



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