



General Description

The WSF12N10 is the highest performance trench N-ch MOSFET with extreme high cell density , which provide excellent RDSON and gate charge for most of the synchronous buck converter applications .

The WSF12N10 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent Cdv/dt effect decline
- Green Device Available

Product Summery

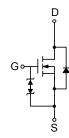
BVDSS	RDSON	ID
100V	175mΩ	12A

Applications

- High Frequency Point-of-Load Synchronous Buck Converter
- Networking DC-DC Power System
- Load Switch

TO-252 Pin Configuration





Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	100	V
V_{GS}	Gate-Source Voltage	±20	V
I _D @T _C =25℃	Continuous Drain Current, V _{GS} @ 10V ¹	10	Α
I _D @T _C =100°C	Continuous Drain Current, V _{GS} @ 10V ¹	7	Α
I _{DM}	Pulsed Drain Current ²	15	Α
EAS	Single Pulse Avalanche Energy ³	15	mJ
I _{AS}	Avalanche Current	6	Α
P _D @T _C =25℃	Total Power Dissipation ³	60	W
P _D @T _C =100℃	Total Power Dissipation ³	30	W
T _{STG}	Storage Temperature Range	-55 to 170	$^{\circ}$
TJ	Operating Junction Temperature Range -55 to 170		$^{\circ}$

Thermal Data

Symbol	Parameter	Тур.	Max.	Unit
R _{0JA}	Thermal Resistance Junction-ambient ¹		50	°C/W
$R_{ heta JC}$	Thermal Resistance Junction-Case ¹		2.5	°C/W



Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	100			V
$\triangle BV_{DSS}/\triangle T_{J}$	BVDSS Temperature Coefficient	Reference to 25°C , I _D =1mA		0.098		V/°C
В	Static Drain-Source On-Resistance ²	V _{GS} =10V , I _D =5A		175	220	mΩ
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =4.5V , I _D =2A		220	310	mΩ
V _{GS(th)}	Gate Threshold Voltage	\/ -\/ -250A	1.0	1.8	2.4	V
$\triangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	V _{GS} =V _{DS} , I _D =250uA		-4.57		mV/℃
,	Davis Consort and an Orange	V _{DS} =80V , V _{GS} =0V , T _J =25℃			1	uA
I _{DSS}	Drain-Source Leakage Current	V _{DS} =80V , V _{GS} =0V , T _J =55℃			5	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V , V _{DS} =0V			±100	nA
gfs	Forward Transconductance	V _{DS} =5V , I _D =5A		13		S
Rg	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f=1MHz		2	4	Ω
Q_{g}	Total Gate Charge (10V)		6.0	9.5	13	
Q _{gs}	Gate-Source Charge	V _{DS} =50V , V _{GS} =10V , I _D =5A	1.3	1.9	2.5	nC
Q _{gd}	Gate-Drain Charge		1.0	2.1	3.1	
T _{d(on)}	Turn-On Delay Time			11	21	
Tr	Rise Time	V_{DD} =30V , V_{GS} =10V , R_G =6 Ω I_D =1A , R_L =30 Ω		10	19	
T _{d(off)}	Turn-Off Delay Time			21	39	ns
T _f	Fall Time			13	24	
C _{iss}	Input Capacitance	V _{DS} =30V , V _{GS} =0V , f=1MHz		440		
C _{oss}	Output Capacitance			36		pF
C _{rss}	Reverse Transfer Capacitance			20		

Guaranteed Avalanche Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
EAS	Single Pulse Avalanche Energy ⁵	V _{DD} =25V , L=0.5mH , I _{AS} =3A	10			mJ

Diode Characteristics

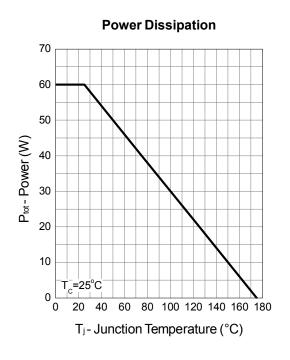
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current ^{1,6}	V _G =V _D =0V , Force Current			3	Α
I _{SM}	Pulsed Source Current ^{2,6}	V _G -V _D -0V , Force Current			9	Α
V_{SD}	Diode Forward Voltage ²	V _{GS} =0V , I _S =3A , T _J =25℃			1.1	V
t _{rr}	Reverse Recovery Time		25	36	47	nS
Q _{rr}	Reverse Recovery Charge	IF=5A,dI/dt=100A/µs,T _J =25℃	34	49	64	nC

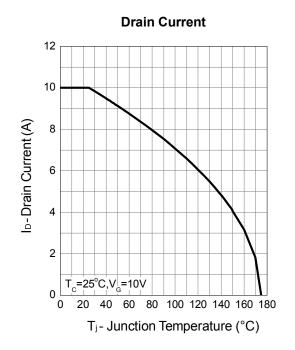
Note:

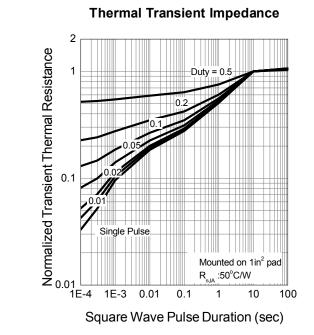
- 1. The data tested by surface mounted on a 1 inch 2 FR-4 board with 2OZ copper, $t \le 10 sec$.
- 2.The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%
- 3.The EAS data shows Max. rating . The test condition is $V_{\text{DD}}\text{=}25\text{V}, V_{\text{GS}}\text{=}10\text{V}, L\text{=}0.5\text{mH}, I_{\text{AS}}\text{=}3\text{A}$
- 5.The Min. value is 100% EAS tested guarantee.
- 6. The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.



Typical Characteristics



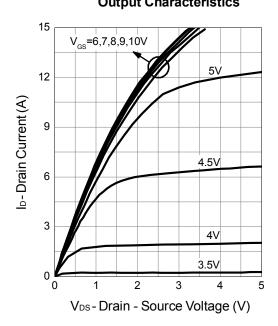




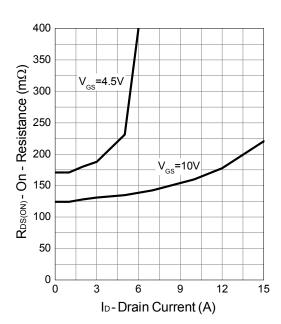


Typical Characteristics

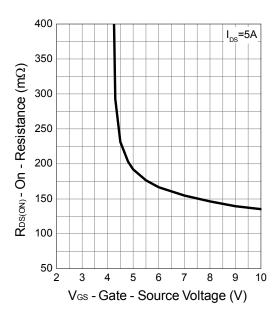
Output Characteristics



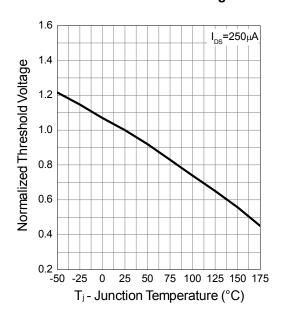
Drain-Source On Resistance



Gate-Source On Resistance



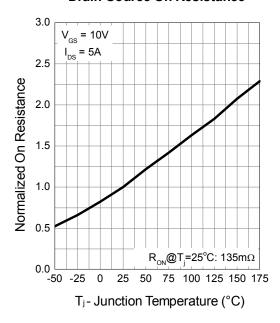
Gate Threshold Voltage



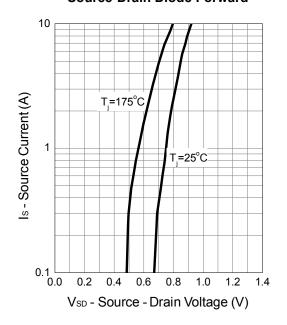


Typical Characteristics

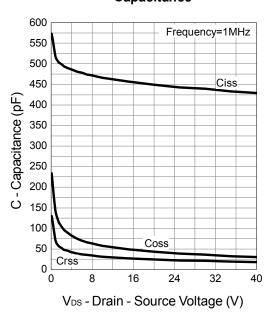
Drain-Source On Resistance



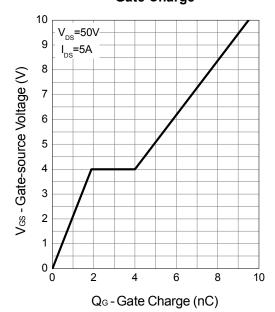
Source-Drain Diode Forward



Capacitance



Gate Charge





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