

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC74VHCT32AF, TC74VHCT32AFK

#### Quad 2-Input OR Gate

The TC74VHCT32A is an advanced high speed CMOS 2-INPUT OR GATE fabricated with silicon gate C<sup>2</sup>MOS technology.

It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

The internal circuit is composed of 4 stages including buffer output, which provide high noise immunity and stable output.

The input voltage are compatible with TTL output voltage.

This device may be used as a level converter for interfacing 3.3 V to 5 V system.

Input protection and output circuit ensure that 0 to 5.5 V can be applied to the input and output  $^{\rm (Note)}$  pins without regard to the supply voltage. These structure prevents device destruction due to mismatched supply and input/output voltages such as battery back up, hot board insertion, etc.

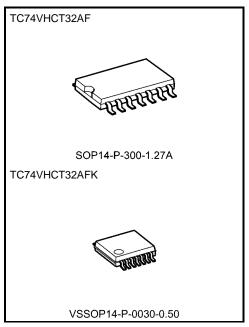
Note: VCC = 0 V

#### **Features**

- High speed: tpd = 3.8 ns (typ.) at VCC = 5 V
- Low power dissipation: ICC = 2 μA (max) at Ta = 25°C
- Compatible with TTL inputs: VIL = 0.8 V (max)

VIH = 2.0 V (min)

- Power down protection is provided on all inputs and outputs.
- Balanced propagation delays: t<sub>pLH</sub> ≃ t<sub>pHL</sub>
- Low noise: VOLP = 0.8 V (max)
- Pin and function compatible with the 74 series (74AC/HC/F/ALS/LS etc.) 32 type.



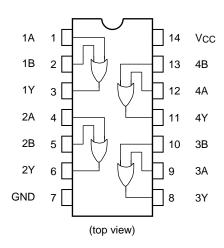
Weight

SOP14-P-300-1.27A: 0.18 g (typ.) VSSOP14-P-0030-0.50: 0.02 g (typ.)

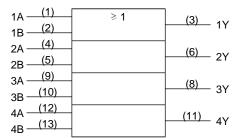
Start of commercial production 1998-01



#### **Pin Assignment**



#### **IEC Logic Symbol**



#### **Truth Table**

Α	В	Υ
Н	Н	Н
L	Н	Н
Н	L	Н
L	L	L

### **Absolute Maximum Ratings (Note 1)**

Characteristics	Symbol	Rating	Unit
Supply voltage range	Vcc	−0.5 to 7.0	V
DC input voltage	VIN	−0.5 to 7.0	V
DC output voltage	\/	-0.5 to 7.0 (Note 2)	\/
	Vout	-0.5 to V <sub>CC</sub> + 0.5 (Note 3)	V
Input diode current	lıĸ	-20	mA
Output diode current	lok	±20 (Note 4)	mA
DC output current	lout	±25	mA
DC Vcc/ground current	Icc	±50	mA
Power dissipation	PD	180	mW
Storage temperature	T <sub>stg</sub>	−65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: VCC = 0 V

Note 3: High or low state. IOUT absolute maximum rating must be observed.

Note 4: VOUT < GND, VOUT > VCC



# **Operating Ranges (Note 1)**

Characteristics	Symbol	Rating	Unit
Supply voltage	Vcc	4.5 to 5.5	V
Input voltage	VIN	0 to 5.5	V
Output voltage	Vour	0 to 5.5 (Note 2)	V
	Vout	0 to Vcc (Note 3)	V
Operating temperature	Topr	-40 to 85	°C
Input rise and fall time	dt/dV	0 to 20	ns/V

Note 1: The operating ranges must be maintained to ensure the normal operation of the device.

Unused inputs must be tied to either VCC or GND.

Note 2: VCC = 0 V

Note 3: High or low state

#### **Electrical Characteristics**

#### **DC Characteristics**

Characteristics Symbol		Test Condition			Ta = 25°C			Ta = −40 to 85°C		Unit
				V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	
High-level input voltage	VIH	_		4.5 to 5.5	2.0	-	_	2.0	ı	V
Low-level input voltage	VIL	_		4.5 to 5.5	_	-	0.8	-	0.8	V
High-level output	Vou	VIN = VIH or VIL	I <sub>OH</sub> = -50 μA	4.5	4.40	4.50	_	4.40	_	V
voltage	Vон		I <sub>OH</sub> = −8 mA	4.5	3.94	_	_	3.80	-	
Low-level output voltage	V	\/\/	I <sub>OL</sub> = 50 μA	4.5	_	0.0	0.1	_	0.1 V	
	VOL	VIN = VIL	I <sub>OL</sub> = 8 mA	4.5	_	_	0.36	_	0.44	V
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = 5.5 V or GND		0 to 5.5	_	_	±0.1	_	±1.0	μΑ
Icc		VIN = VCC or GND		5.5	_	_	2.0	_	20.0	μΑ
Quiescent supply current	Ісст	Per input: V <sub>IN</sub> = 3.4 V Other input: V <sub>CC</sub> or GND		5.5	_	_	1.35	_	1.50	mA
Output leakage current (Power-OFF)	IOPD	V <sub>OUT</sub> = 5.5 V		0	_	_	0.5	_	5.0	μА



### AC Characteristics (input: tr = tf = 3 ns)

Characteristics Sym	Symbol			st Condition		Ta = 25°C			Ta = −40 to 85°C	
	Cymbol		V <sub>CC</sub> (V)	C <sub>L</sub> (pF)	Min	Тур.	Max	Min	Max	Unit
Propagation delay tpLH time tpHL	tpLH	_	5.0 ± 0.5	15	_	3.8	5.5	1.0	6.5	- ns
	t <sub>pHL</sub>			50	_	5.3	7.5	1.0	8.5	
Input capacitance	CIN		_		_	4	10	_	10	pF
Power dissipation capacitance	CPD			(Note)	_	14	_	_	_	pF

Note: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

ICC (opr) = CPD·VCC·fIN + ICC/4 (per gate)

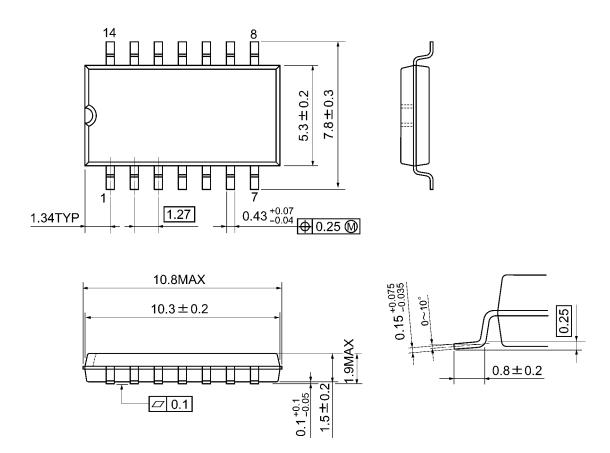
### Noise Characteristics (input: tr = tf = 3 ns)

Ohamatariatian	0	Test Condition	Ta =	1.121		
Characteristics	Symbol		V <sub>CC</sub> (V)	Тур.	Limit	Unit
Quiet output maximum dynamic VOL	VOLP	C <sub>L</sub> = 50 pF	5.0	0.4	0.8	V
Quiet output minimum dynamic VoL	Volv	C <sub>L</sub> = 50 pF	5.0	-0.4	-0.8	V
Minimum high level dynamic input voltage	VIHD	C <sub>L</sub> = 50 pF	5.0	_	2.0	V
Maximum low level dynamic input voltage	VILD	C <sub>L</sub> = 50 pF	5.0	1	0.8	V



# **Package Dimensions**

SOP14-P-300-1.27A Unit: mm

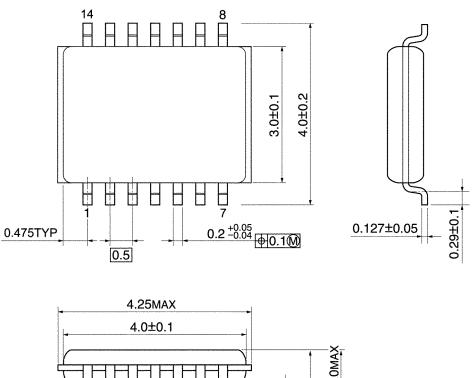


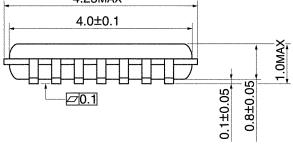
Weight: 0.18 g (typ.)



# **Package Dimensions**

VSSOP14-P-0030-0.50 Unit: mm





Weight: 0.02 g (typ.)



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