

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74VHC132F, TC74VHC132FK

Quad 2-Input Schmitt NAND Gate

The TC74VHC132 is an advanced high speed CMOS 2-Input Schmitt NAND Gate fabricated with silicon gate $\rm C^2MOS$ technology.

It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

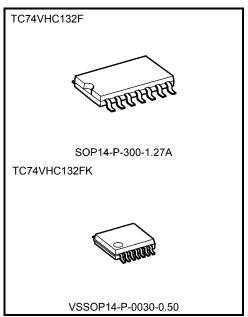
Pin configuration and function are the same as the TC74VHC00 but the inputs have hysteresis and with its schmitt trigger function, the TC74VHC132 can be used as a line receivers which will receive slow input signals.

An input protection circuit ensures that 0 to 5.5 V can be applied to the input pins without regard to the supply voltage. This device can be used to interface 5 V to 3 V systems and two supply systems such as battery back up.

This circuit prevents device destruction due to mismatched supply and input voltages.

Features

- High speed: $t_{pd} = 4.9 \text{ ns (typ.)}$ at $V_{CC} = 5 \text{ V}$
- Low power dissipation: $I_{CC} = 2 \mu A \text{ (max)}$ at $T_a = 25 \text{°C}$
- Power down protection is provided on all inputs.
- Balanced propagation delays: t_pLH ≃ t_pHL
- Wide operating voltage range: V_{CC} (opr) = 2 to 5.5 V
- Low noise: VOLP = 0.8 V (max)
- Pin and function compatible with 74ALS132



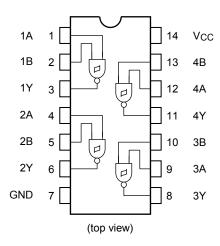
Weight

SOP14-P-300-1.27A : 0.18 g (typ.) VSSOP14-P-0030-0.50 : 0.02 g (typ.)

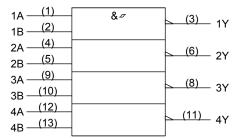
Start of commercial production 1992-05



Pin Assignment



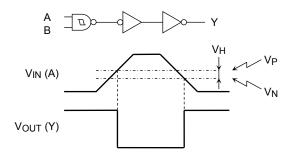
IEC Logic Symbol



Truth Table

Α	В	Υ
L	L	Н
L	Н	Н
Н	L	Н
Н	Н	L

System Diagram, Waveform



2019-01-31



Absolute Maximum Ratings (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage range	Vcc	−0.5 to 7.0	V
DC input voltage	VIN	−0.5 to 7.0	V
DC output voltage	Vout	-0.5 to V _{CC} + 0.5	V
Input diode current	lıĸ	-20	mA
Output diode current	Іок	±20	mA
DC output current	lout	±25	mA
DC V _{CC} /ground current	Icc	±50	mA
Power dissipation	PD	180	mW
Storage temperature	T _{stg}	−65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Operating Ranges (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	Vcc	2.0 to 5.5	V
Input voltage	V _{IN}	0 to 5.5	V
Output voltage	Vout	0 to V _{CC}	V
Operating temperature	Topr	−40 to 85	°C

Note: The operating ranges must be maintained to ensure the normal operation of the device.

Unused inputs must be tied to either VCC or GND.



Electrical Characteristics

DC Characteristics

Characteristics	Symbol	Test Condition			Ta = 25°C		Ta = -40 to 85°C		Unit		
onaractoriotics	Cymbol			Vcc (V)	Min	Тур.	Max	Min	Max	O.I.I.	
Positive threshold		_		3.0	_	_	2.20	_	2.20		
voltage	VP			4.5	_	_	3.15	_	3.15	V	
				5.5	_	_	3.85	_	3.85		
Negative threshold				3.0	0.90	_	_	0.90	_		
voltage	VN	_	-	4.5	1.35	_	_	1.35	_	V	
				5.5	1.65	_	_	1.65	_		
I brokemenia arriburd				3.0	0.30	_	1.20	0.30	1.20		
Hysteresis output voltage	VH	_	-	4.5	0.40	_	1.40	0.40	1.40	V	
				5.5	0.50	_	1.60	0.50	1.60		
		VIN = VIH or VIL		2.0	1.9	2.0	_	1.9	_		
			IOH = −50 μA	3.0	2.9	3.0	_	2.9	_		
High-level output voltage	Voн			4.5	4.4	4.5	1	4.4	_	V	
voltago			I _{OH} = −4 mA	3.0	2.58	_	_	2.48	_		
			I _{OH} = −8 mA	4.5	3.94	_	_	3.80	_		
		V _{IN} = V _{IH}		2.0	_	0.0	0.1	_	0.1		
			I _{OL} = 50 μA	3.0	_	0.0	0.1	_	0.1		
Low-level output voltage	V _{OL}			4.5	_	0.0	0.1	_	0.1	V	
			I _{OL} = 4 mA	3.0	_	_	0.36	_	0.44		
			I _{OL} = 8 mA	4.5	_	_	0.36	_	0.44		
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND		0 to 5.5		_	±0.1	_	±1.0	μA	
Quiescent supply current	Icc	V _{IN} = V _{CC} or GND		5.5	_	_	2.0	_	20.0	μΑ	

AC Characteristics (input: $t_r = t_f = 3 \text{ ns}$)

Characteristics Symbol	Test Condition		Ta = 25°C			Ta = −40 to 85°C		- Unit		
	- Cymbol		V _{CC} (V)	C _L (pF)	Min	Тур.	Max	Min	Max	Onne
Propagation delay tpLH time tpHL			3.3 ± 0.3	15	_	7.6	11.9	1.0	14.0	- - ns
	t _{pLH}			50	_	10.1	15.4	1.0	17.5	
	t _{pHL}	_	5.0 ± 0.5	15	_	4.9	7.7	1.0	9.0	
				50	_	6.4	9.7	1.0	11.0	
Input capacitance	C _{IN}	_		_	4	10	_	10	pF	
Power dissipation capacitance	CPD			(Note)	ı	16	ı	ı	ı	pF

Note: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

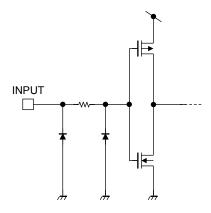
ICC (opr) = CPD·VCC·fIN + ICC/4 (per gate)



Noise Characteristics (input: $t_r = t_f = 3 \text{ ns}$)

Characteristics	Course In a I	Test Condition	Ta=	l lmi4		
	Symbol		Vcc (V)	Тур.	Max	Unit
Quiet output maximum dynamic V _{OL}	V _{OLP}	C _L = 50 pF	5.0	0.3	8.0	V
Quiet output minimum dynamic V _{OL}	V _{OLV}	C _L = 50 pF	5.0	-0.3	-0.8	V
Minimum high level dynamic input voltage	V _{IHD}	C _L = 50 pF	5.0	_	3.5	V
Maximum low level dynamic input voltage	V _{ILD}	C _L = 50 pF	5.0	_	1.5	V

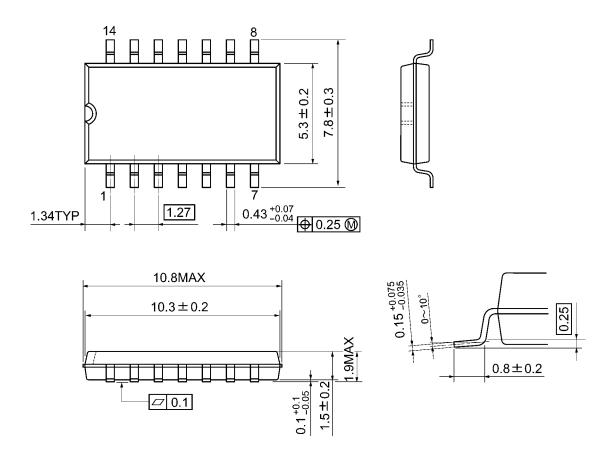
Input Equivalent Circuit





Package Dimensions

SOP14-P-300-1.27A Unit: mm

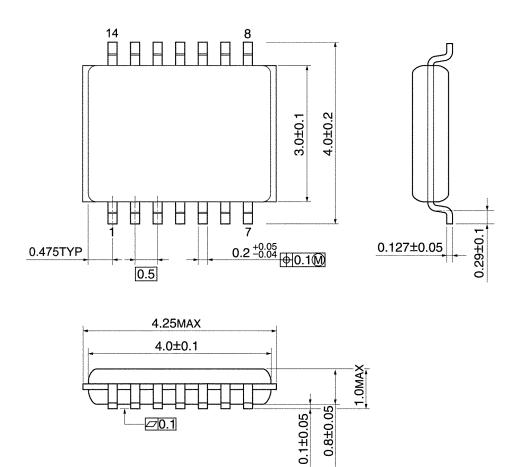


Weight: 0.18 g (typ.)



Package Dimensions

VSSOP14-P-0030-0.50 Unit: mm



Weight: 0.02 g (typ.)



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