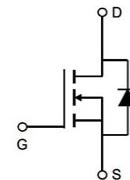
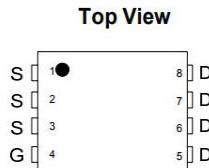
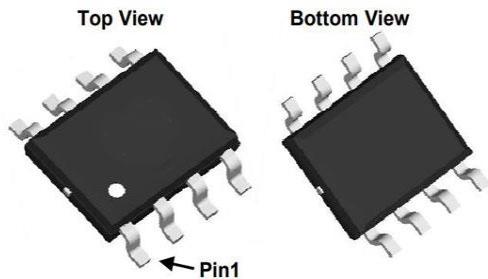


### General Description

40V /6A Complementary NP Power MOSFET  
 Very low on-resistance RDS(on) @ VGS=4.5 V  
 Pb-free lead plating; RoHS compliant

<b>V<sub>DS</sub></b>	40	V
<b>R<sub>D(on)</sub>, TYP@VGS=10V</b>	23.0	mΩ
<b>R<sub>D(on)</sub>, TYP@VGS=4.5</b>	35.2	mΩ
<b>I<sub>D</sub></b>	6	A



Part ID	Package Type	Marking	Tape and reel infomation
AC4614B	SOP8	4614	3000



100% UIS Tested  
 100% RG tested

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	V <sub>DS</sub>	40	V
Gate-Source Voltage	V <sub>GS</sub>	20	±V
Continuous Drain Current A	I <sub>D</sub>	6.0	A
		5.0	
Pulsed Drain Current B	I <sub>DM</sub>	9.6	
Avalanche Current G	I <sub>AR</sub>	1.9	
Repetitive avalanche energy L=0.1mH G	E <sub>AR</sub>	4.4	mJ
Power Dissipation A	P <sub>D</sub>	2	W
		1.28	
Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to 150	°C

### Thermal Characteristics

Parameter	Symbol	Typ	Max	Units
Maximum Junction-to-Ambient A	R <sub>θJA</sub>	95	142	°C/W
Maximum Junction-to-Ambient A		190	228	°C/W
Maximum Junction-to-Lead c	R <sub>θJL</sub>	57	91	°C/W

**STATIC PARAMETERS**

Symbol	Parameter	Conditions	Min	Typ	Max	Units
$BV_{DSS}$	Drain-Source Breakdown Voltage	$I_D = -250\mu A, V_{GS} = 0V$	40			V
$Id_{SS}$	Zero Gate Voltage Drain Current	$V_{DS}=40V, V_{GS}=0V$			1	uA
					5	
$I_{GSS}$	Gate-Body leakage current	$V_{DS} = 0V, V_{GS} = \pm 20V$			$\pm 100$	nA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu A$	1.5	2.3	3	V
$R_{DS(on)}$	Static Drain-Source On-Resistance	$V_{GS}=-10V, I_D=6A$		23.0	32.0	$m\Omega$
		$V_{GS}=4.5V, I_D=6A$		35.2	45.8	
$g_{FS}$	Forward Transconductance	$V_{DS}=5V, I_D=6A$		87		S
$V_{SD}$	Diode Forward Voltage	$I_S=1A, V_{GS}=22V$		0.72	1	V
$I_S$	Maximum Body-Diode Continuous Current				6	A

**DYNAMIC PARAMETERS**

Symbol	Parameter	Conditions	Min	Typ	Max	Units
$C_{iss}$	Input Capacitance	$V_{GS}=0V, V_{DS}=15V, f=1MHz$		516	629	pF
$C_{oss}$	Output Capacitance			82	100	pF
$C_{rss}$	Reverse Transfer Capacitance			43	51	pF
$R_g$	Gate resistance	$V_{GS}=0V, V_{DS}=0V, f=1MHz$			0.7	$\Omega$

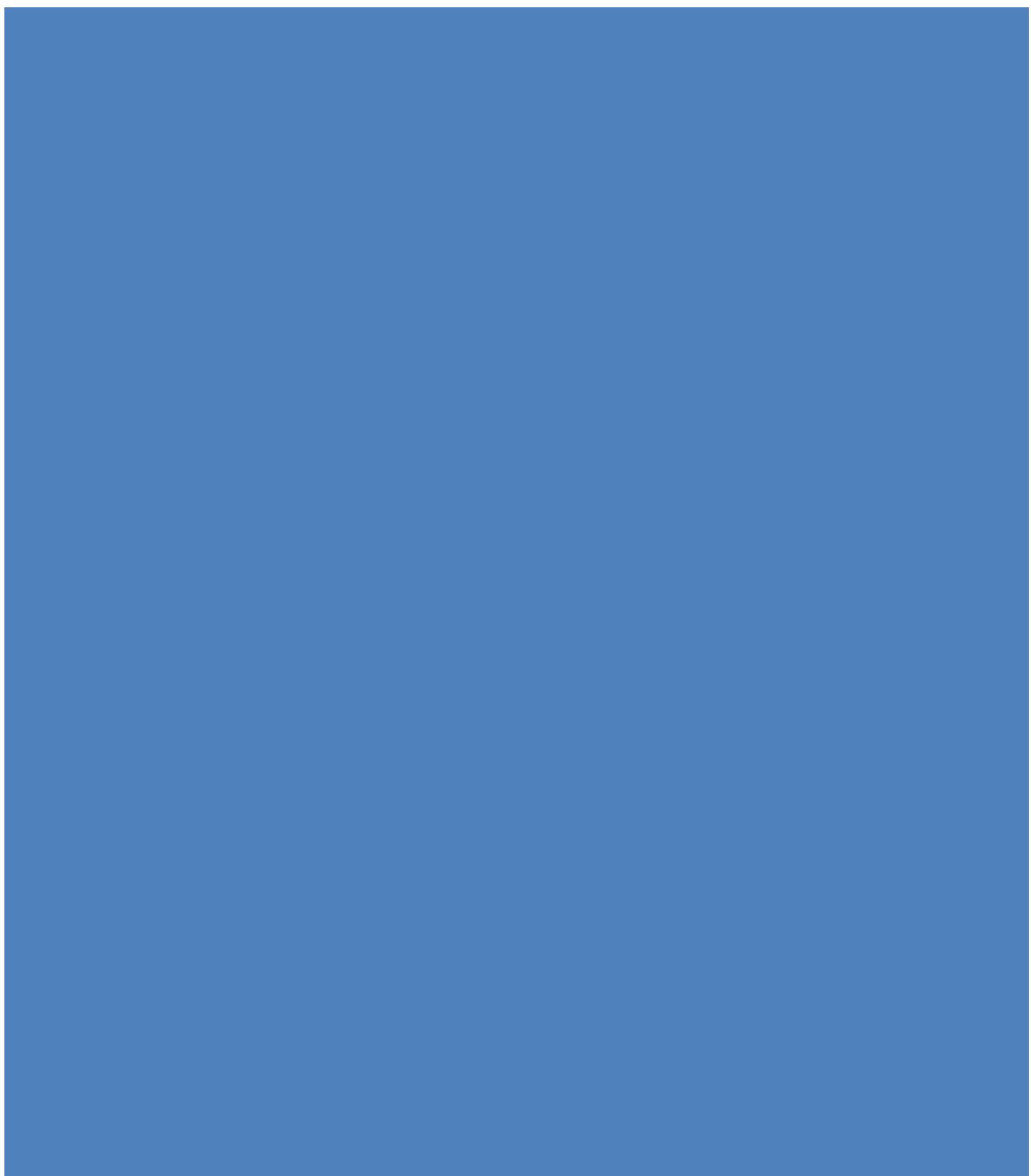
**SWITCHING PARAMETERS**

Symbol	Parameter	Conditions	Min	Typ	Max	Units
$Q_g(10V)$	Total Gate Charge	$V_{GS}=10V, V_{DS}=15V, I_D=6A$		4.3		nC
$Q_g 4.5V$	Total Gate Charge			2.15		
$Q_{gs}$	Gate Source Charge			0.98		
$Q_{gd}$	Gate Drain Charge			1.4		
$t_{D(on)}$	Turn-On Delay Time	$V_{GS}=10V, V_{DS}=15V, RL=0.75\Omega, R_{GEN}=3\Omega$		9		ns
$t_r$	Turn-On Rise Time			7.2		
$t_{D(off)}$	Turn-Off Delay Time			25.2		
$t_f$	Turn-Off Fall Time			8.1		
$t_{rr}$	Body Diode Reverse Recovery Time	$I_F=-8A, dI/dt=500A/\mu s$		18		ns
$Q_{rr}$	Body Diode Reverse Recovery Charge	$I_F=18A, dI/dt=500A/\mu s$		10		nC



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