

SS54FL THRU SS56FL

DESCRIPTION

Cathode

Anode

Surface Mount Schottky Barrier Rectifier

Reverse Voltage - 40 and 60V Forward Current - 5.0A

FEATURES

- Metal silicon junction, majority carrier conduction
- For surface mounted applications
- Low power loss, high efficiency
- High forward surge current capability
- For use in low voltage, high frequency inverters, fre

\mathbf{M}

- C
- T
- A

Absolute Maximum Ratings and Electrical characteristics

Ratings at 25 °C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz resistive or inductive load, for capacitive load, derate by 20 %

ree wheeling, and polarity protection applications	
MECHANICAL DATA	1 2
Case: SMAF	
Terminals: Solderable per MIL-STD-750, Method 2026	Simplified outline SMAF and symbol
Approx. Weight:27mg 0.00086oz	
hard to Markey and Dathers and Electrical should be seen at the	

PINNING

PIN

1

2

Parameter	Symbols	SS54FL	SS56FL	Units		
Maximum Repetitive Peak Reverse Voltage	V_{RRM}	40	60	V		
Maximum RMS voltage	V _{RMS}	28	42	٧		
Maximum DC Blocking Voltage	V _{DC}	40	60	٧		
Maximum Average Forward Rectified Current	I _{F(AV)}	5.0				
Peak Forward Surge Current,8.3ms Single Half Sine-wave Superimposed on Rated Load (JEDEC method)	I _{FSM}	150				
Max Instantaneous Forward Voltage at 5 A	V _F	0.45	0.55	٧		
Maximum DC Reverse Current T _a = 25°C at Rated DC Reverse Voltage T _a =100°C	I _R	1.0 50				
Typical Junction Capacitance 1)	Junction Capacitance 1) C _j 800 500		500	pF		
Typical Thermal Resistance 2)	Reja	55				
Operating Junction Temperature Range	Tj	-55 ~ +125				
Storage Temperature Range	T _{stg}	-55~+150				

¹⁾ Measured at 1MHz and applied reverse voltage of 4 V D.C.

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²⁾ P.C.B. mounted with 0.2 X 0.2" (5 X 5 mm) copper pad areas.



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Fig.1 Forward Current Derating Curve

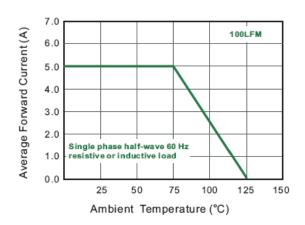


Fig.3 Typical Forward Characteristic

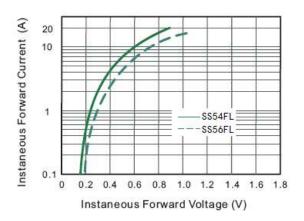


Fig.5 Maximum Non-Repetitive Peak Forward Surage Current

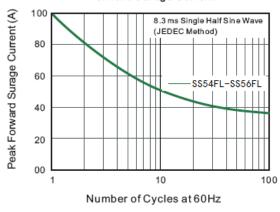


Fig.2 Typical Reverse Characteristics

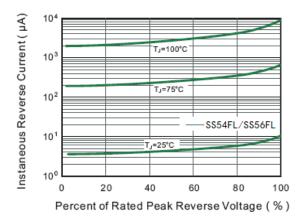


Fig.4 Typical Junction Capacitance

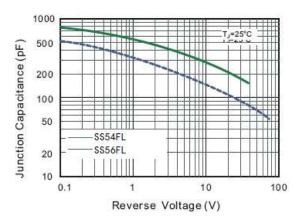
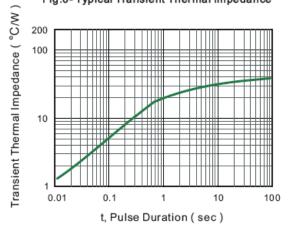


Fig.6-Typical Transient Thermal Impedance



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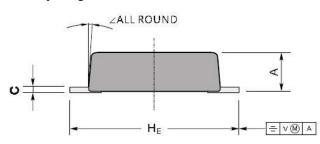
REV.07

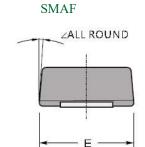


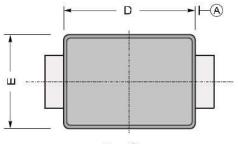
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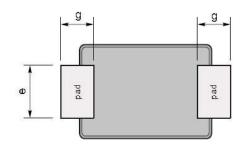
PACKAGE OUTLINE

Plastic surface mounted package; 2 leads







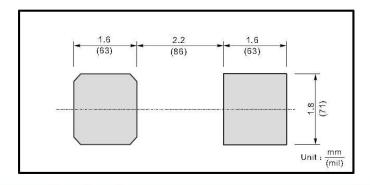


Top View

Bottom View

UNIT		Α	С	D	Е	е	g	HE	2
mm	max	1.1	0.20	3.7	2.7	1.6	1.2	4.9	7∘
	min	0.9	0.12	3.3	2.4	1.3	0.8	4.4	
mil	max	43	7.9	146	106	63	47	193	(
	min	35	4.7	130	94	51	31	173	

The recommended mounting pad size



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