

深圳市华锐达电子有限公司 产品承认书

SPECIFICATION FOR APPROVAL

编号: 202068Mon162522

客	户:						
客户制	斗号:		\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \				
料	号:	CMBZ060	3-601T04				
日	期:	2020/06/08					
	客	户承认栏	DRAWN	CHECKED	APPROVED		
AF	PPROVEI	FOR CUSTOMER	(制图)	(检查)	(确认)		
60,			许丽琳	甘元培	胡林立		

地 址:中国广东省深圳市宝安区石岩水田金凯进工业区B栋2楼

确认无误后敬请签回(Please Return After Approved)

电 话: 86 0755 29485625 公司主页: http://www.coil-rida.com

传 真: 86 0755 29485623 邮 箱: coilrida@coil-rida.com.cn



深圳市华锐达电子有限公司 SPECIFICATION FOR APPROVAL

CUSTOME (客户):				PAGE: 2 FO 9				
ITEM: CMBZC	0603-601T04			Customer's ITEM:				
变更次数	变更项目	变更	前内容	2	变更后内容	变更日期		
1								
2								
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CUSTOMER AF	PPROVED(客户确	认栏)	APPROVED	(确认)	CHECKED (检查)	DRAWN (制图)		
			胡林		甘元培	许丽琳		

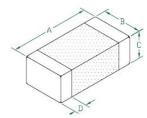


华锐达样品承认书

SPECIFICATION FOR APPROVAL

CUSTOMER (客户):	PAGE :
ITEM: CMBZ0603-601T04	Customer's ITEM:

1. EXTERNAL DIMENSIONS(外观尺寸):



A (mm)	B (mm)	C (mm)	D (mm)
1.6 ± 0.15	0.8 ± 0.15	0.8 ± 0.15	0.3 ± 0.2

2、ELECTRICAL CHRACTERISTICS (电特性要求):

ITEM (项目)	Specifications (规格值)	Typ. (参考值)	Testequipments (测试仪器)
L	$600 \Omega \pm 25\%$	600 Ω	HP4291B LCR METER
Test Freq.	100MHz 60mV		HP4291B LCR METER
DCR	0.5 oHM MAX	0.38 oHM	CH502A LCR METER
IDC	400mA		HP42841A
Ope. Temp.	-40°C TO 125°C		HC-D3M TEMP. &HUMIDITY CHAMBER

3、Product size specification reference table(产品尺寸规格对照表)

英寸制	毫米制	英寸制	毫米制				
0402	100505	1206	321611				
0603	160808	1206A	3216	313			
0805	201209	1210	322013				
0805T	201212	1812	453215				
1008	252012	1812A	451616				
1008A	252010	2220	5750	018			
CUSTOMER	APPROVED(客户确认栏)	APPROVED(确认)	CHECKED (检查)	DRAWN (制图)			
		胡林立	甘元培	许丽琳			
ISSUED DATE	ISSUED DATE (出版日期): 2020/06/08 版次: 1.0						

4. Reliability and Test Condition

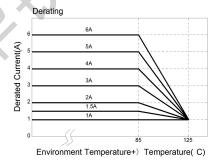
Item		Perform	ance						Test Con	dition		
Series No.	HLLB											
Operating	-40~+125℃		-40~+	+105℃								
Temperature	(Including self-temperat	ure rise)	(Including self-t	temperature	rise)							
Transportation Storage Temperature	-40~+125℃ (on board)			+105℃ coard)		For long	storag	e conditio	ons, please	see the	Application Notic	се
Impedance (Z)						Agilent4	291					
Inductance (Ls)						Agilent E						
Q Factor	Refer to standard electrical c	aractorictics I	liet			Agilent4 Agilent1						
DC Resistance	Refer to standard electrical c	iaracteristics i	iist			Agilent 4						
Rated Current						DC Pow Over Ra			uirements,	there will	be some risk	
Temperature Rise	Rated Current < 1A ΔT 20°C	Мах							C current.	Lourface		
Test	Rated Current ≧ 1A ΔT 40°C	Max				therm	nomete		u by uigita	Surface		
Resistance to Soldering Heat	Appearance: No damage. Impedance: within±15% of in Inductance: within±10% of it Q: Shall not exceed the sper RDC: within ±15% of initial visual visua	nitial value cification value		fication valu	ie	Solder to Flux for Tempera Dip time Depth: o	Sn99.5 empera lead fre ature ra a: 10±1 complet	%-Cu0.5° ture: 260 ee; Rosin. mp/imme sec. ely cover	±5℃ 9.5% ersion and the termin		n rate: 25±6 mm	ı/s
Solderability	More than 95% of the te electrode should be covered solder.	l with		al cooling 4∲1 second		Solder to Flux for Depth: o Dip time	Sn99.5' empera lead fre complet :: 4±1se	%-Cu0.59 ture: 245 ee: Rosin. ely cover ec.	±5 $℃$. 9.5 $\%$ the termin			
Terminal strength	Appearance: No damage. Impedance: within±15% of il Inductance: within±10% of il Q: Shall not exceed the spe RDC: within ±15% of initial v exceed the specificat	nitial value cification value alue and shall	DUT }	press tool	wide	J-STD-0 Compor <=0805: be appli	20D Cl ent m 0.5kg)t ied for	assification ounted of the side 60 +1 s	on Reflow on a PCI e of a device econds. A	Profiles) B apply be being the following the following the second in the following the	2 times.(IPC/JE a force (>080 ested. This force orce shall be ap- ing tested.)5:1kg e shall
Bending	Appearance: No damage. Impedance: within±10% of in Inductance: within±10% of in Q: Shall not exceed the spe RDC: within ±15% of initial v	nitial value cification value		fication valu	ie	following Bending	dimer	sions:>=	0.8mm	00x1.2mı	m	
Vibration Test	Appearance: No damage. Impedance: within±15% of in Inductance: within±10% of in Q: Shall not exceed the sper RDC: within ±15% of initial variations.	nitial value cification value		fication valu	ie	J-STD-0 Oscillation Equipment Total Am	020D Cl on Fred ent: V nplitude Time	assification of the control of the c	on Reflow $0{\sim}2{\rm K}{\sim}10$ checker $\pm10\%$	Profiles))Hz for 20	2 times.(IPC/JE 0 minutes 2 cycles each	
Shock	Appearance: No damage. Impedance: within±15% of in Inductance: within±10% of in Q: Shall not exceed the spe	nitial value cification value				Test con	Peak Value (g's)	Normal duration (D) (ms) 0.5	Wave form	Velocity change (Vi)ft/sec		
	RDC: within ±15% of initial v			fication valu	ie	Lead	100	6	Half-sine	12.3		
							1		1			

Item	Performance	Test Condition

Life test	Appearance: no damage. Impedance: within±15%of initial value. Inductance: within±10%of initial value. Q: Shall not exceed the specification value. RDC: within±15% of initial value and shall not exceed the specification value H TT	Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020D Classification Reflow Profiles) Temperature: $125\pm2^{\circ}$ C(bead), $85\pm2^{\circ}$ C(inductor) Applied current: rated current. Duration: 1000 ± 12 hrs. Measured at room temperature after placing for 24 ± 2 hrs.
Load Humidity		Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020D Classification Reflow Profiles) Humidity: $85\pm2\%$ R.H. Temperature: $85\pm2\%$. Duration: 1000hrs Min. with 100% rated current. Measured at room temperature after placing for 24 ± 2 hrs.
Thermal shock	Appearance: no damage. Impedance: within±15%of initial value. Inductance: within±10%of initial value. Q: Shall not exceed the specification value. RDC: within±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020D Classification Reflow Profiles) Condition for 1 cycle Step1: $-40\pm2^{\circ}\mathbb{C}$ 30 ± 5 min. Step2: $25\pm2^{\circ}\mathbb{C}$ ≤ 0.5 min Step3: $+105\pm2^{\circ}\mathbb{C}$ 30 ± 5 min. Number of cycles: 500 Measured at room temperature after placing for 24 ± 2 hrs.
Insulation Resistance	IR>1GΩ	Chip Inductor Only Test Voltage:100±10%V for 30Sec.

**Derating Curve

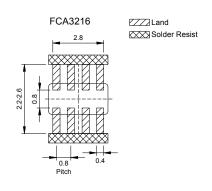
For the ferrite chip bead which withstanding current over 1.5A, as the operating temperature over 85 $^\circ\!\!\!\!\!\!^\circ$, the derating current information is necessary to consider with. For the detail derating of current, please refer to the Derated Current vs. Operating Temperature curve.

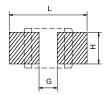


5. Soldering and Mounting

5-1. Recommended PC Board Pattern

	Chip Size							s For ering
Series	Type	A(mm)	B(mm)	C(mm)	D(mm)	L(mm)	G(mm)	H(mm)
	0603	0.6±0.03	0.30±0.03	0.30±	0.15±	0.80	0.30	0.30
	1005	1.0±0.10	0.50±0.10	0.50±	0.25±	1.50	0.40	0.55
HLLB	1608	1.6±0.15	0.80±0.15	0.80±	0.30±	2.60	0.60	0.80
GHB	2012	2.0±0.20	1.25±0.20	0.85±	0.50±	2.00	1.00	1.00
FCI	2012	2.0±0.20	1.25±0.20	1.25±	0.50±	3.00		
FHI	3216	3.2±0.20	1.60±0.20	1.10±	0.50±	4.40	2.20	1.40
FCH	3225	3.2±0.20	2.50±0.20	1.30±	0.50±	4.40	2.20	3.40
HCI	4516	4.5±0.20	1.60±0.20	1.60±	0.50±	5.70	2.70	1.40
	4532	4.5±0.20	3.20±0.20	1.50±	0.50±	5.90	2.57	4.22





PC board should be designed so that products can prevent damage from mechanical stress when warping the board.

5-2. Soldering

Mildly activated rosin fluxes are preferred. The terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools. Note.

If wave soldering is used ,there will be some risk.

Re-flow soldering temperatures below 240 degrees, there will be non-wetting risk

5-2.1 Lead Free Solder re-flow:

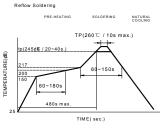
Recommended temperature profiles for lead free re-flow soldering in Figure 1. (Refered to J-STD-020C)

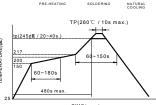
5-2.2 Soldering Iron:

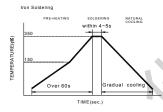
Products attachment with a soldering iron is discouraged due to the inherent process control limitations. If a soldering iron must be employed the following precautions are recommended. for Iron Soldering in Figure 2.

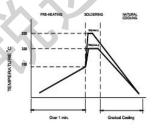
- \bullet Preheat circuit and products to 150 $^\circ\!\mathbb{C}$ 350°C tip temperature (max)
- · Never contact the ceramic with the iron tip • 1.0mm tip diameter (max)
- Use a 20 watt soldering iron with tip diameter of 1.0mm

· Limit soldering time to 4~5sec.









Reflow times: 3 times max

Iron Soldering times: 1 times max

Hand Soldering

5-2.3 Solder Volume

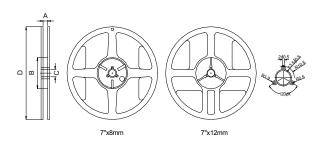
Accordingly increasing the solder volume, the mechanical stress to product is also increased. Exceeding solder volume may cause the failure of mechanical or electrical performance. Solder shall be used not to be exceed as shown in right side:

Minimum fillet height = soldering thickness + 25% product height



6.Packaging Information

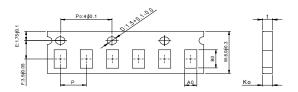
6-1. Reel Dimension



Туре	Type A(mm)		C(mm)	D(mm)	
7"x8mm	9.0±0.5	60±2	13.5±0.5	178±2	
7"x12mm	13.5±0.5	60±2	13.5±0.5	178±2	

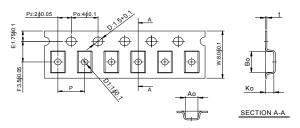
6-2.1 Tape Dimension / 8mm

■Material of taping is paper



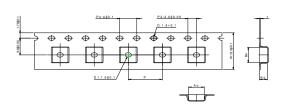
Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)
060303	0.68±	0.38±	0.50max	2.0±0.05	0.50max
100505	1.12±	0.62±	0.60±	2.0±0.05	0.60±

■Material of taping is plastic



Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)	D1(mm)
201212	2.10±	1.28±	1.28±	4.0±0.10	0.22±	1.0±0.10
321611	3.35±	1.75±	1.25±	4.0±0.10	0.23±	1.0±0.10
322513	3.42±	2.77±	1.55±	4.0±0.10	0.22±	1.0±0.10
321609	3.40±	1.77±	1.04±	4.0±0.10	0.22±	1.0±0.10

6-2.2 Tape Dimension / 12mm

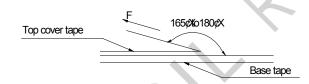


Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)	D1(mm)
451616	4.70±0.10	1.75±	1.75±0.10	4.0±0.1	0.24±0.05	1.5±0.1
453215	4.70±0.10	3.45±	1.60±0.10	8.0±0.1	0.24±0.05	1.5±0.1

6-3. Packaging Quantity

Chip Size	453215	451616	322513	321611	321609	201212	201209	160808	100505	060303
Chip / Reel	1000	1000	2000	4000	4000	4000	4000	4000	10000	15000
Inner box	5000	5000	14000	28000	28000	28000	28000	28000	70000	105000
Carton	20000	20000	56000	112000	112000	112000	112000	112000	280000	420000

6-4. Tearing Off Force



The force for tearing off cover tape is 15 to 60 grams in the arrow direction under the following conditions.

Room Temp.	Room Humidity	Room atm	Tearing Speed
(℃)	(%)	(hPa)	mm/min
5~35	45~85	860~1060	300

Application Notice

- Storage Conditions(component level)
 - To maintain the solder ability of terminal electrodes:
 - 1. TAI-TECH products meet IPC/JEDEC J-STD-020D standard-MSL, level 1.
 - 2. Temperature and humidity conditions: Less than 40 $^{\circ}\mathrm{C}$ and 60% RH.
 - 3. Recommended products should be used within 12 months from the time of delivery.
 - 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
 - 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
 - 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
 - $\ensuremath{\mathsf{3}}.$ Bulk handling should ensure that a brasion and mechanical shock are minimized.