

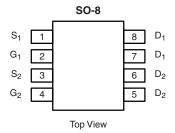
# **Dual N-Channel 20-V (D-S) MOSFET**

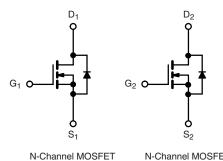
| PRODUCT SUMMARY     |                                  |                    |  |  |
|---------------------|----------------------------------|--------------------|--|--|
| V <sub>DS</sub> (V) | $R_{DS(on)}(\Omega)$             | I <sub>D</sub> (A) |  |  |
| 20                  | 0.025 at V <sub>GS</sub> = 4.5 V | 7.1                |  |  |
|                     | 0.035 at V <sub>GS</sub> = 2.5 V | 6.0                |  |  |

#### **FEATURES**

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- 100 %  $\rm R_{\rm g}$  Tested Compliant to RoHS Directive 2002/95/EC







| hannel | MOSFET | N-Channel MO | SEET |
|--------|--------|--------------|------|
| Harmer | MOSFEI | N-Charmer MO | SF⊑I |

| ABSOLUTE MAXIMUM RATINGS T <sub>A</sub> = 25 °C, unless otherwise noted |                        |                                   |             |      |  |
|---|------------------------|-----------------------------------|-------------|------|--|
| Parameter   |                        | Symbol                            | Limit       | Unit |  |
| Drain-Source Voltage  |                        | V <sub>DS</sub>                   | 20          | V    |  |
| Gate-Source Voltage   |                        | V <sub>GS</sub> ± 12              |             | ¬    |  |
| Continuous Drain Current /T 150 °C\8                                    | T <sub>A</sub> = 25 °C | I <sub>D</sub>                    | 7.1         | _    |  |
| Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>         | T <sub>A</sub> = 70 °C |                                   | 5.7         |      |  |
| Pulsed Drain Current (10 µs Pulse Width)                                |                        | I <sub>DM</sub>                   | 40          | Α    |  |
| Continuous Source Current (Diode Conduction) <sup>a</sup>               |                        | I <sub>S</sub>                    | 1.7         |      |  |
| Mariana Barra Biraira di ad   | T <sub>A</sub> = 25 °C | P <sub>D</sub>                    | 2           | W    |  |
| Maximum Power Dissipation <sup>a</sup>                                  | T <sub>A</sub> = 70 °C | ] ' D                             | 1.3         | ] vv |  |
| Operating Junction and Storage Temperature Range                        |                        | T <sub>J</sub> , T <sub>stg</sub> | - 55 to 150 | °C   |  |

| THERMAL RESISTANCE RATINGS               |                   |       |      |  |  |
|--|-------------------|-------|------|--|--|
| Parameter                                | Symbol            | Limit | Unit |  |  |
| Maximum Junction-to-Ambient <sup>a</sup> | R <sub>thJA</sub> | 62.5  | °C/W |  |  |

a. Surface Mounted on FR4 board,  $t \le 10 \text{ s.}$ 



| Parameter                                     | Symbol              | Test Conditions  | Min.  | Тур.  | Max.  | Unit |
|---|---------------------|--|---|-------|-------|------|
| Static  | •                   |  |   | •     |       |      |
| Gate Threshold Voltage                        | V <sub>GS(th)</sub> | $V_{DS} = V_{GS}, I_{D} = 250 \mu A$                                       | 0.6   |       | 1.5   | V    |
| Gate-Body Leakage                             | I <sub>GSS</sub>    | $V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$                          |   |       | ± 100 | nA   |
| Zara Cata Valta na Dunin Comunant             |                     | V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0 V                              | $V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}$ 1 |       | 1     | μΑ   |
| Zero Gate Voltage Drain Current               | I <sub>DSS</sub>    | $V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$ |   |       | 5     |      |
| On-State Drain Current <sup>a</sup>           | I <sub>D(on)</sub>  | $V_{DS} \ge 5 \text{ V}, V_{GS} = 4.5 \text{ V}$                           | 20  |       |       | Α    |
|   |                     | $V_{GS} = 4.5 \text{ V}, I_D = 7.1 \text{ A}$                              |   | 0.019 | 0.025 | 0    |
| Drain-Source On-State Resistance <sup>a</sup> | R <sub>DS(on)</sub> | $V_{GS} = 2.5 \text{ V}, I_D = 6.0 \text{ A}$ 0.026                        |   | 0.026 | 0.035 | Ω    |
| Forward Transconductance <sup>a</sup>         | 9 <sub>fs</sub>     | V <sub>DS</sub> = 10 V, I <sub>D</sub> = 7.1 A                             |   | 27    |       | S    |
| Diode Forward Voltage <sup>a</sup>            | $V_{SD}$            | I <sub>S</sub> = 1.7 A, V <sub>GS</sub> = 0 V                              |   |       | 1.2   | ٧    |
| Dynamic <sup>b</sup>                          |                     |  |   | •     |       |      |
| Total Gate Charge                             | Qg                  |  |   | 9.5   |       |      |
| Gate-Source Charge                            | $Q_{gs}$            | $V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 7.1 \text{ A}$       |   | 1.5   |       | nC   |
| Gate-Drain Charge                             | $Q_{gd}$            |  |   | 2.5   |       |      |
| Gate Resistance                               | $R_{g}$             | f = 1 MHz  |   | 1.6   | 2.7   | Ω    |
| Turn-On Delay Time                            | t <sub>d(on)</sub>  |  |   | 10    |       |      |
| Rise Time                                     | t <sub>r</sub>      | $V_{DD}$ = 10 V, $R_L$ = 10 $\Omega$                                       |   | 15    |       |      |
| Turn-Off Delay Time                           | t <sub>d(off)</sub> | $I_D\cong 1$ A, $V_{GEN}=4.5$ V, $R_g=10\Omega$                            |   | 38    |       | ns   |
| Fall Time                                     | t <sub>f</sub>      |  |   | 25    |       |      |
| Source-Drain Reverse Recovery Time            | t <sub>rr</sub>     | $I_F = 1.7 \text{ A}, dI/dt = 100 \text{ A}/\mu\text{s}$                   |   | 26    |       |      |

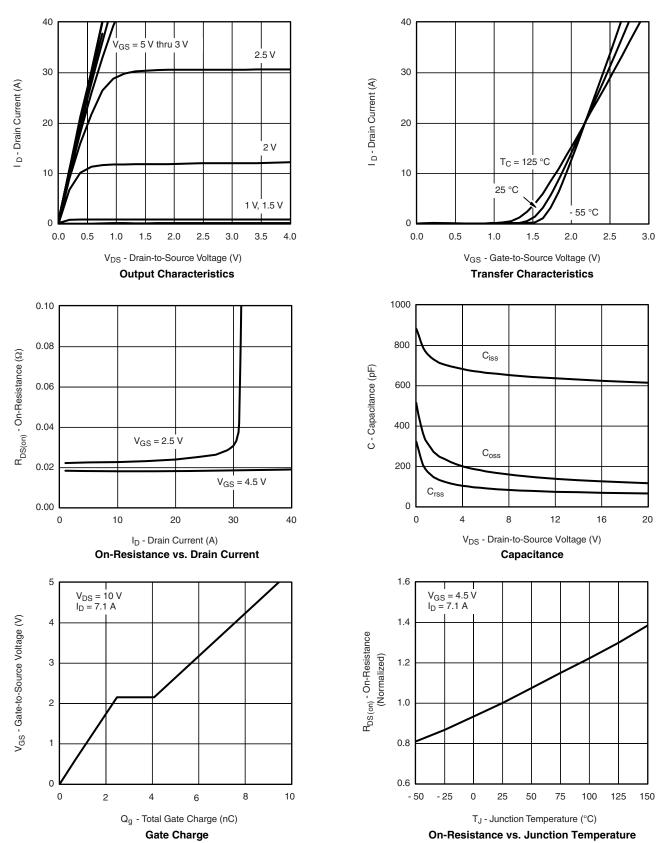
#### Notes:

- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

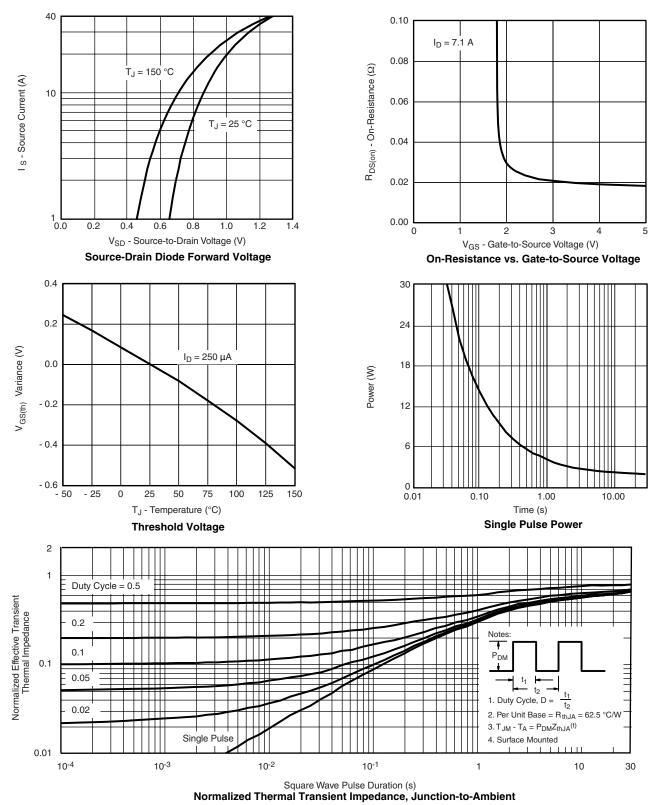


### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



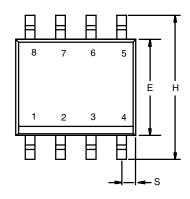


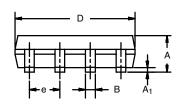
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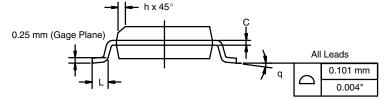




SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012







|                                | MILLIMETERS |      | INC       | HES   |  |
|--------------------------------|-------------|------|-----------|-------|--|
| DIM                            | Min         | Max  | Min       | Max   |  |
| Α                              | 1.35        | 1.75 | 0.053     | 0.069 |  |
| A <sub>1</sub>                 | 0.10        | 0.20 | 0.004     | 0.008 |  |
| В                              | 0.35        | 0.51 | 0.014     | 0.020 |  |
| С                              | 0.19        | 0.25 | 0.0075    | 0.010 |  |
| D                              | 4.80        | 5.00 | 0.189     | 0.196 |  |
| Е                              | 3.80        | 4.00 | 0.150     | 0.157 |  |
| е                              | 1.27 BSC    |      | 0.050 BSC |       |  |
| Н                              | 5.80        | 6.20 | 0.228     | 0.244 |  |
| h                              | 0.25        | 0.50 | 0.010     | 0.020 |  |
| L                              | 0.50        | 0.93 | 0.020     | 0.037 |  |
| q                              | 0°          | 8°   | 0°        | 8°    |  |
| S                              | 0.44        | 0.64 | 0.018     | 0.026 |  |
| ECN: C-06527-Rev. I. 11-Sep-06 |             |      |           |       |  |

DWG: 5498



### **RECOMMENDED MINIMUM PADS FOR SO-8**



Recommended Minimum Pads Dimensions in Inches/(mm)



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