Vishay Semiconductors





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SOT-227

FEATURES

- Two fully independent diodes
- Fully insulated package



- High voltage rectifiers optimized for very low COMPLIANT forward voltage drop
- · Industry standard outline
- UL approved file E78996
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION / APPLICATIONS

These devices are intended for use in main rectification. Single or three phase bridge.

PRIMARY CHARACTERISTICS							
I _{F(AV)} per module	220 A, T _C = 88 °C						
V _{FM} typical at 110 A	1.13 V						
Туре	Modules - diode, high voltage						
Package	SOT-227						
Circuit configuration	Two separate diodes, parallel pin-out						

MAJOR RATINGS AND CHARACTERISTICS								
SYMBOL	CHARACTERISTICS	VALUES	UNITS					
I _{F(AV)}	90 °C	108						
I _{F(RMS)}		173	A					
1	50 Hz	1170	A					
IFSM	60 Hz	1225						
l ² t	50 Hz	6840	A ² s					
1-1	60 Hz	6225	A-5					
l²√t		68 440	A²√s					
V _{RRM}		1200	V					
TJ		-55 to +150	°C					

ELECTRICAL SPECIFICATIONS

VOLTAGE RATINGS										
TYPE NUMBER	VOLTAGE CODE	V _{RRM,} MAXIMUM REPETITIVE PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} TYPICAL AT 150 °C mA						
VS-RA220FA120	120	1200	1300	1.0						

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FORWARD CONDUCTION							
PARAMETER	SYMBOL		TEST CON	VALUES	UNITS		
Maximum average forward current at case temperature per leg	I _{F(AV)}	180° condu	uction, half sine	wave, 90 °C	108	А	
Maximum RMS forward current per leg	I _{F(RMS)}	DC at 94 °C	C case tempera	ture	173		
		t = 10 ms	No voltage		1170		
Maximum peak, one-cycle forward,		t = 8.3 ms	reapplied		1225	А	
non-repetitive surge current per leg	IFSM	t = 10 ms	100 % V _{RRM}	Sinusoidal half wave, initial $T_J = T_J$ maximum	985		
		t = 8.3 ms	reapplied		1030	1	
		t = 10 ms	No voltage		6840	A ² s	
Maximum 12t for fusing par log	l ² t	t = 8.3 ms	reapplied		6225		
Maximum I ² t for fusing per leg		t = 10 ms	100 % V _{RRM}		4840		
		t = 8.3 ms	reapplied		4400		
Maximum I ² \sqrt{t} for fusing per leg	l²√t	t = 0.1 ms t	to 10 ms, no vo	Itage reapplied	68 440	A²√s	
Low level of threshold voltage per leg	V _{F(TO)1}	(16 7 0/ x -			0.75	V	
Low level value of forward slope resistance	r _{f1}	(IO.7 % X π	t x I _{F(AV)}), T _J = T	4.93	mΩ		
High level of threshold voltage per leg	V _{F(TO)2}	(1 × – × 1		0.84	V		
High level value of forward slope resistance	r _{f2}	$(I > \pi \times I_{F(A)})$	$_{0}$), T _J = T _J maxi	4.85	mΩ		
Movingung forward valtage drag per lag	V	I _{FM} = 110 A	∧, T _J = 25 °C		1.31	V	
Maximum forward voltage drop per leg	V _{FM}	I _{FM} = 110 A	∧, T _J = 150 °C		1.24	V	

BLOCKING				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum peak reverse leakage current	1	T _J = 25 °C	150	μA
per leg	IRRM	T _J = 150 °C	1.5	mA
RMS insulation voltage	V _{INS}	$T_J = 25 \text{ °C}$, any terminal to case, t = 1 minute	2500	V

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNITS			
Thermal resistance,	per leg	Б	-	-	0.2				
junction to case	per module	R _{thJC}	-	-	0.1	°C/W			
Thermal resistance, case to heatsink	per module	R _{thCS}	-	0.1	-				
Weight			-	30	-	g			
Mounting torque to terminal			-	-	1.1 (9.7)	Nm (lbf. in)			
Mounting torque to heatsink			-	-	1.8 (15.9)	Nm (lbf. in)			
Case style				SO	T-227				

DEVICE	w	SINE HALF	WAVE CO	NDUCTIO	N	RECTANGULAR WAVE CONDUCTION					UNITS
DEVICE	180°	120°	90°	60°	30°	180°	120°	90°	60°	30°	°C/W
VS-RA220FA120	0.06	0.037	0.082	0.116	0.188	0.039	0.066	0.087	0.121	0.19	C/ W

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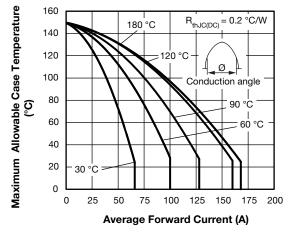


Fig. 1 - Current Ratings Characteristics (A)

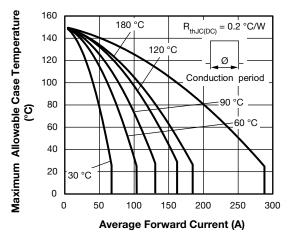


Fig. 2 - Current Ratings Characteristics (A)

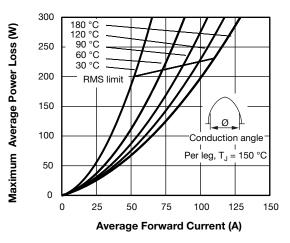
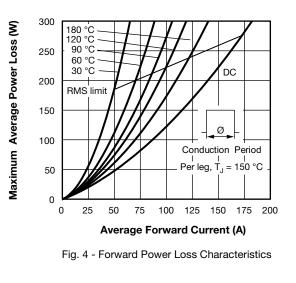


Fig. 3 - Forward Power Loss Characteristics

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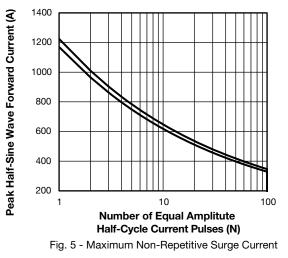




Fig. 6 - Maximum Non-Repetitive Surge Current

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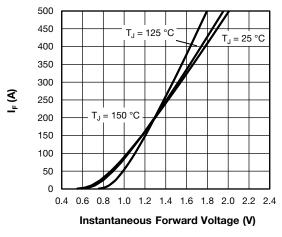


Fig. 7 - Typical Forward Voltage Characteristics

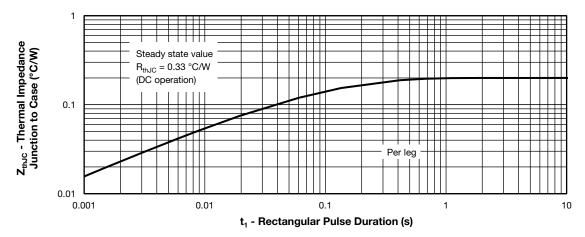


Fig. 8 - Thermal Impedance Z_{thJC} Characteristics

ORDERING INFORMATION TABLE

Device code	VS-	R	Α	220	F	Α	120	
	1	2	3	4	5	6	(7)	
	1 - 2 -		,	niconduc covery		oduct		
	3 -	Pre	Present silicon generation					
	4 -	Cur	rent rati	ng (220	= 220 A	()		
	5 -	Circ	uit conf	iguratior	n (2 sep	arate di	odes, p	arallel pin-o
	6 -	Pac	kage in	dicator (SOT-22	7 stand	lard insi	ulated base)
	7 -	Volt	age rati	ng (120	= 1200	V)		

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CIRCUIT CONFI	CIRCUIT CONFIGURATION									
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING								
Two separate diodes, parallel pin-out	F	Lead Assignment								

LINKS TO RELATED DOCUMENTS						
Dimensions www.vishay.com/doc?95423						
Packaging information	www.vishay.com/doc?95425					

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SOT-227 Generation 2

DIMENSIONS in millimeters (inches)



Note

• Controlling dimension: millimeter



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