

TFE20 Series Low ESR Tuning Fork Crystal

Features

- 32.7680kHz Frequency Reference
- Low ESR Tuning Fork Crystal Design, <50k Ohms
- Hermetic Ceramic Surface Mount Package
- Ideal for High Density Circuit Boards
- Frequency Tolerance, ±20ppm Standard
- Parabolic Temperature Coefficient
- Tape and Reel Packaging, EIA-418

Applications

- Real Time Clock Reference
- Low Power FPGAs & MCUs

Wearable Electronics

Healthcare Devices

- Data Loggers
- Smart Meters

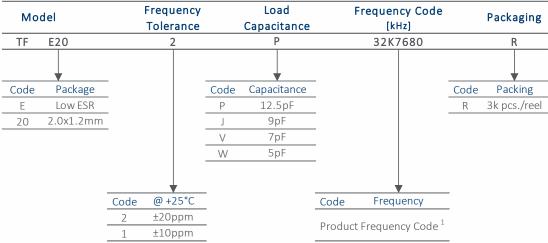
Description

CTS TFE20 Series is designed to pair with low power microcontrollers requiring a Real Time Clock reference with an ESR of 50k Ohms maximum. This series will support general commercial and industrial applications.

Battery Powered Applications

Portable Electronics

Ordering Information



Notes:

1] Frequency is recorded with two leading digits before the 'K' and 4 significant digits after the 'K' [including zeros].

Not all performance combinations and frequencies may be available. Contact your local CTS Representative or CTS Customer Service for availability.

This product is specified for use only in standard commercial applications. Supplier disclaims all express and implied warranties and liability in connection with any use of this product in any non-commercial applications or in any application that may expose the product to conditions that are outside of the tolerances provided in its specification.

DOC#008-0561-0 Rev. D

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Part Dimensions: 2.0 × 1.2 × 0.6mm • 4.5926mg



Electrical Specifications

Operating Conditions

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNIT
Operating Temperature	T _A	-	-40	+25	+85	°C
Turnover Temperature	T _M	-	+20	+25	+30	°C
Storage Temperature	T _{STG}	-	-55	-	+125	°C

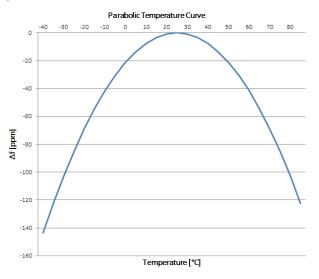
Frequency Stability

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNIT
Frequency	f _o	-	32.7680			kHz
Frequency Tolerance [Note 1]	$\Delta f/f_{O}$	Standard @ +25°C	-20	-	20	ppm
Parabolic Coefficient	ß	See Figure 1	-0.034 ±0.006			ppm/°C ²
Aging	$\Delta f/f_0$	First Year @ +25°C	-3	-	3	ppm

Crystal Parameters

-						
	-	Flexural Mode [Tuning Fork] -				
CL	Standard	-	12.5	-	pF	
Co	-	-	1.8	-	рF	
C ₁	-	-	9.5	-	fF	
R_1	-	-	-	50	KΩ	
DL	-	-	0.1	0.5	μW	
R _i	+100Vdc ±15Vdc	500	-	-	MΏ	
	C ₀ C ₁ R ₁ DL	Co - C1 - R1 - DL - Ri +100Vdc ±15Vdc	C ₀ - - C ₁ - - R ₁ - - DL - - R _i +100Vdc ±15Vdc 500	C ₀ - 1.8 C ₁ - - 9.5 R ₁ - - - DL - - 0.1 R _i +100Vdc ±15Vdc 500 -	C ₀ - 1.8 - C ₁ - 9.5 - R ₁ - - 50 DL - 0.1 0.5 R _i +100Vdc ±15Vdc 500 -	

Figure 1



Frequency Stability $\left[\Delta f\right]$ at a given temperature,

$\Delta f = \beta [T_A - T_M]^2$

 β = Parabolic Coefficient T_A = Ambient Temperature T_M = Turnover Temperature Ex. Find frequency stability at $T_A = +45$ °C $\Delta f = -0.034[45-25]^2$ $\Delta f = -0.034[20]^2$ $\Delta f = -13.6ppm$

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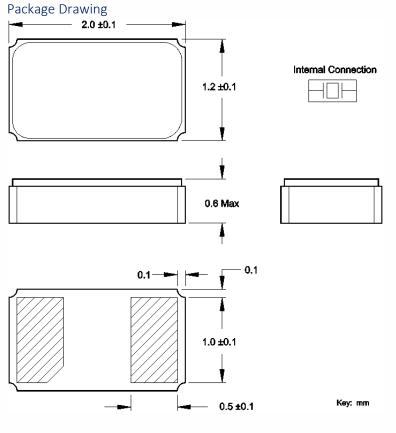
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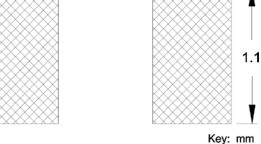


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Mechanical Specifications



0.6



0.8

Marking Information

Contact factory for marking formats that apply to this model series.

Notes

- 1. JEDEC termination code (e4). Barrier-plating is nickel [Ni] with gold [Au] flash plate.
- Reflow conditions per JEDEC J-STD-020; +260°C maximum, 20 seconds.
- 3. MSL = 1.
- 4. Due to the large world-wide production volumes for this model series, product variability may exist between production date codes, such as package coloring and product marking format. CTS guarantees form-fit-function performance to published data sheet parameters. Contact your local CTS Representative or CTS Customer Service with specific questions.

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Recommended Pad Layout

0.6

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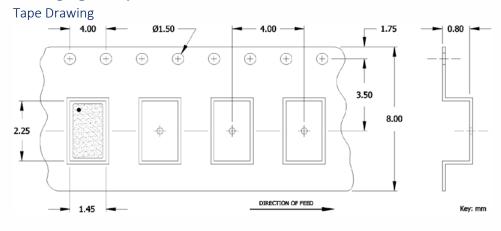
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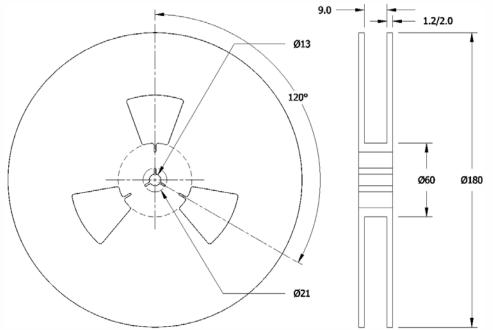


TFE20 Series Low ESR Tuning Fork Crystal

Packaging - Tape and Reel



Reel Drawing



Notes

- 1. Device quantity is 3k pieces maximum per 180mm reel.
- 2. Complete CTS part number, frequency value, date code and manufacturing site code information must appear on reel and carton labels.

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