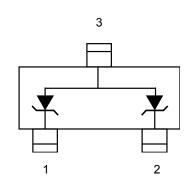


# **Low Capacitance ESD Protector**

### **Description**

The PESDUC523T5VU is a TVS array designed to protect I/O or data lines from the damaging effects of ESD. it is low capacitance transient voltage suppressors for high speed data interface that designed to protect sensitive electronics from damage or latch-up due to ESD lightning, and other voltage induced transient events. The SOT-523 is a very small package which allows space saving on high density printed circuit board and also gives the designer the flexibility to provide two I/O lines protection. All pins are rated to withstand 15kV ESD pulses using the IEC61000-4-2 air discharge method, which can meet the requirement of level 4.



#### **Feature**

- SOT-523 package
- Protect up two data lines
- Low clamping voltage
- Working voltage: 5V
- Low leakage current
- 125W peak pulse power(tp=8/20us)
- RoHS compliant transient protection for high speed data lines to IEC61000-4-2(ESD)±15kV(air),±8kV(contact)

### **Applications**

- > High-definition multimedia interface(HDMI)
- Mobile display digital interface(MDDI)
- RF/Antenna circuits
- USB 2.0&firewire ports
- HBT power amp protection
- Transceiver protection

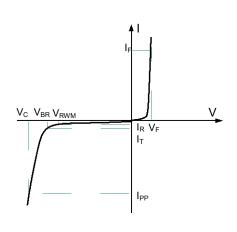
#### **Mechanical Characteristics**

- Lead finish:100% matte Sn(Tin)
- Mounting position: Any
- ➤ Qualified max reflow temperature:260 °C
- Device meets MSL 1 requirements
- ➤ Pure tin plating: 7 ~ 17 um
- Pin flatness:≤3mil

#### McChainear Onaracteristics

### **Electronics Parameter**

Symbol	Parameter	
$V_{RWM}$	Peak Reverse Working Voltage	
I <sub>R</sub>	Reverse Leakage Current @ V <sub>RWM</sub>	
$V_{BR}$	Breakdown Voltage @ I <sub>T</sub>	
I <sub>T</sub>	Test Current	
I <sub>PP</sub>	Maximum Reverse Peak Pulse Curren	
V <sub>C</sub>	Clamping Voltage @ I <sub>PP</sub>	
P <sub>PP</sub>	Peak Pulse Power	
CJ	Junction Capacitance	
I <sub>F</sub>	Forward Current	
V <sub>F</sub>	Forward Voltage @ I <sub>F</sub>	



## Electrical characteristics per line@25℃ (unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Reverse Stand-off Voltage	$V_{RWM}$				5	V
Reverse Breakdown Voltage	$V_{BR}$	I <sub>t</sub> = 1mA	5.6			V
Reverse Leakage Current	I <sub>R</sub>	V <sub>RWM</sub> = 5V			1	μA
Clamping Voltage	V <sub>C</sub>	I <sub>PP</sub> = 1A, t <sub>P</sub> = 8/20μs pin1 to pin2			13.5	V
Clamping Voltage	V <sub>C</sub>	$I_{PP}$ = 5A, $t_P$ = 8/20 $\mu$ s pin1or pin2 to pin3			20.0	V
Clamping Voltage	Vc	I <sub>PP</sub> = 5A, t <sub>P</sub> = 8/20µs pin1to pin2			23.0	V
Junction Capacitance	C <sub>j</sub>	V <sub>R</sub> =0V, f = 1MHz Pin1 to Pin2		0.35	0.45	pF
Junction Capacitance	C <sub>j</sub>	V <sub>R</sub> =0Vf = 1MHz pin1or pin2 to pin3		0.7	0.9	pF

## Absolute maximum rating@25℃

Rating	Symbol	Value	Units
Peak Pulse Power (t <sub>p</sub> =8/20µs)	P <sub>pp</sub>	125	W
Peak Pulse Power (t <sub>p</sub> =8/20μs)	I <sub>pp</sub>	5.5	А
Operating Temperature	TJ	-55 to +150	$^{\circ}$
Storage Temperature	T <sub>STG</sub>	-55 to +150	°C

# **Typical Characteristics**

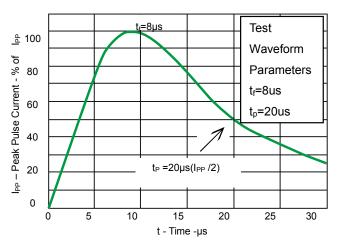


Fig 1.Pulse Waveform

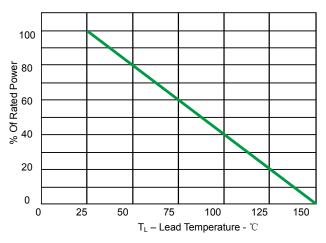


Fig 2.Power Derating Curve

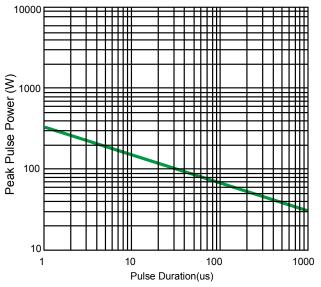


Fig 3. Non Repetitive Peak Pulse Power vs. Pulse time

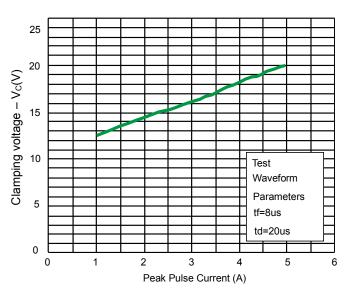


Fig 4. Clamping Voltage vs. Peak Pulse Current (Pin 1 to Pin 2)

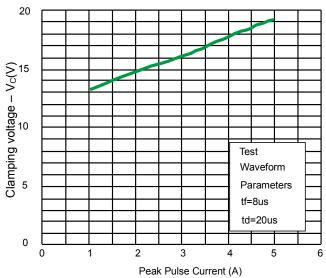
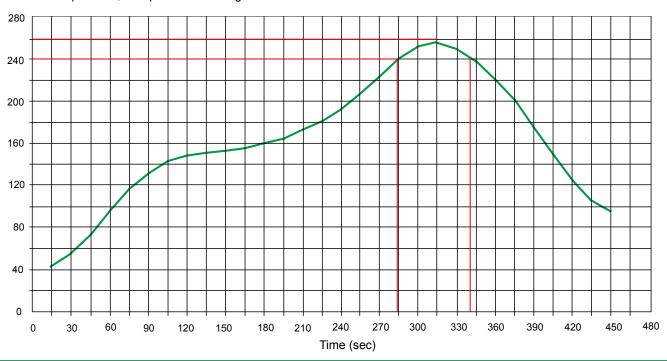


Fig.5 Clamping Voltage vs. Peak Pulse Current (Pin 1 or Pin 2 to Pin 3)

### **Solder Reflow Recommendation**

Peak Temp=257°C, Ramp Rate=0.802deg. °C/sec

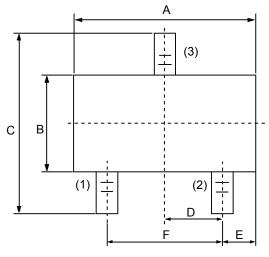


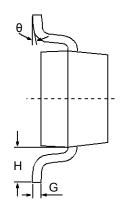
### **PCB Design**

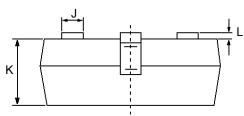
For TVS diodes a low-ohmic and low-inductive path to chassis earth is absolutely mandatory in order to achieve good ESD protection. Novices in the area of ESD protection should take following suggestions to heart:

- > Do not use stubs, but place the cathode of the TVS diode directly on the signal trace.
- Do not make false economies and save copper for the ground connection.
- Place via holes to ground as close as possible to the anode of the TVS diode.
- > Use as many via holes as possible for the ground connection.
- > Keep the length of via holes in mind! The longer the more inductance they will have.

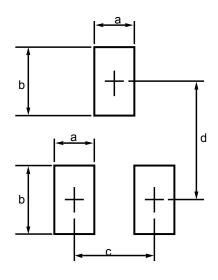
## **Product dimension (SOT-523)**







Dim	Millimeters		Inches		
Dim	MIN	MAX	MIN	MAX	
А	1.50	1.70	0.059	0.067	
В	0.75	0.85	0.030	0.033	
С	1.450	1.750	0.057	0.069	
D	0.50BSC		0.020BSC		
Е	0.30	0.33	0.012	0.015	
F	0.900	1.100	0.035	0.043	
G	0.100	0.200	0.004	0.008	
Н	0.550		0.0	)22	
J	0.150	0.250	0.006	0.010	
К	0.700	0.900	0.028	0.038	
L	0.024	0.027	0.600	0.700	
θ	0°	4°	0°	4°	



Dim	Millimeters		
	MIN	MAX	
а		0.4	
b		0.6	
С		1.0	
d		1.24	

# Ordering information

Device	Package	Shipping
PESDUC523T5VU	SOT-523 (Pb-Free)	3000 / Tape & Reel

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