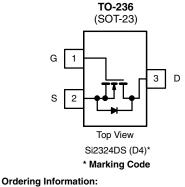


Vishay Siliconix

N-Channel 100 V (D-S) MOSFET

MOSFET PRODUCT SUMMARY					
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A) ^a	Q _g (Typ.)		
	0.234 at V_{GS} = 10 V	2.3			
100	0.267 at V_{GS} = 6 V	2.1	2.9 nC		
	0.278 at V _{GS} = 4.5 V	1.7			



Si2324DS-T1-GE3 (Lead (Pb)-free and Halogen-free)

FEATURES

- TrenchFET[®] Power MOSFET
- 100 % R_g Tested 100 % UIS Tested
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

- **DC/DC** Converters •
- Load Switch
- LED Backlighting in LCD TVs

ABSOLUTE MAXIMUM RATINGS $(T_A = 2)$	25 °C, unless oth	nerwise noted)		
Parameter		Symbol	Limit	Unit
Drain-Source Voltage Gate-Source Voltage		V _{DS}	100	V
		V _{GS}	± 20	
	T _C = 25 °C		2.3	
Continuous Drain Current ($T_1 = 150 \ ^{\circ}C$)	T _C = 70 °C	1-	1.8	
	T _A = 25 °C	I _D	1.6 ^{b, c}	
	T _A = 70 °C		1.3 ^{b, c}	А
Pulsed Drain Current (t = 300 μs)		I _{DM}	5	- A
Continuous Source-Drain Diode Current	T _C = 25 °C	la la	2.1	
Continuous Source-Drain Diode Current	T _A = 25 °C	I _S	1.0 ^{b, c}	1
L = 0.1 mH		I _{AS}	5	
Single Pulse Avalanche Energy		E _{AS}	1.25	mJ
	T _C = 25 °C	P _D	2.5	
Maximum Power Dissipation	T _C = 70 °C		1.6	w
	T _A = 25 °C		1.25 ^{b, c}	vv
	T _A = 70 °C		0.8 ^{b, c}	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C

Typical	Maximum	Unit	
	maximam	Unit	
75	100	°C/W	
40	50	0,00	
_			

Notes:

a. Based on T_C = 25 °C.

b. Surface mounted on 1" x 1" FR4 board.

c. t = 5 s.

d. Maximum under steady state conditions is 166 °C/W.

Document Number: 67691 S12-1140-Rev. B, 21-May-12

For technical questions, contact: pmostechsupport@vishay.com

www.vishay.com

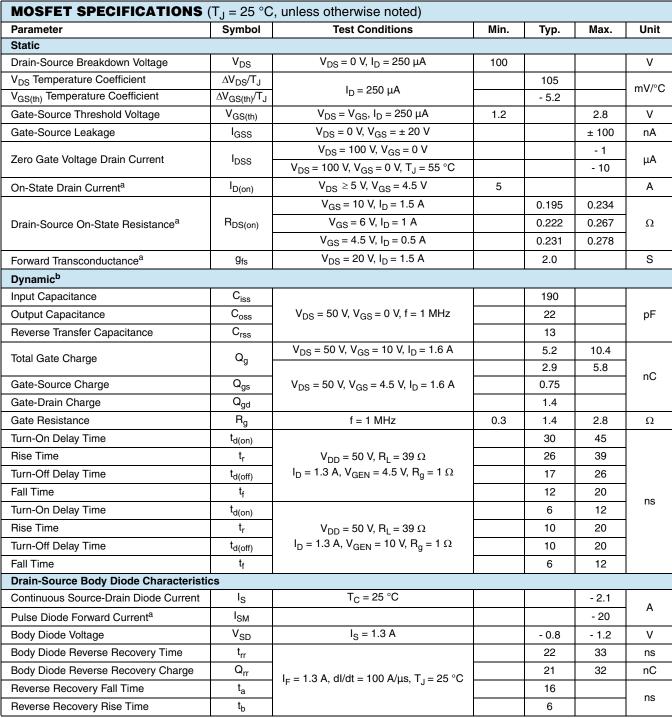
1

RoHS COMPLIANT

HALOGEN FREE

Si2324DS

Vishay Siliconix



Notes:

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

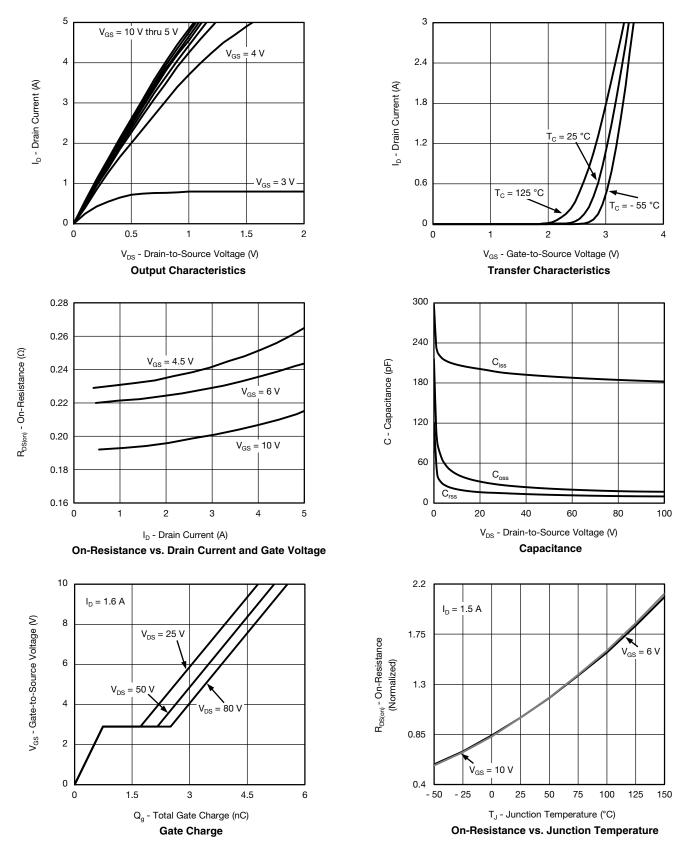
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Si2324DS

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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



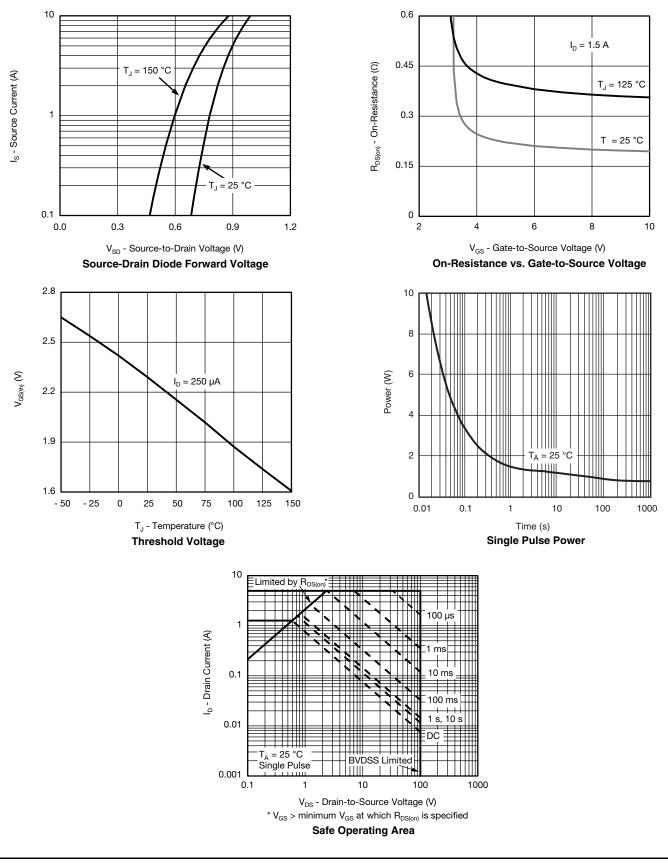
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Si2324DS

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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

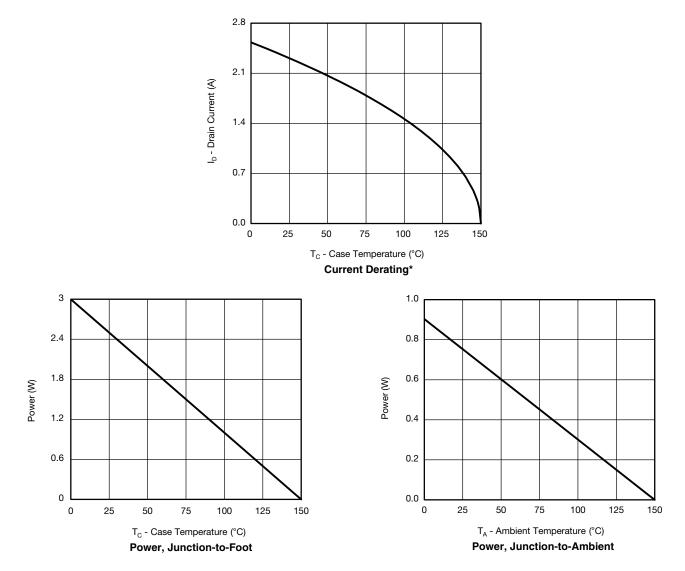


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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

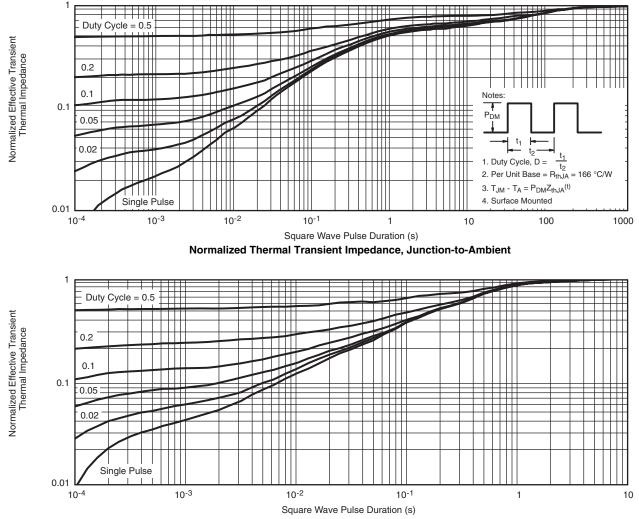


* The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?67691.

www.vishay.com 6 For technical questions, contact: pmostechsupport@vishay.com

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Package Information

Vishay Siliconix

SOT-23 (TO-236): 3-LEAD







Dim	MILLIMETERS		INCHES		
	Min	Max	Min	Мах	
Α	0.89	1.12	0.035	0.044	
A ₁	0.01	0.10	0.0004	0.004	
A ₂	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
С	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E ₁	1.20	1.40	0.047	0.055	
е	0.95 BSC		0.0374 Ref		
e ₁	1.90 BSC		0.0748 Ref		
L	0.40	0.60	0.016	0.024	
L ₁	0.64 Ref		0.025 Ref		
S	0.50 Ref		0.020 Ref		
q	3°	8°	3°	8°	



Application Note 826

Vishay Siliconix

RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads Dimensions in Inches/(mm)

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