MMBFJ175LT1G

JFET Chopper

P-Channel - Depletion

Features

- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Drain-Gate Voltage	V_{DG}	25	V
Reverse Gate-Source Voltage	V _{GS(r)}	-25	V

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board, (Note 1) T _A = 25°C Derate above 25°C	P _D	225 1.8	mW mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	556	°C/W
Junction and Storage Temperature	T _J , T _{stg}	-55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. $FR-5 = 1.0 \times 0.75 \times 0.062$ in.

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Gate – Source Breakdown Voltage $(V_{DS} = 0, I_D = 1.0 \mu A)$	V _{(BR)GSS}	30	_	٧
Gate Reverse Current (V _{DS} = 0 V, V _{GS} = 20 V)	I _{GSS}	_	1.0	nA
Gate – Source Cutoff Voltage (V _{DS} = 15, I _D = 10 nA)	V _{GS(OFF)}	3.0	6.0	V

ON CHARACTERISTICS

Zero Gate-Voltage [(V _{GS} = 0, V _{DS} =	I _{DSS}	7.0	60	mA	
Drain Cutoff Current (V _{DS} = 15 V, V _{GS}	I _{D(off)}	-	1.0	nA	
Drain Source On Re (I _D = 500 μA)	r _{DS(on)}	_	125	Ω	
Input Capacitance	V 0 V 10V	C _{iss}	-	11	
Reverse Transfer Capacitance	V _{DS} = 0, V _{GS} = 10V f = 1.0 MHz	C _{rss}	_	5.5	pF

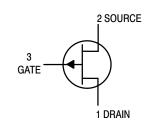
Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

2. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%.



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SOT-23 (TO-236) CASE 318 STYLE 10

MARKING DIAGRAM



6W = Device Code
M = Date Code*

Pb-Free Package

(Note: Microdot may be in either location)

*Date Code orientation and/or overbar may vary depending upon manufacturing location.

ORDERING INFORMATION

Device	Package	Shipping [†]
MMBFJ175LT1G	SOT-23 (Pb-Free)	3000 / Tape & Reel
SMMBFJ175LT1G	SOT-23 (Pb-Free)	3000 / Tape & Reel

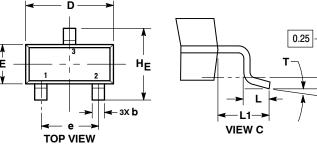
†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

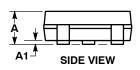


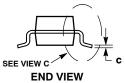
SOT-23 (TO-236) CASE 318-08 **ISSUE AS**

DATE 30 JAN 2018

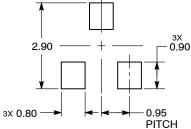
SCALE 4:1 D







RECOMMENDED SOLDERING FOOTPRINT



DIMENSIONS: MILLIMETERS

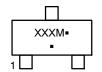
NOTES:

- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
 2. CONTROLLING DIMENSION: MILLIMETERS.
 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH.
 MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE BASE MATERIAL
- 4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH,

PROTRUSIONS, OR GATE BURRS.

	M	MILLIMETERS			INCHES	
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.89	1.00	1.11	0.035	0.039	0.044
A1	0.01	0.06	0.10	0.000	0.002	0.004
b	0.37	0.44	0.50	0.015	0.017	0.020
С	0.08	0.14	0.20	0.003	0.006	0.008
D	2.80	2.90	3.04	0.110	0.114	0.120
E	1.20	1.30	1.40	0.047	0.051	0.055
е	1.78	1.90	2.04	0.070	0.075	0.080
L	0.30	0.43	0.55	0.012	0.017	0.022
L1	0.35	0.54	0.69	0.014	0.021	0.027
HE	2.10	2.40	2.64	0.083	0.094	0.104
Т	0°		10°	0°		10°

GENERIC MARKING DIAGRAM*



XXX = Specific Device Code

= Date Code

= Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

STYLE 1 THRU 5:	STYLE 6:	STYLE 7:	STYLE 8:
CANCELLED	PIN 1. BASE	PIN 1. EMITTER	PIN 1. ANODE
	2. EMITTER	2. BASE	NO CONNECTION
	COLLECTOR	COLLECTOR	CATHODE

SOT-23 (TO-236)

STYLE 9	:	STYLE 10:	STYLE 11:	STYLE 12:	STYLE 13:	STYLE 14:
PIN 1.	ANODE	PIN 1. DRAIN	PIN 1. ANODE	PIN 1. CATHODE	PIN 1. SOURCE	PIN 1. CATHODE
2.	ANODE	SOURCE	CATHODE	CATHODE	2. DRAIN	2. GATE
3.	CATHODE	GATE	CATHODE-ANODE	3. ANODE	3. GATE	ANODE

STYLE 15:	STYLE 16:	STYLE 17:	STYLE 18:	STYLE 19:	STYLE 20:
PIN 1. GATE	PIN 1. ANODE	PIN 1. NO CONNECTION	PIN 1. NO CONNECTION	PIN 1. CATHODE	PIN 1. CATHODE
CATHODE	CATHODE	2. ANODE	CATHODE	2. ANODE	ANODE
ANODE	CATHODE	CATHODE	ANODE	CATHODE-ANOD	E 3. GATE

STYLE 21:	STYLE 22:	STYLE 23:	STYLE 24:	STYLE 25:	STYLE 26:
PIN 1. GATE	PIN 1. RETURN	PIN 1. ANODE	PIN 1. GATE	PIN 1. ANODE	PIN 1. CATHODE
SOURCE	OUTPUT	2. ANODE	2. DRAIN	2. CATHODE	2. ANODE
3 DDAIN	2 INDLIT	3 CATHODE	3 SOURCE	3 GATE	3 NO CONNECTION

	/LE 28: N 1. ANODE 2. ANODE 3. ANODE	
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