



General Description

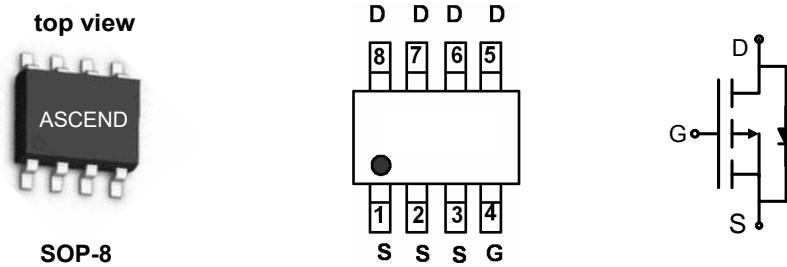
- Trench Power LV MOSFET technology
- High density cell design for Low $R_{DS(ON)}$
- High Speed switching
- $V_{GS} > \pm 25V$

Applications

- Battery protection
- Load switch
- Power management

Product Summary

V_{DS}	-30	V
$R_{DS(on),TYP} @ V_{GS}=10V$	8.8	$m\Omega$
I_D	-14	A



Absolute Maximum Ratings $T_A = 25^\circ C$, unless otherwise noted

Parameter	Symbol	Values	Unit
Drain-Source voltage($V_{GS}=0V$)	V_{DS}	-30	V
Continuous Drain Current ²⁾	I_D	-14	A
$T_A = 100^\circ C$	I_D	-8.8	
Pulsed Drain Current ³⁾	$I_{D,pulse}$	-56	A
Gate-Source Voltage	V_{GSS}	$\pm 25V$	V
Single Pulse Avalanche Energy	E_{AS}	135	mJ
Power Dissipation	P_D	2.9	W
Continuous Diode Forward Current	I_S	-4	A
Diode Forward Voltage	V_{SD}	-1.2	V
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55~+150	$^\circ C$

Electrical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
Static Characteristics						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = -250\mu\text{A}$	-30	--	--	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}} = -30\text{V}$ $V_{\text{GS}} = 0\text{V}, T_J = 25^\circ\text{C}$	--	--	-1	μA
		$V_{\text{DS}} = -24\text{V}$, $V_{\text{GS}} = 0\text{V}, T_J = 125^\circ\text{C}$	--	--	-100	
Gate-Source Leakage Current	I_{GSS}	$V_{\text{GS}} = \pm 25\text{V}$	--	--	± 100	nA
Gate-Source Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = -250\mu\text{A}$	-1.2	-1.7	-2.3	V
Drain-Source On-State-Resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = -20\text{V}, I_D = -14\text{A}$	--	7.9	10.5	$\text{m}\Omega$
		$V_{\text{GS}} = -10\text{V}, I_D = -14\text{A}$	--	8.8	11.5	$\text{m}\Omega$
		$V_{\text{GS}} = -6\text{V}, I_D = -10\text{A}$	--	10	13	$\text{m}\Omega$
		$V_{\text{GS}} = -4.5\text{V}, I_D = -10\text{A}$	--	11.8	16	$\text{m}\Omega$
Gate Resistance	R_G	$f = 1.0\text{MHz}$ open drain	--	3.7	--	Ω
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{\text{GS}} = 0\text{V},$ $V_{\text{DS}} = -15\text{V}$ $f = 1.0\text{MHz}$	--	3351	--	pF
Output Capacitance	C_{oss}		--	327.6	--	
Reverse Transfer Capacitance	C_{rss}		--	285	--	
Total Gate Charge	Q_g	$V_{\text{DS}} = -15\text{V}, I_D = -14\text{A}$ $V_{\text{GS}} = -10\text{V}$	--	61.9	--	nC
Gate-Source Charge	Q_{gs}		--	9.85	--	
Gate-Drain Charge	Q_{gd}		--	11.5	--	
Gate Plateau Voltage	V_{Plateau}		--	2.9	--	V
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DS}} = -15\text{V}, V_{\text{GS}} = -10\text{V}$ $R_G = 3\Omega$	--	12	--	ns
Turn-on Rise Time	t_r		--	7	--	
Turn-off Delay Time	$t_{\text{d}(\text{off})}$		--	53	--	
Turn-off Fall Time	t_f		--	16.5	--	
Drain-Source Body Diode Characteristics						
Body Diode Forward Voltage	V_{SD}	$T_J = 25^\circ\text{C}, I_{\text{SD}} = -14\text{A},$ $V_{\text{GS}} = 0\text{V}$	--	-0.7	-1.2	V
Reverse Recovery Time	t_{rr}	$I_F = -14\text{A}, di_F/dt = 500\text{A}/\mu\text{s}$	--	18	--	ns
Reverse Recovery Charge	Q_{rr}		--	32	--	μC

Typical Performance Characteristics

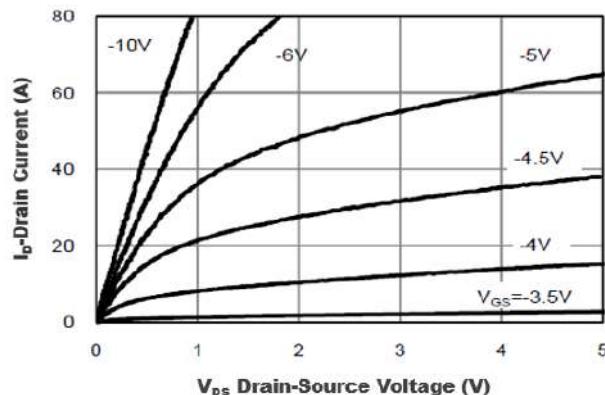


Figure1. Output Characteristics

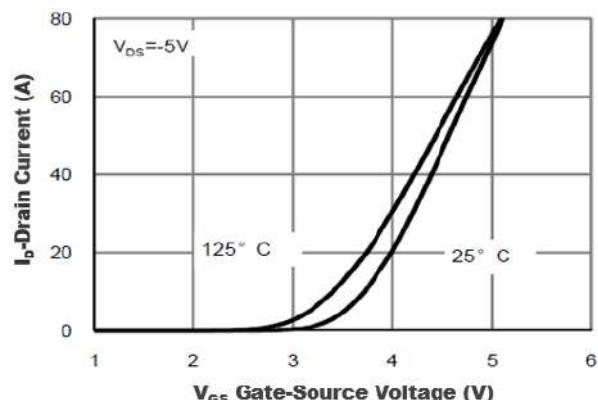


Figure2. Transfer Characteristics

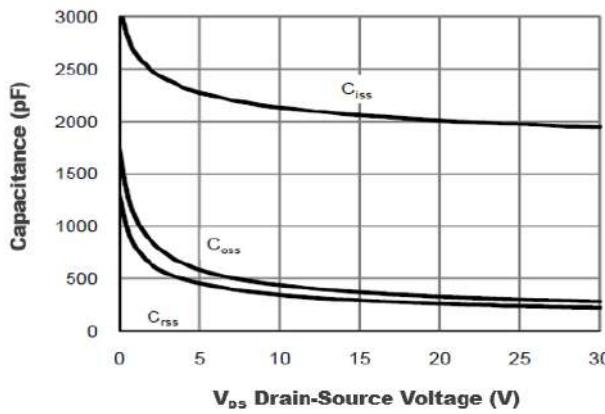


Figure3. Capacitance Characteristics

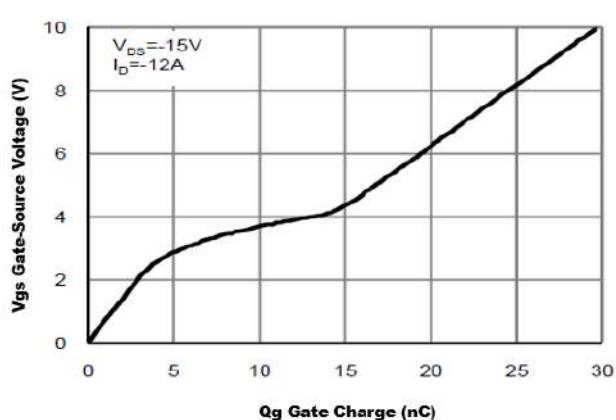


Figure4. Gate Charge

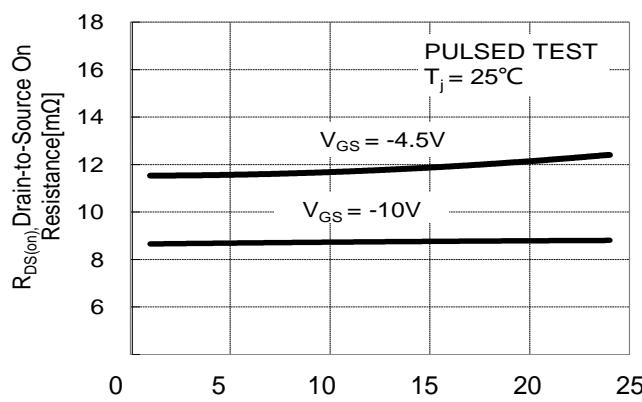


Figure5. Drain-Source on Resistance

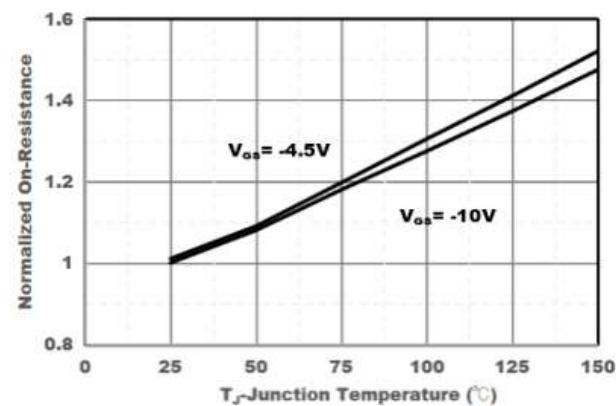


Figure6. Drain-Source on Resistance

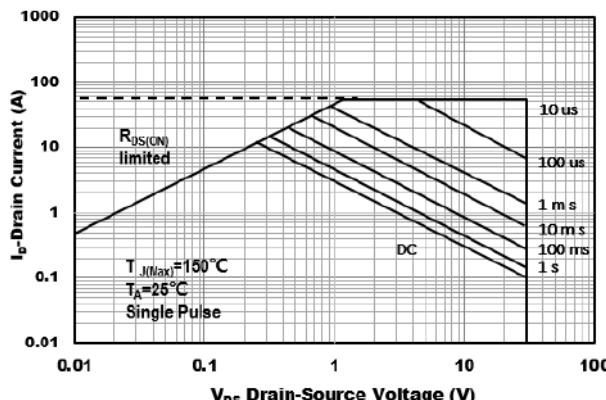


Figure7. Safe Operation Area

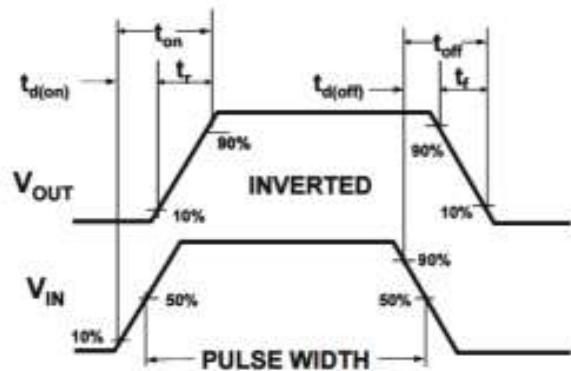
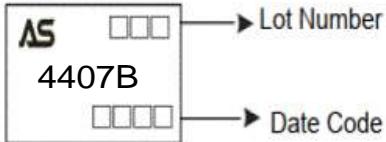
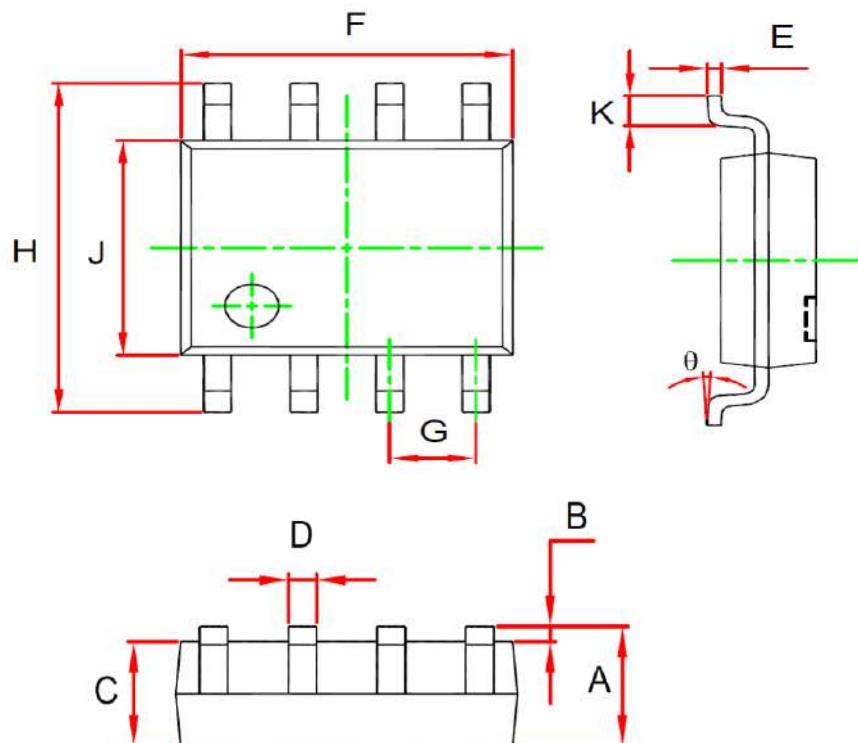


Figure8. Switching wave

Ordering and Marking Information

Ordering Device No.	Marking	Package	Packing	Quantity
ASDM4407BS-R	4407B	SOP-8	Tape&Reel	4000/Reel

PACKAGE	MARKING
SOP-8	

SOP-8 Package information


DIM	DIMENSIONS				
	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	0.053	0.069	1.350	1.750	
B	0.004	0.010	0.100	0.250	
C	0.053	0.061	1.350	1.550	
D	0.013	0.020	0.330	0.510	
E	0.007	0.010	0.170	0.250	
F	0.189	0.197	4.800	5.000	
G	0.050 (BSC)		1.270 (BSC)		
H	0.228	0.244	5.800	6.200	
J	0.150	0.157	3.800	4.000	
K	0.016	0.050	0.400	1.270	
θ	0°	8°	0°	8°	

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