

## 抗辐射航空级裸片，四路 2 输入正或非 (NOR) 门。

 查询样品: [SN54AC02-DIE](#)

### 特性

- AC 类型特有 1.5V 至 5.5V 运行
  - 抗辐射: 50kRad (Si) 总电离辐射剂量 (TID) <sup>(1)</sup>
  - (1) 放射耐受性是一个基于最初器件标准的典型值。提供辐射批次验收测试 – 联系厂家了解详细信息。
- TID 剂量率 < 每秒 2mRAD

### 说明

SN54AC02-DIE 器件包含 4 个执行布尔函数

 $Y = \overline{A}$  的独立 2 输入 NOR 门。• 在正逻辑中,  $\overline{B}$  或  $Y = \overline{A+B}$ 。

### ORDERING INFORMATION<sup>(1)</sup>

PRODUCT	PACKAGE DESIGNATOR	PACKAGE	ORDERABLE PART NUMBER	PACKAGE QUANTITY
SN54AC02	TD	Bare die in waffle pack <sup>(2)</sup>	SN54AC02VTD1	100
			SN54AC02VTD2	10

- (1) For the most current package and ordering information, see the Package Option Addendum at the end of this document, or see the TI web site at [www.ti.com](http://www.ti.com).
- (2) Processing is per the Texas Instruments space production baseline and is in compliance with the Texas Instruments Quality Control System in effect at the time of manufacture. Electrical screening consists of DC parametric and functional testing at room temperature only. Unless otherwise specified by Texas Instruments AC performance and performance over temperature is not warranted. Visual Inspection is performed in accordance with MIL-STD-883 Test Method 2010 Condition B at 75X minimum.



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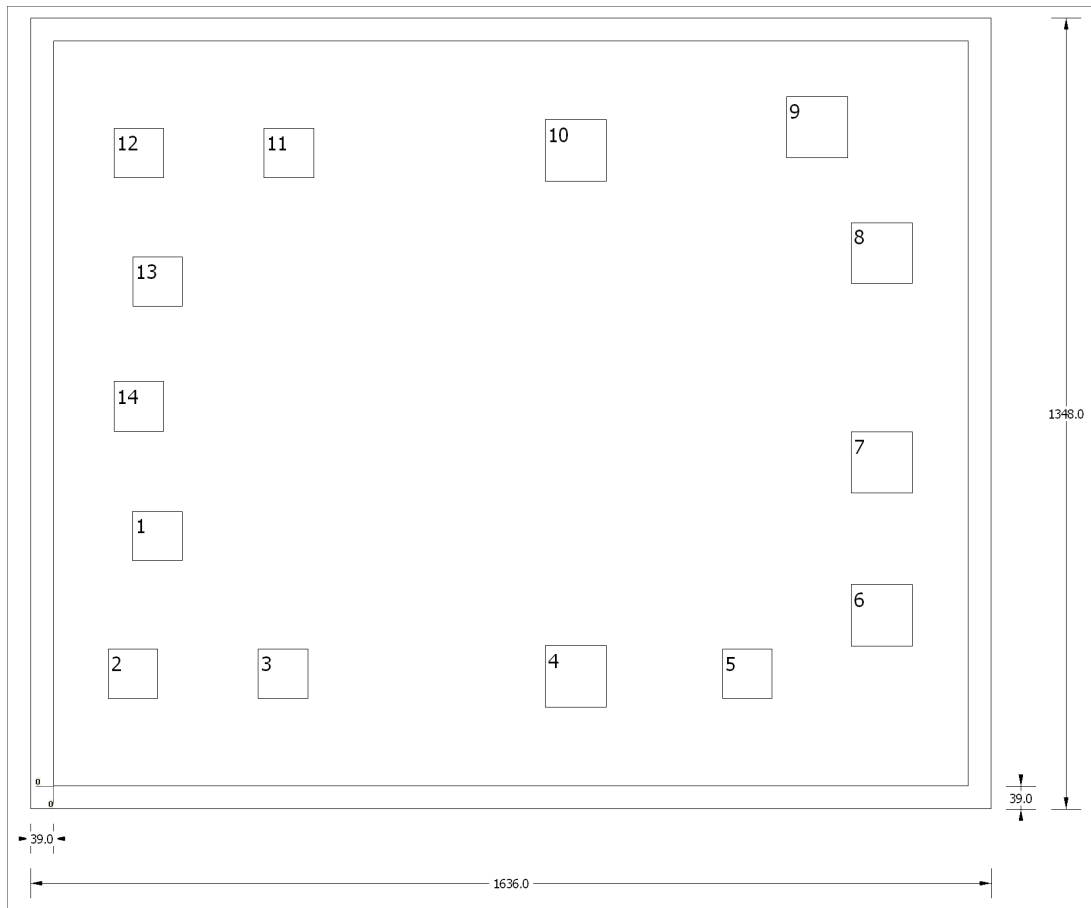


This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

**BARE DIE INFORMATION**

DIE THICKNESS	BACKSIDE FINISH	BACKSIDE POTENTIAL	BOND PAD METALLIZATION COMPOSITION	BOND PAD THICKNESS
10.5 mils.	Silicon with backgrind	Floating	AlCu(2%) TiW	880.3 nm



**Table 1. Bond Pad Coordinates in Microns**

DESCRIPTION	PAD NUMBER	X MIN	Y MIN	X MAX	Y MAX
1Y	1	134.5	383.5	219.5	468.5
1A	2	92.75	149	178	234.25
1B	3	348.25	149	433.5	234.25
2Y	4	837	134.5	942	239.5
2A	5	1139	149	1224.25	234.25
2B	6	1358	238.5	1463	343.5
GND	7	1358	499	1463	604
3A	8	1358	855.5	1463	960.5
3B	9	1248	1070	1353	1175
3Y	10	837	1030.5	942	1135.5
4A	11	358.25	1035.75	443.5	1121
4B	12	102.75	1035.75	188	1121
4Y	13	135.25	817	220.5	902.25
VCC	14	103.25	604.25	188.5	689.5

**PACKAGING INFORMATION**

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead/Ball Finish (6)	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
SN54AC02VTD1	ACTIVE			0	100	TBD	Call TI	N / A for Pkg Type	25 to 25		<b>Samples</b>
SN54AC02VTD2	ACTIVE			0	10	TBD	Call TI	N / A for Pkg Type	25 to 25		<b>Samples</b>

(1) The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

(2) **RoHS:** TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".

**RoHS Exempt:** TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.

**Green:** TI defines "Green" to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

(3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

(6) Lead/Ball Finish - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

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**OTHER QUALIFIED VERSIONS OF SN54AC02-DIE :**

- Space: [SN54AC02-SP](#)

## NOTE: Qualified Version Definitions:

- Space - Radiation tolerant, ceramic packaging and qualified for use in Space-based application

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