

产品概览

AR0135CS: 1/3" 1.2 MP Global Shutter CMOS Image Sensor

欲看完整文档，请参阅数据表。

This 1/3-inch format, 1.2 MP imaging device has been designed to address the challenging requirements of Industrial, consumer and IoT applications. The new global shutter sensor allows the camera to “freeze” fast moving scene data, and ensures effective synchronization with pulsed light sources. The AR0135 sensor incorporates a new innovative global shutter pixel design, with 10X lower dark current and 4X higher shutter efficiency vs. previous generation products. These improvements allow the sensor to produce clear, low-noise images, in both low-light and bright scenes, and in high temperature environments. This performance enables the required eye tracking and gesture detection functionality in 3D scanner, gesture recognition devices.

特性

- ON Semiconductor's Next Generation Global Shutter Technology
- Superior Low-light Performance
- HD Video (720p60)
- Video/Single Frame Mode
- Flexible Row-skip Modes
- On-chip AE and Statistics Engine
- Parallel and Serial Output
- Support for External LED or Flash
- Auto Black Level Calibration
- Context Switching

For more features, see the data sheet

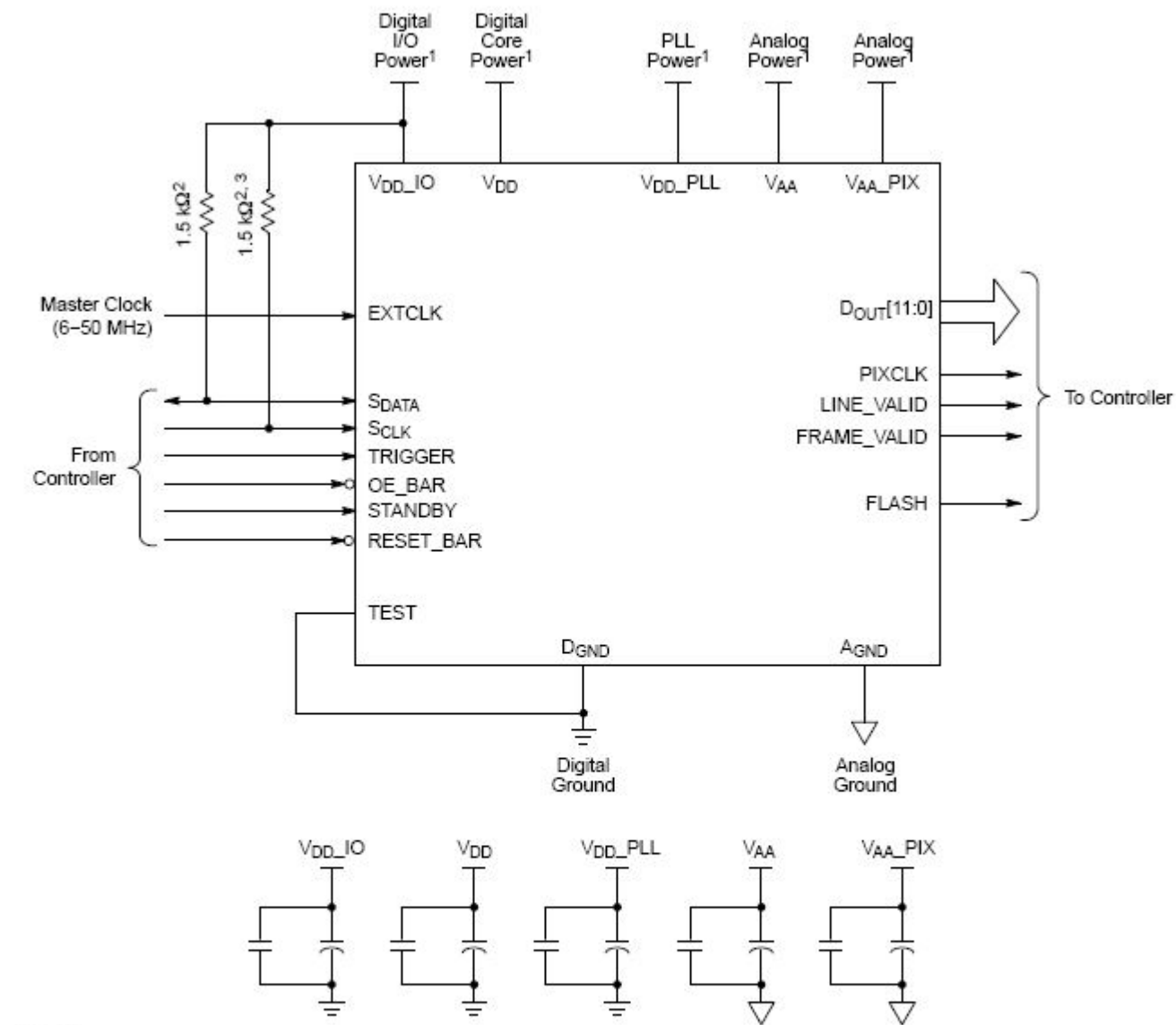
应用

- Barcode scanner
- 3D scanning
- Positional tracking
- Iris Scanning
- Augmented Reality

器件电气规格

产品	Compliance	Status	Type	Megapixel s	Frame Rate (fps)	Optical Format	Shutter Type	Pixel Size (µm)	Output Interface	Color	Package Type
AR0135CS2C00SUD2 0	Pb-free	Active	CMOS	1.2	1.2MP 54fps, 720P 60fps	1/3 inch	Global Shutter	3.75 x 3.75	Parallel	RGB	
	Halide free								HiSPi™		
AR0135CS2C00SUEA 0-DPBR1	Pb-free	Active	CMOS	1.2	1.2MP 54fps, 720P 60fps	1/3 inch	Global Shutter	3.75 x 3.75	Parallel	RGB	IBGA-63
	Halide free								HiSPi™		
AR0135CS2C00SUEA 0-DRBR	Pb-free	Active	CMOS	1.2	1.2MP 54fps, 720P 60fps	1/3 inch	Global Shutter	3.75 x 3.75	Parallel	RGB	IBGA-63
	Halide free								HiSPi™		
AR0135CS2C00SUEA 0-DRBR1	Pb-free	Active	CMOS	1.2	1.2MP 54fps, 720P 60fps	1/3 inch	Global Shutter	3.75 x 3.75	Parallel	RGB	IBGA-63
	Halide free								HiSPi™		
AR0135CS2M00SUD2 0	Pb-free	Active	CMOS	1.2	1.2MP 54fps, 720P 60fps	1/3 inch	Global Shutter	3.75 x 3.75	HiSPi™	Mono	
	Halide free								Parallel		
AR0135CS2M00SUEA 0-DPBR	Pb-free	Active	CMOS	1.2	1.2MP 54fps, 720P 60fps	1/3 inch	Global Shutter	3.75 x 3.75	HiSPi™	Mono	IBGA-63
	Halide free								Parallel		
AR0135CS2M00SUEA 0-DPBR1	Pb-free	Active	CMOS	1.2	1.2MP 54fps, 720P 60fps	1/3 inch	Global Shutter	3.75 x 3.75	HiSPi™	Mono	IBGA-63
	Halide free								Parallel		
AR0135CS2M00SUEA 0-DRBR	Pb-free	Active	CMOS	1.2	1.2MP 54fps, 720P 60fps	1/3 inch	Global Shutter	3.75 x 3.75	HiSPi™	Mono	IBGA-63
	Halide free								Parallel		
AR0135CS2M00SUEA 0-DRBR1	Pb-free	Active	CMOS	1.2	1.2MP 54fps, 720P 60fps	1/3 inch	Global Shutter	3.75 x 3.75	HiSPi™	Mono	IBGA-63
	Halide free								Parallel		
AR0135CS2M25SUD2 0	Pb-free	Active	CMOS	1.2	1.2MP 54fps, 720P 60fps	1/3 inch	Global Shutter	3.75 x 3.75	Parallel	Mono	
	Halide free								HiSPi™		
AR0135CS2M25SUEA 0-DPBR	Pb-free	Active	CMOS	1.2	1.2MP 54fps, 720P 60fps	1/3 inch	Global Shutter	3.75 x 3.75	Parallel	Mono	IBGA-63
	Halide free								HiSPi™		
AR0135CS2M25SUEA 0-DPBR1	Pb-free	Active	CMOS	1.2	1.2MP 54fps, 720P 60fps	1/3 inch	Global Shutter	3.75 x 3.75	Parallel	Mono	IBGA-63
	Halide free								HiSPi™		
AR0135CS2M25SUEA 0-DRBR	Pb-free	Active	CMOS	1.2	1.2MP 54fps, 720P 60fps	1/3 inch	Global Shutter	3.75 x 3.75	HiSPi™	Mono	IBGA-63
	Halide free								Parallel		
AR0135CS2M25SUEA 0-DRBR1	Pb-free	Active	CMOS	1.2	1.2MP 54fps, 720P 60fps	1/3 inch	Global Shutter	3.75 x 3.75	HiSPi™	Mono	IBGA-63
	Halide free								Parallel		

应用框图



- Notes:
1. All power supplies must be adequately decoupled.
 2. ON Semiconductor recommends a resistor value of 1.5 k Ω , but a greater value may be used for slower two-wire speed.
 3. This pull-up resistor is not required if the controller drives a valid logic level on SCLK at all times.
 4. The serial interface output pads can be left unconnected if the parallel output interface is used.
 5. ON Semiconductor recommends that 0.1 μ F and 10 μ F decoupling capacitors for each power supply are mounted as close as possible to the pad. Actual values and results may vary depending on the layout and design considerations. Refer to the AR0135CS demo headboard schematics for circuit recommendations.
 6. ON Semiconductor recommends that analog power planes are placed in a manner such that coupling with the digital power planes is minimized.

欲了解更多信息，请联系您当地的销售支援 www.onsemi.cn。

创建于：11/27/2017