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May 2015



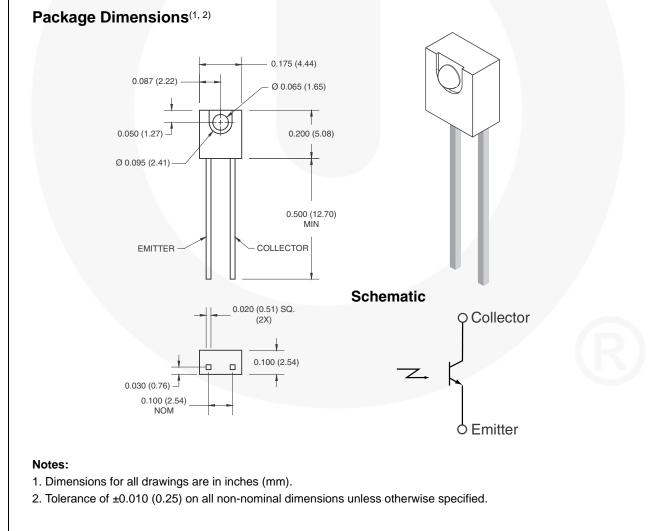
QSE122 Plastic Silicon Infrared Phototransistor

Features

- NPN Silicon Phototransistor
- Package Type: Sidelooker
- Medium Wide Reception Angle, 50°
- Package Material and Color: Black Epoxy
- Matched Emitter: QEE113
- Daylight Filter
- High Sensitivity
- Red dot marking on the top side

Description

The QSE122 is a silicon phototransistor encapsulated in a wide angle, infrared transparent, black plastic sidelooker package.



Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}$ C unless otherwise noted.

| Symbol | Parameter | Value | Unit |
|--------------------|---|----------------|------|
| T _{OPR} | Operating Temperature | -40 to +100 | °C |
| T _{STG} | Storage Temperature | -40 to +100 | °C |
| T _{SOL-I} | Soldering Temperature (Iron) ^(4, 5, 6) | 240 for 5 sec | °C |
| T _{SOL-F} | Soldering Temperature (Flow) ^(4, 5) | 260 for 10 sec | °C |
| V _{CE} | Collector Emitter Voltage | 30 | V |
| V _{EC} | Emitter Collector Voltage | 5 | V |
| P _D | Power Dissipation ⁽³⁾ | 100 | mW |

Notes:

- 3. Derate power dissipation linearly 1.33 mW/°C above 25°C.
- 4. RMA flux is recommended.
- 5. Methanol or isopropyl alcohols are recommended as cleaning agents.
- 6. Soldering iron 1/16" (1.6mm) minimum from housing.

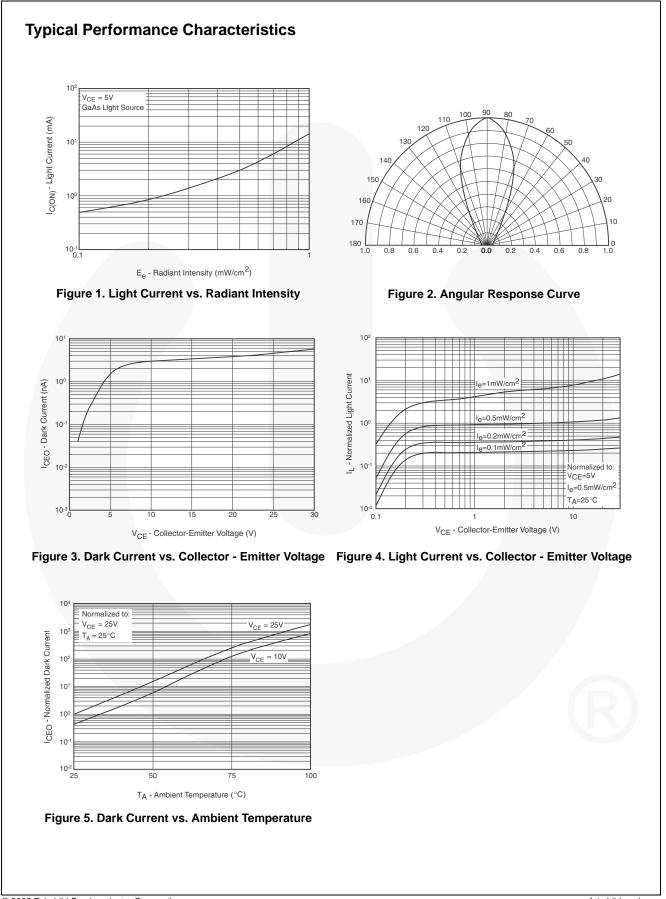
Electrical / Optical Characteristics

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

| Symbol | Parameter | Conditions | Min. | Тур. | Max. | Unit |
|----------------------|---|--|------|------|------|------|
| λ _{PS} | Peak Sensitivity | | | 880 | | nm |
| Θ | Reception Angle | | | ±25 | | 0 |
| I _{CEO} | Collector Emitter Dark Current | $V_{CE} = 10 \text{ V}, \text{ E}_{e} = 0$ | | | 100 | nA |
| BV _{CEO} | Collector-Emitter Breakdown | I _C = 1 mA | 30 | | | V |
| BV _{ECO} | Emitter-Collector Breakdown | I _E = 100 μA | 5 | | | V |
| I _{C(ON)} | On-State Collector Current ⁽⁷⁾ | $E_e = 0.5 \text{ mW/cm}^2$, $V_{CE} = 5 \text{ V}$ | 3.0 | | 12.0 | mA |
| V _{CE(SAT)} | Saturation Voltage ⁽⁷⁾ | $E_e = 0.5 \text{ mW/cm}^2$, $I_C = 0.1 \text{ mA}$ | | | 0.4 | V |
| t _r | Rise Time | I _C = 1 mA, V _{CC} = 5 V, | | 8 | / | μs |
| t _f | Fall Time | $R_L = 100 \Omega$ | | 8 | | μs |

Note:

7. λ = 880 nm (AlGaAs)



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| Datasheet Identification | Product Status | Definition |
|--------------------------|-----------------------|--|
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| Preliminary | First Production | Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design. |
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