0.4 to 0.6A Peak Output Current Driver Coupler（DIP8）

CHARACTERISTIC	TOSHIBA Leading Innovation >>> TLP351A / TLP351AF		TOSHIBA Leading Innovation >>> TLP351H / TLP351HF		AVAGO TECHNOLOGIES HCPL-T251		AVAGO TECHNOLOGIES HCPL-3140		AVAGO TECHNOLOGIES HCPL-J314		AVAGO TECHNOLOGIES HCPL-3150		AVAGO TECHNOLOGIES ACPL-J314	
Package	DIP8		DIP8		DIP8		DIP8		DIP8		DIP8		DIP8	
Output Current IOPH/IOPL (max)	±0.6 A		±0.6 A		±0.4 A		±0.6 A		±0.6 A		±0.6 A		±0.6 A	
Operating Temperature Range Topr	-40~100°C		**-40~125°C**		-40~85°C		-40~100°C		-40~100°C		-40~100°C		-40~100°C	
Supply Voltage VCC	**10~30 V**		**10~30 V**		15~30 V		15~30 V		10~30 V		15~30 V		10~30 V	
Logic Low/High Supply Current ICCH/ICCL (max)	**2 mA**		**2 mA**		11 mA		3 mA		3 mA		5 mA		3 mA	
Threshold Input Current IFLH (max)	5 mA		5 mA		7 mA		6 mA		6 mA		5 mA		6 mA	
Propagation Delay Time	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.
tpLH/tpHL(max.)	30 ns	**500 ns**	30 ns	700 ns	100 ns	700 ns	100 ns	700 ns	100 ns	700 ns	100 ns	500 ns	100 ns	700 ns
Pulse Width Distortion	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.
|tpHL-tpLH|	-	350 ns	-	500 ns	-	-	-	-	-	-	-	300 ns	-	-
Propagation Delay Difference Between	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.
Any Two Parts or Channels PDD (tpHL-tpLH)	-	-	-	-	-500 ns	500 ns	-500 ns	500 ns	-500 ns	500 ns	-350 ns	350 ns	-500 ns	500 ns
Common Mode Transient Immunity CMH/CML (min.) (Ta=25°C)	**± 20 kV/ms**		**± 20 kV/ms**		± 10 kV/μs		± 10 kV/μs		± 25 kV/μs		± 15 kV/μs		± 25 kV/μs	
Isolation Voltage BVs (min) (Ta=25°C)	3750 Vrms		3750 Vrms		3750 Vrms		3750 Vrms		3750 Vrms		3750 Vrms		3750 Vrms	