POWER INTEGRATIONS

PI News

Primary-side regulation now efficient for 9-90W applications

Primary-side regulation (PSR) has been standard in many low-power applications such as mobile-phone chargers but until now has lacked the constant-voltage accuracy required in higher-power products. New *LinkSwitch-HP* ICs from Power Integrations feature a unique multi-mode control architecture enabling a CV performance of +/- 5%, so PSR is now an efficient approach for power-supply applications from 9 W to 90 W, dramatically reducing component count and saving space and cost while enhancing reliability.

LinkSwitch-HP ICs are efficient across the entire load range, and are capable of no-load power consumption of less than 30 mW at 230 VAC and are more than 50 percent efficient at 0.1 W input power, easily meeting all global energy efficiency regulations such as ErP (EuP), ENERGY STAR® EPS V2.0, DOE V6, CoC Tier 2 and EC Ecodesign Directive Tier 2 for external power supplies.

Devices feature OCP and OVP (latching and non-latching) as well as fast transient response protection which is a requirement in certain power adapters, and 6 kV surge immunity. Three package styles are available: eSIP™-7C vertical package for minimum PCB footprint; the low profile eSOP™-12B package for ultra-slim designs; and the eDIP™-12B low profile through-hole package for ultra-slim designs.

Learn more

- Watch the LinkSwitch-HP video
- Get new LinkSwitch-HP design ideas
- Order samples
- Download the LinkSwitch-HP datasheet