

6

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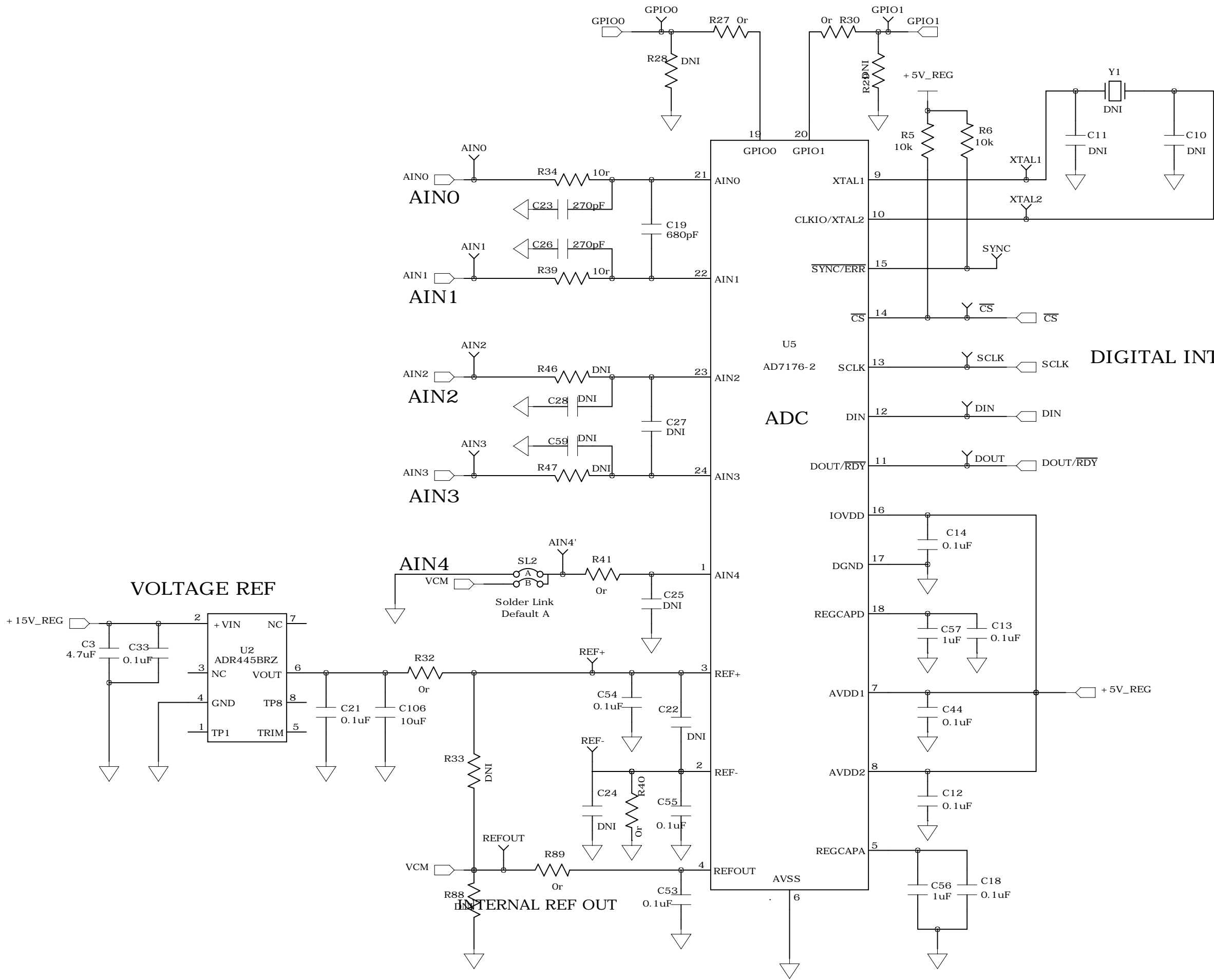
2

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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

AD7176-2 24-Bit 250KSPS SD ADC

GPIO Ref to 5V Analog
Provision for voltage divider
for low voltages

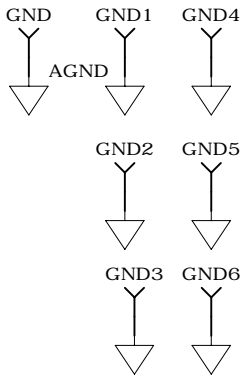


DIGITAL INTERFACE

ADC

VOLTAGE REF

INTERNAL REF OUT



COMPANY: ANALOG DEVICES				
TITLE: EVAL-CN0292-SDZ				
DRAWN: Pat Sheahan	DATED: 20-Jun 2014	CODE:	SIZE:	DRAWING NO:
CHECKED: Robert Kiely	DATED: 20-Jun 2014			REV:
QUALITY CONTROL: < QC By >	DATED: < QC Date >	< Code > A202-038798-01		B
RELEASED: < Released By >	DATED: < Release Date >	REV: < Scale >		SHEET: 1 of 6

6

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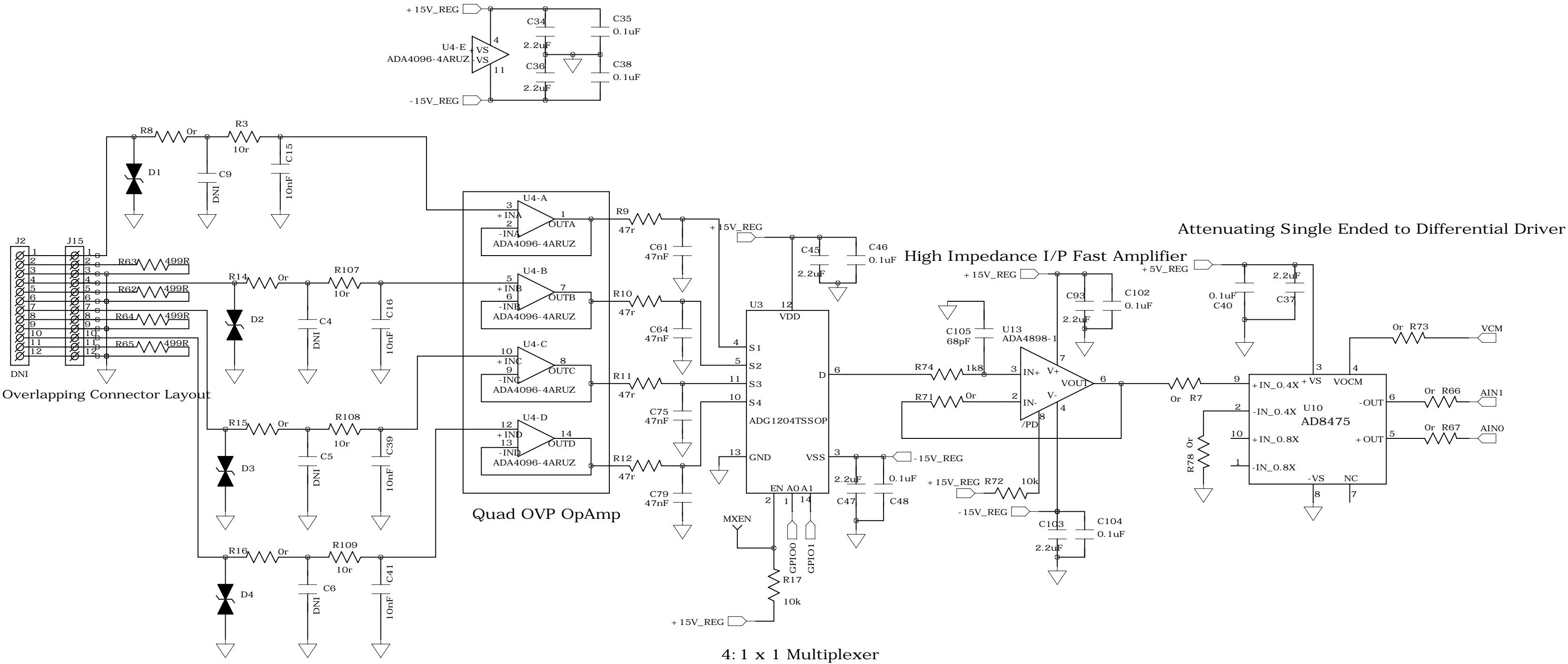
4

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1

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



COMPANY:		ANALOG DEVICES	
TITLE:			
EVAL-CN0292-SDZ			
CODE:	SIZE:	DRAWING NO:	REV:
< Code>	A202	038798-01	B
SCALE: < Scale>		SHEET: 2 of 6	

6

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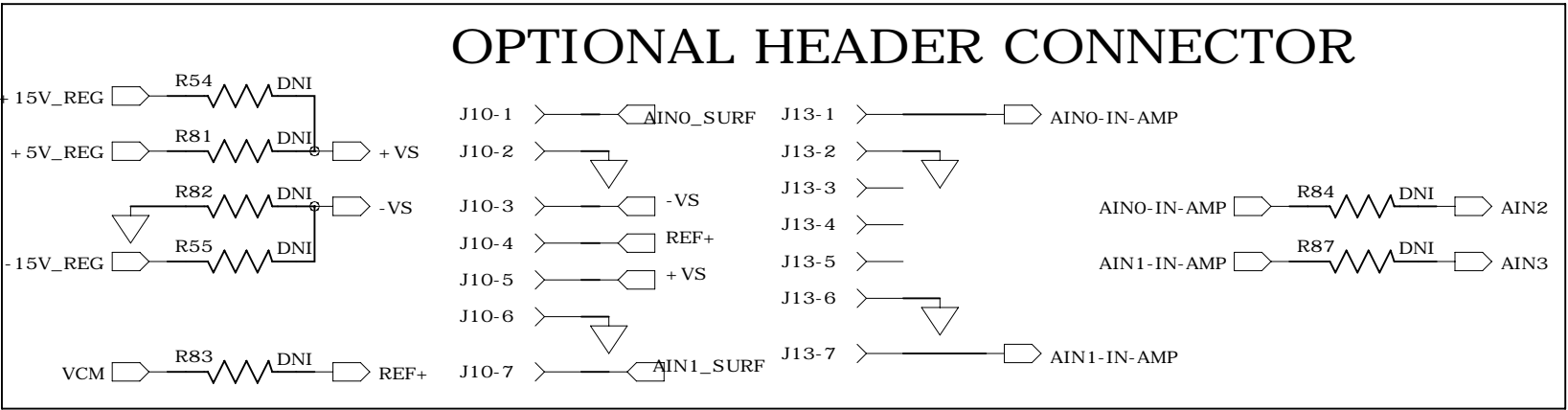
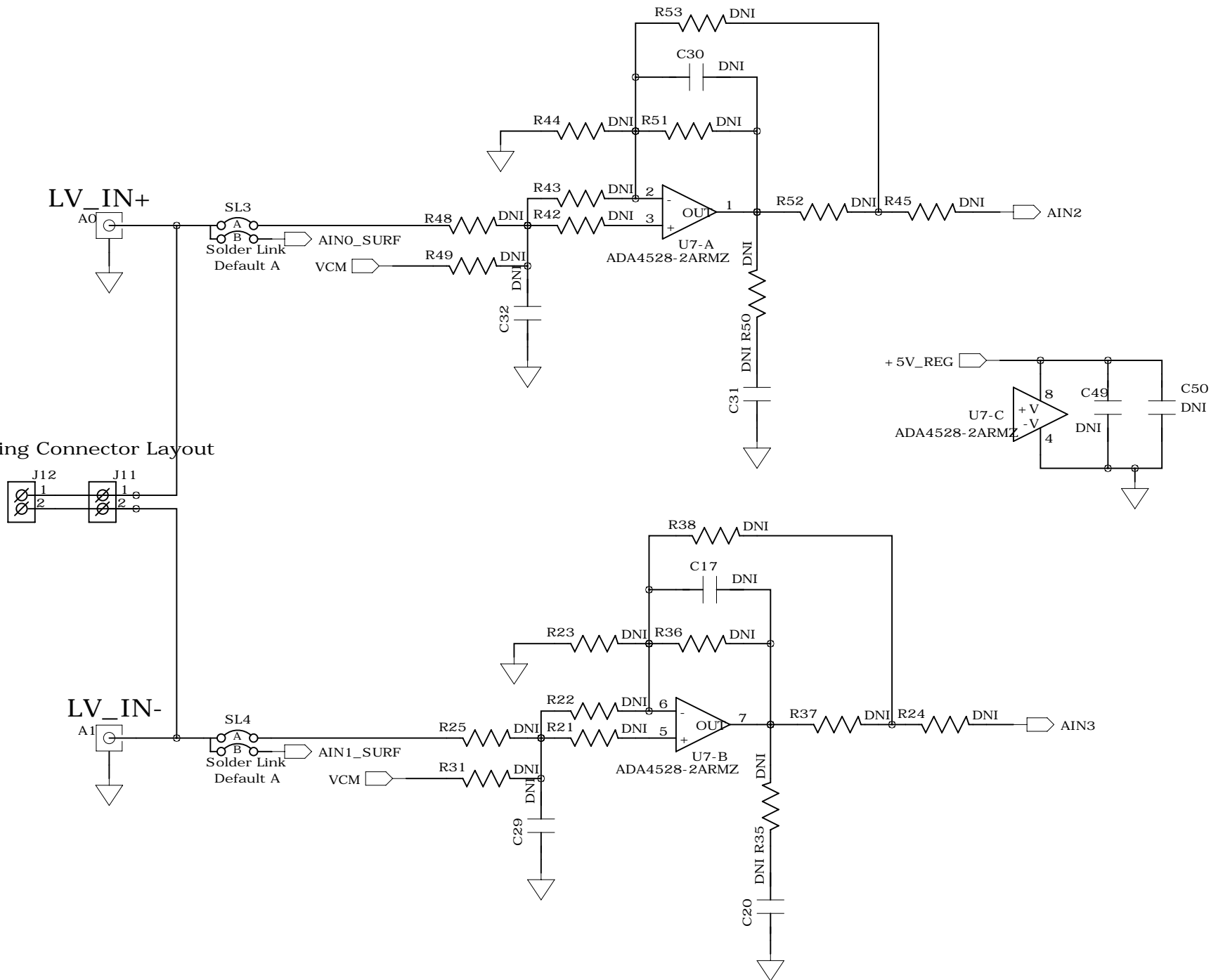
4

3

2

1

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



COMPANY: ANALOG DEVICES			
TITLE: EVAL-CN0292-SDZ			
CODE:	SIZE:	DRAWING NO:	REV:
< Code >	A2	02-038798-01	B
SCALE: < Scale >		SHEET: 3 OF 6	

DRAWN: Pat Sheahan	DATED: 20-Jun 2014
CHECKED: Robert Kiely	DATED: 20-Jun 2014
QUALITY CONTROL: < QC By >	DATED: < QC Date >
RELEASED: < Released By >	DATED: < Release Date >

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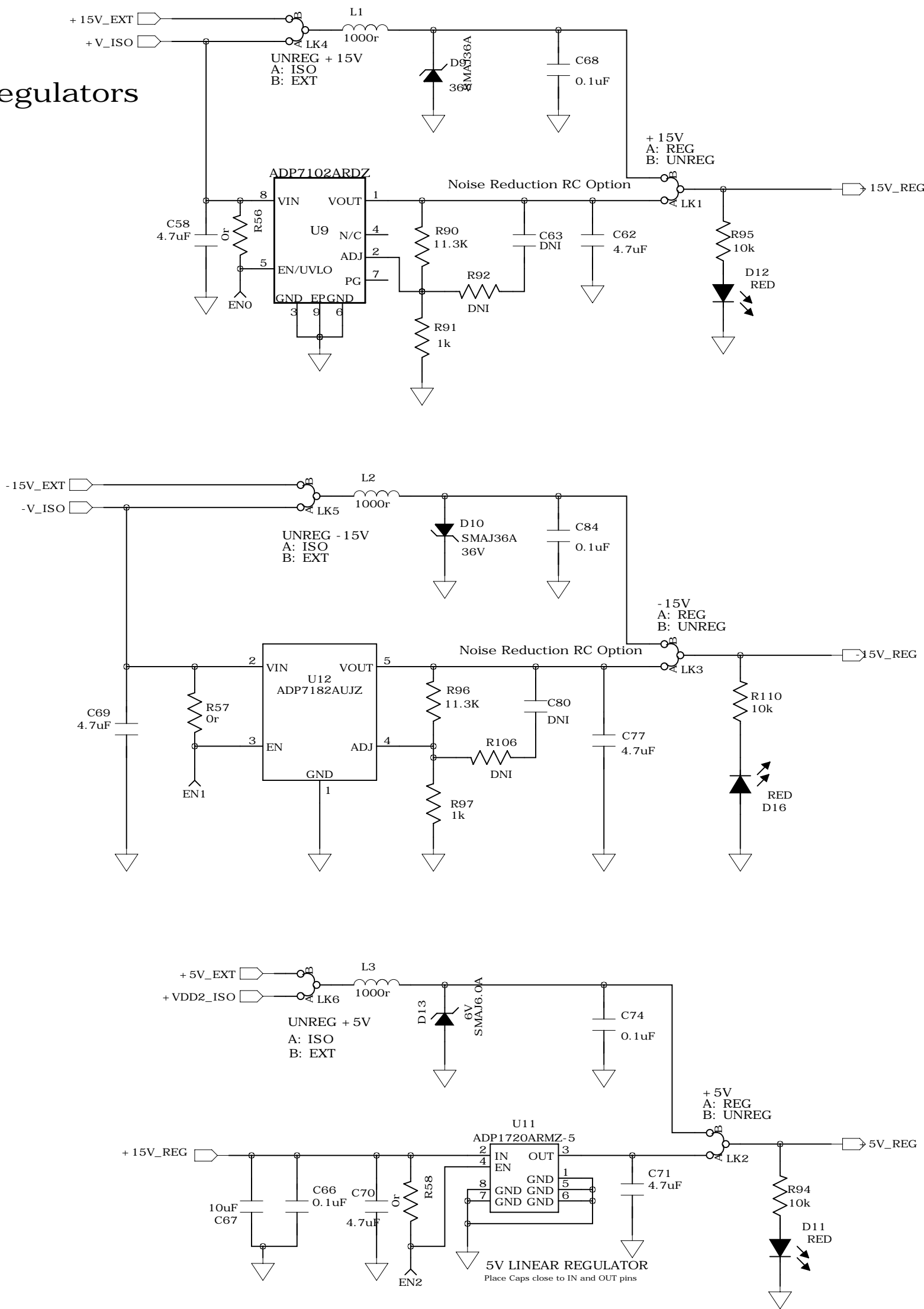
3

2

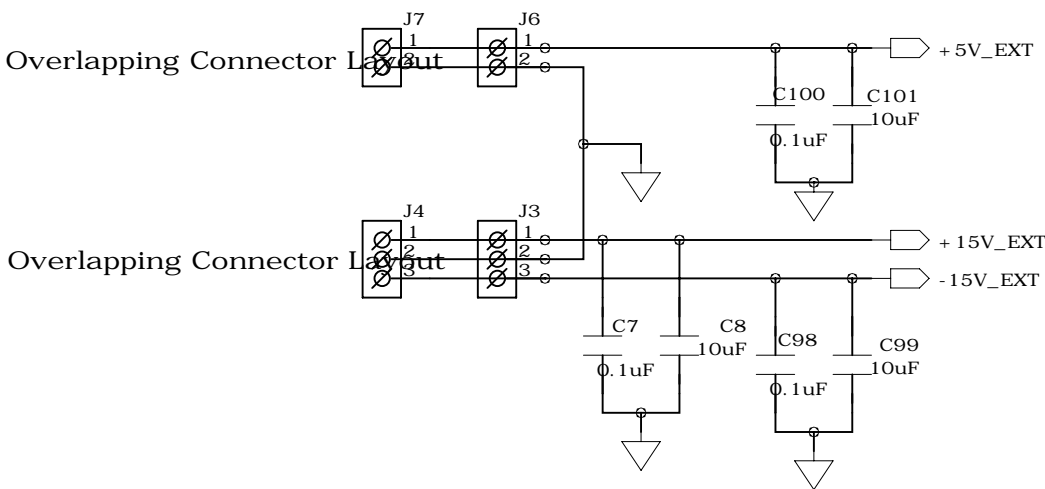
1

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LDO Regulators



EXTERNAL POWER SUPPLY INPUTS



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TITLE: EVAL-CN0292-SDZ					
DRAWN: Pat Sheahan	DATED: 20-Jun 2014	CODE:	SIZE:	DRAWING NO:	REV:
CHECKED: Robert Kiely	DATED: 20-Jun 2014	< Code> A202-038798-01			B
QUALITY CONTROL: < QC By>	DATED: < QC Date>	SCALE: < Scale>			SHEET: 4 of 6
RELEASED: < Released By>	DATED: < Release Date>				

6

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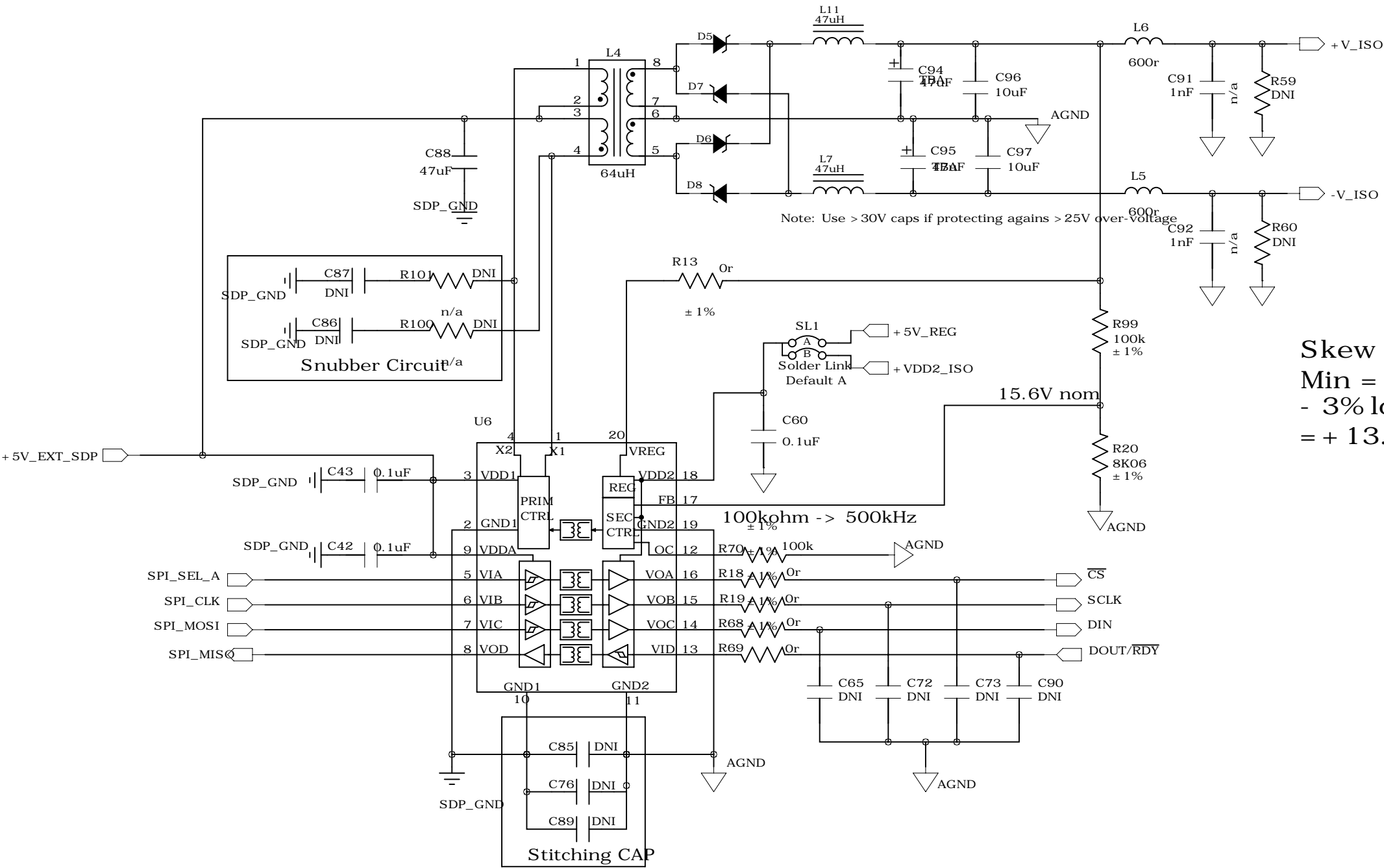
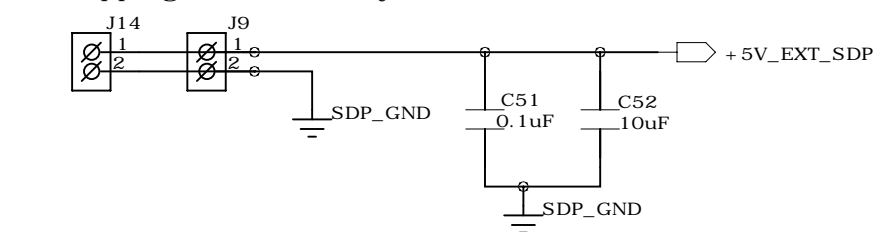
3

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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

Overlapping Connector Layout



Skew slightly high
Min = + 13.8Vadi + 0.5Vskew - 10mV/VlineReg
- 3% loadReg - 1% resistorError
= + 13.59Vmin

Overlap internal Ground and power planes per AN0971
Ensure keepout area on top an bottom layer for isolation

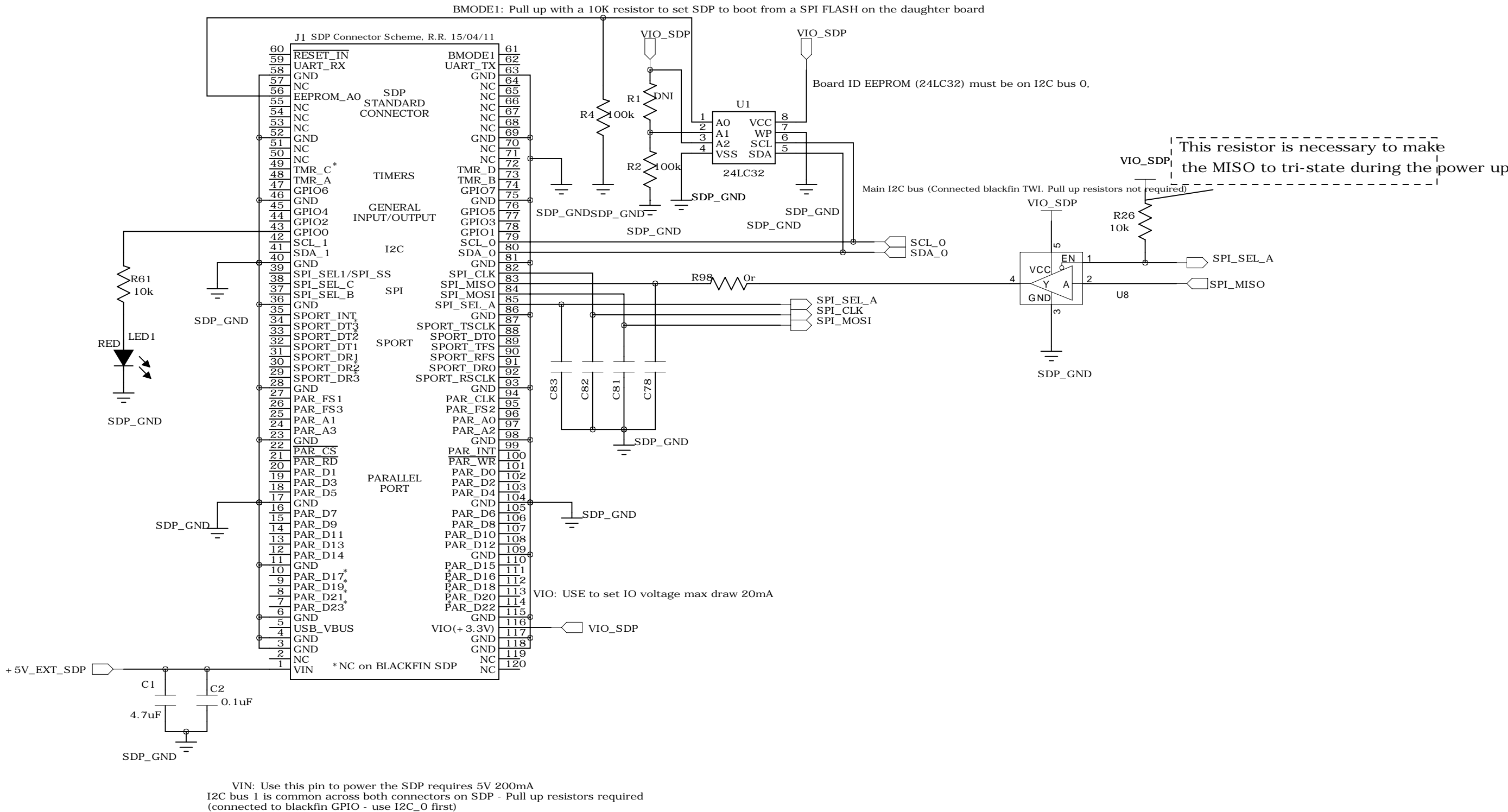
COMPANY: ANALOG DEVICES			
TITLE: EVAL-CN0292-SDZ			
CODE:	SIZE:	DRAWING NO:	REV:
< Code >	A202	038798-01	B
SCALE: < Scale >			
SHEET: 5 OF 6			

DRAWN: Pat Sheahan	DATED: 20-Jun 2014
CHECKED: Robert Kiely	DATED: 20-Jun 2014
QUALITY CONTROL: < QC By >	DATED: < QC Date >
RELEASED: < Released By >	DATED: < Release Date >

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

SDP CONNECTOR EEPROM-SW/USB ID

VIO: USE to set IO voltage max draw 20mA
VIN: Use this pin to power the SDP requires 4-7V 200mA
BMODE1: Pull up with a 10K resistor to set SDP to boot from a SPI FLASH on the daughter board



COMPANY: ANALOG DEVICES			
TITLE: EVAL-CN0292-SDZ			
CODE:	SIZE:	DRAWING NO:	REV:
< Code >	A202	02-038798-01	B
SCALE: < Scale >			
SHEET: 6		6	

DRAWN: Pat Sheahan	DATED: 20-Jun 2014
CHECKED: Robert Kiely	DATED: 20-Jun 2014
QUALITY CONTROL: < QC By >	DATED: < QC Date >
RELEASED: < Released By >	DATED: < Release Date >