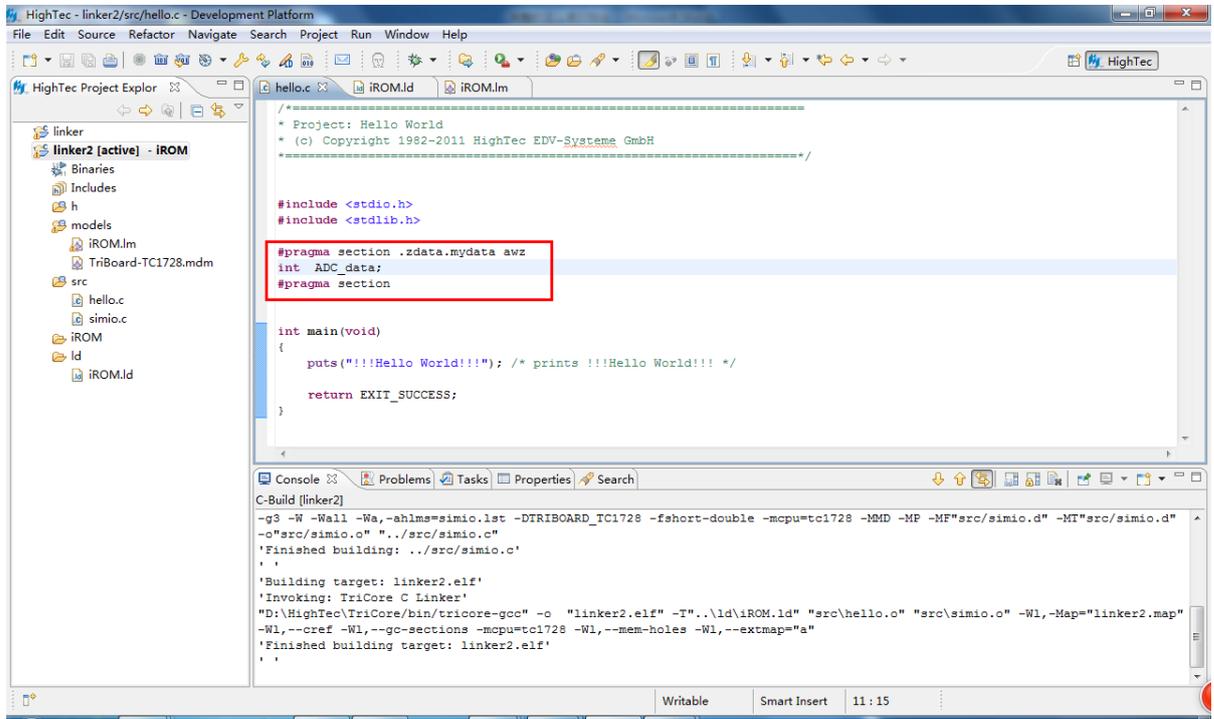


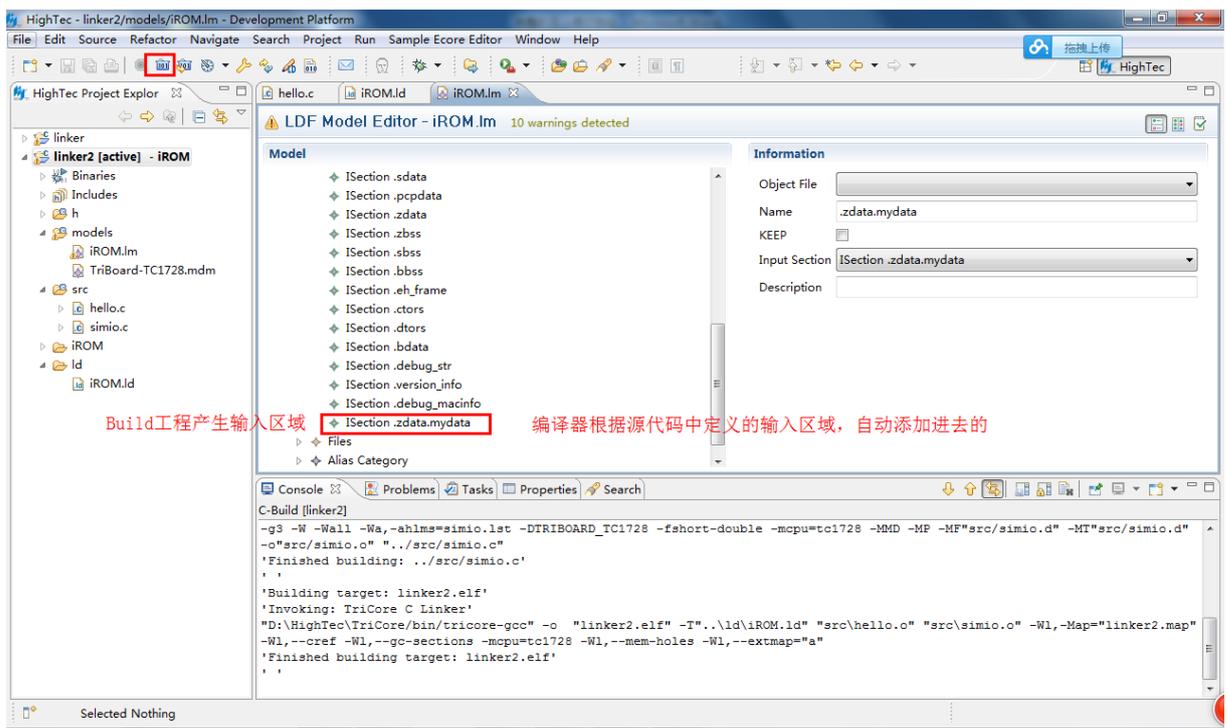
完整的定义绝对地址

大头小脑

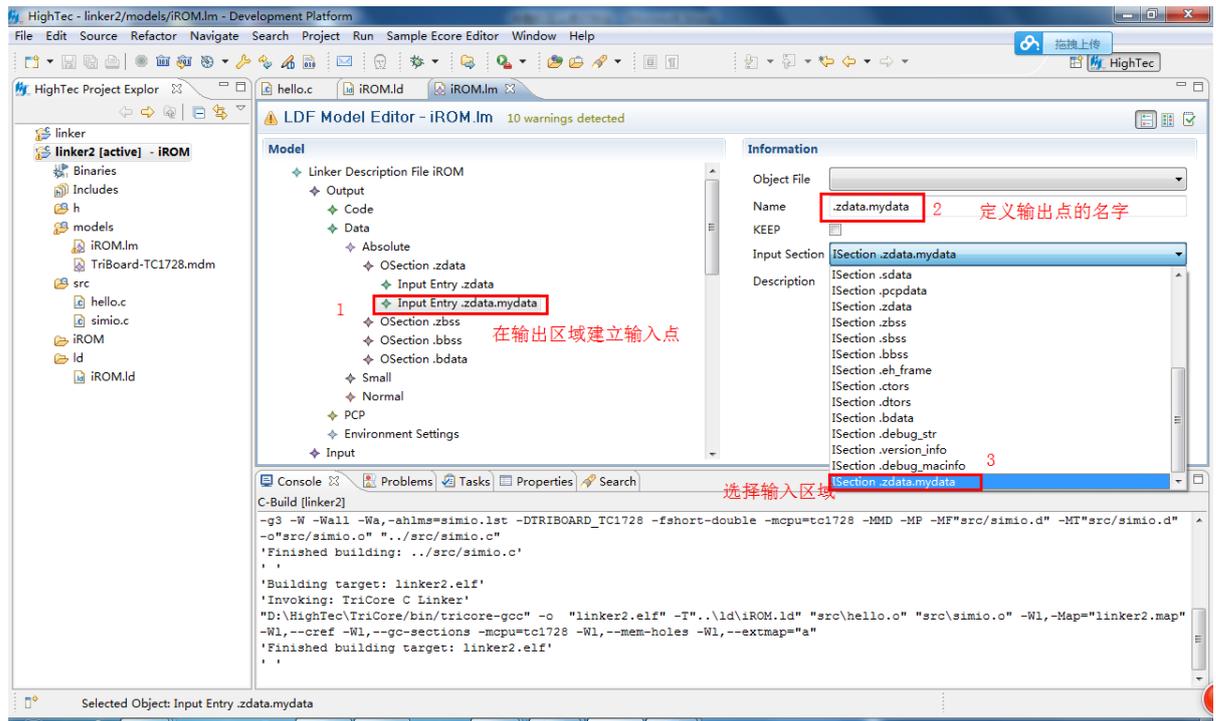
在源代码中添加输入:



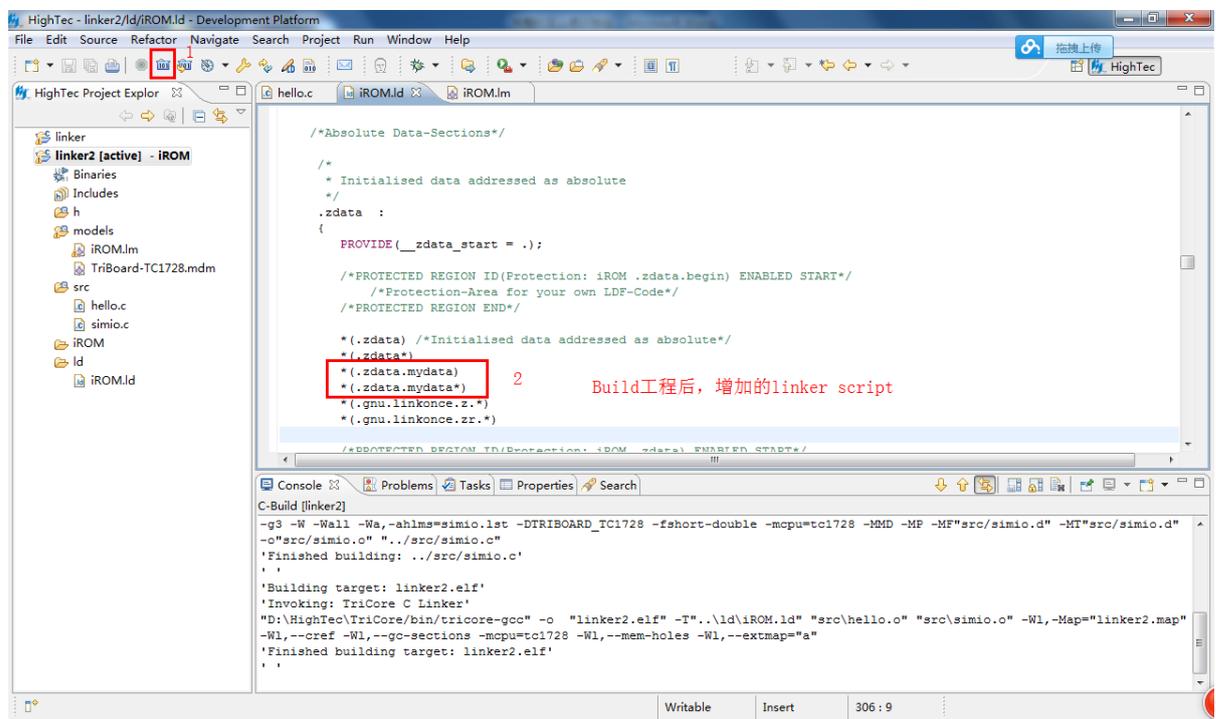
Build 工程:



输出点名字不出现在 linker script 中:

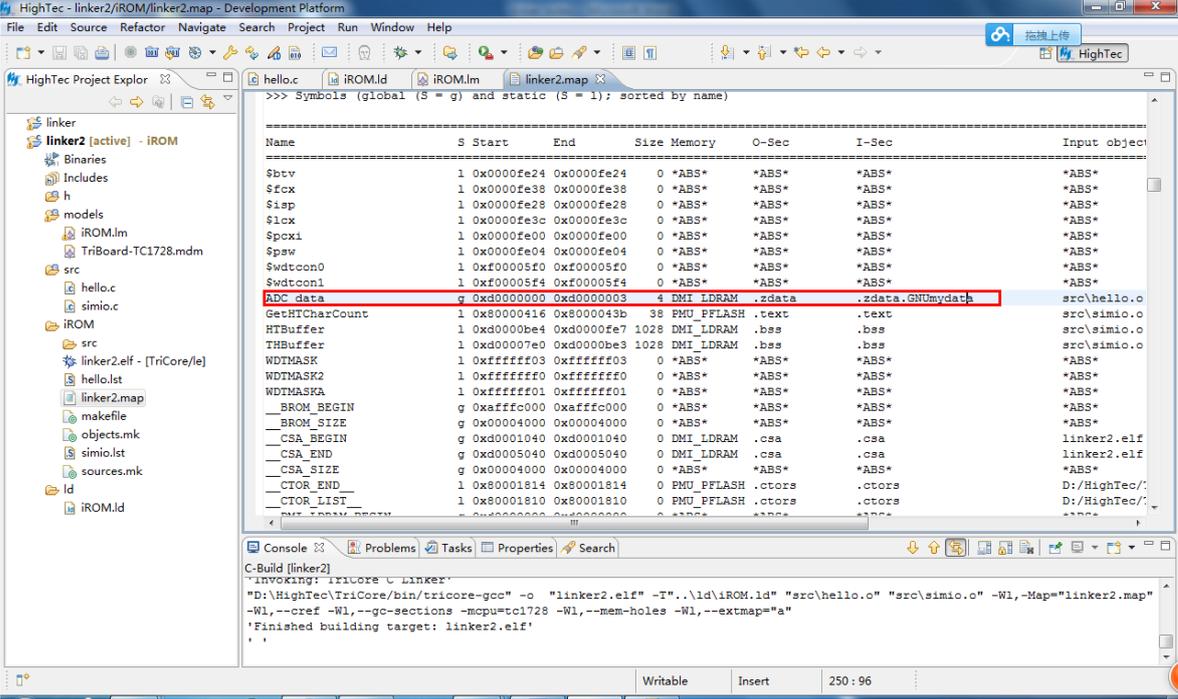


Build 工程后, 没有错误.



在 mapfile 中查看变量的地址和区域, MAP 非常重要, 要建立起

MAP 查看的习惯。



HighTec linker2.iROM/linker2.map - Development Platform

File Edit Source Refactor Navigate Search Project Run Window Help

HighTec Project Explorer

- linker
- linker2 [active] - iROM
 - Binaries
 - Includes
 - h
 - models
 - iROM.lm
 - TriBoard-TC1728.mdm
 - src
 - hello.c
 - simio.c
 - iROM
 - src
 - linker2.elf - [TriCore/e]
 - hello.lst
 - linker2.map
 - makefile
 - objects.mk
 - simio.lst
 - sources.mk
 - ld
 - iROM.ld

hello.c iROM.ld iROM.lm linker2.map

>>> Symbols (global (S = g) and static (S = 1); sorted by name)

Name	S	Start	End	Size	Memory	O-Sec	I-Sec	Input object
\$btv	1	0x0000fe24	0x0000fe24	0	*ABS*	*ABS*	*ABS*	*ABS*
\$fcx	1	0x0000fe38	0x0000fe38	0	*ABS*	*ABS*	*ABS*	*ABS*
\$fcp	1	0x0000fe28	0x0000fe28	0	*ABS*	*ABS*	*ABS*	*ABS*
\$ltx	1	0x0000fe3c	0x0000fe3c	0	*ABS*	*ABS*	*ABS*	*ABS*
\$pcxi	1	0x0000fe00	0x0000fe00	0	*ABS*	*ABS*	*ABS*	*ABS*
\$pwr	1	0x0000fe04	0x0000fe04	0	*ABS*	*ABS*	*ABS*	*ABS*
\$wdrcon0	1	0xf00005f0	0xf00005f0	0	*ABS*	*ABS*	*ABS*	*ABS*
\$wdrcon1	1	0xf00005f4	0xf00005f4	0	*ABS*	*ABS*	*ABS*	*ABS*
ADC_data	g	0x40000000	0x40000003	4	DMI_IDRAM	.zdata	.zdata.GNUydata	src\hello.o
GetHCharCount	1	0x80000416	0x8000043b	38	PMU_PFLASH	.text	.text	src\simio.o
HTBuffer	1	0xd0000be4	0xd0000fe7	1028	DMI_IDRAM	.bss	.bss	src\simio.o
THBuffer	1	0xd00007e0	0xd0000be3	1028	DMI_IDRAM	.bss	.bss	src\simio.o
WDTMASK	1	0xffffffff03	0xffffffff03	0	*ABS*	*ABS*	*ABS*	*ABS*
WDTMASK2	1	0xffffffff00	0xffffffff00	0	*ABS*	*ABS*	*ABS*	*ABS*
WDTMASKA	1	0xffffffff01	0xffffffff01	0	*ABS*	*ABS*	*ABS*	*ABS*
__BROM_BEGIN	g	0xaafffc000	0xaafffc000	0	*ABS*	*ABS*	*ABS*	*ABS*
__BROM_SIZE	g	0x00004000	0x00004000	0	*ABS*	*ABS*	*ABS*	*ABS*
__CSA_BEGIN	g	0xd0001040	0xd0001040	0	DMI_IDRAM	.csa	.csa	linker2.elf
__CSA_END	g	0xd00005040	0xd00005040	0	DMI_IDRAM	.csa	.csa	linker2.elf
__CSA_SIZE	g	0x00004000	0x00004000	0	*ABS*	*ABS*	*ABS*	*ABS*
__CTOR_END	1	0x80001814	0x80001814	0	PMU_PFLASH	.ctors	.ctors	D:/HighTec/...
__CTOR_LIST	1	0x80001810	0x80001810	0	PMU_PFLASH	.ctors	.ctors	D:/HighTec/...
__PMU_IDRAM_BEGIN	g	0x40000000	0x40000000	0	*ABS*	*ABS*	*ABS*	*ABS*

Console

```
C-Build [linker2]
Invoking: iRiCore C Linker
"D:\HighTec\TriCore\bin\tricore-gcc" -o "linker2.elf" -I"..\\ld\iROM.ld" "src\hello.o" "src\simio.o" -Wl,-Map="linker2.map"
-Wl,--cref -Wl,--gc-sections -mcpu=tc1728 -Wl,--mem-holes -Wl,--extmap="a"
Finished building target: linker2.elf
```

Writable Insert 250:96