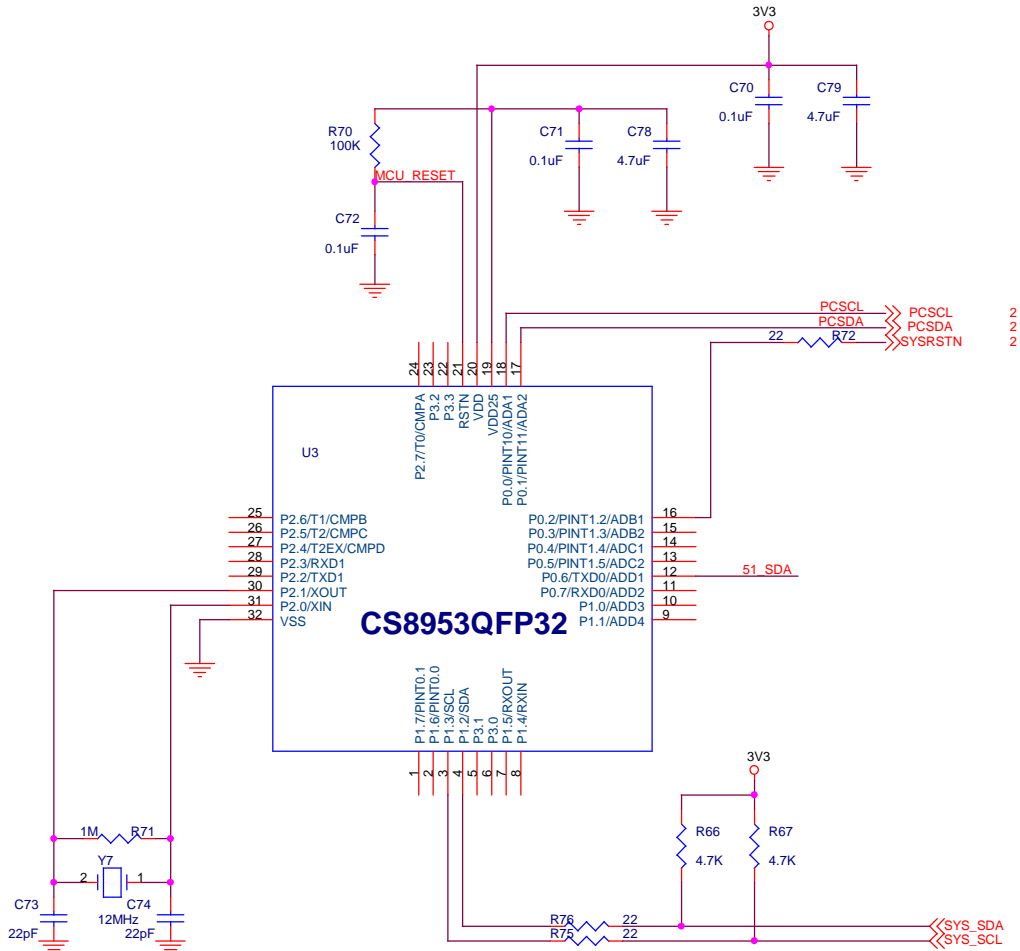


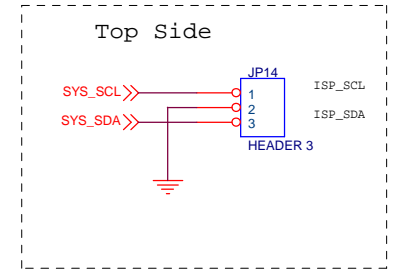
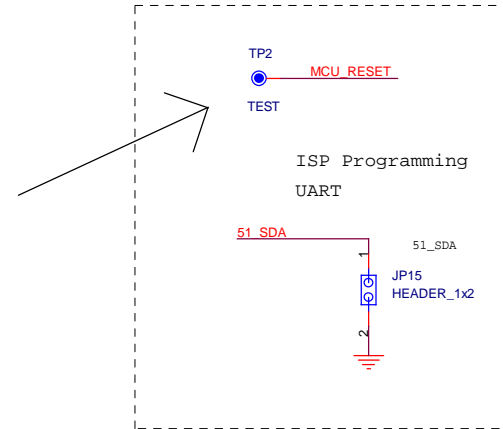
IT6261 APPLICATION CIRCUIT

DATE	VERSION	HISTORY	COMMENTS
10/7, 2009	1.0	The preliminary version is released.	
10/13, 2009	1.1	Add MCU for optional control	
11/11, 2009	1.2	Change to CS8953AR SSOP-28 MCU	
11/13, 2009	1.3	Add MCU_RESET for ISP	
11/24, 2009	1.4	Add LVDS DDC bus to read HDMI EDID	
12/08, 2009	1.5A	Add optional MCU STC12LE5630_TSSOP	
12/14, 2009	1.5B	Add SPDIF ON/OFF switch for option	
12/18, 2009	1.5C	Add R4 and R5. Remove JP14 and JP15, add JP1.	
2/3, 2010	1.5D	1. 增加 I2C 控制 From PC if necessary 2. 增加 U25 控制	

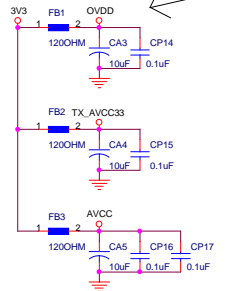
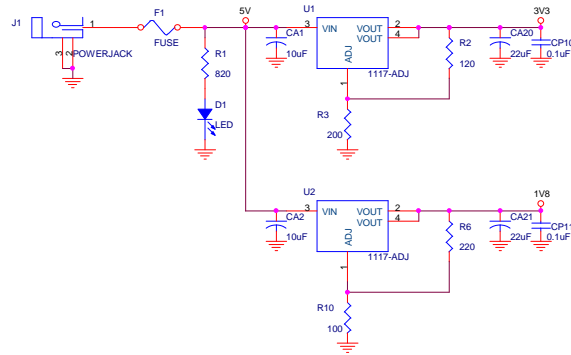
* This schematic is modified by MYSON
 * User should get confrimation with ITE before use
 * C78/C79 Add by MYSON



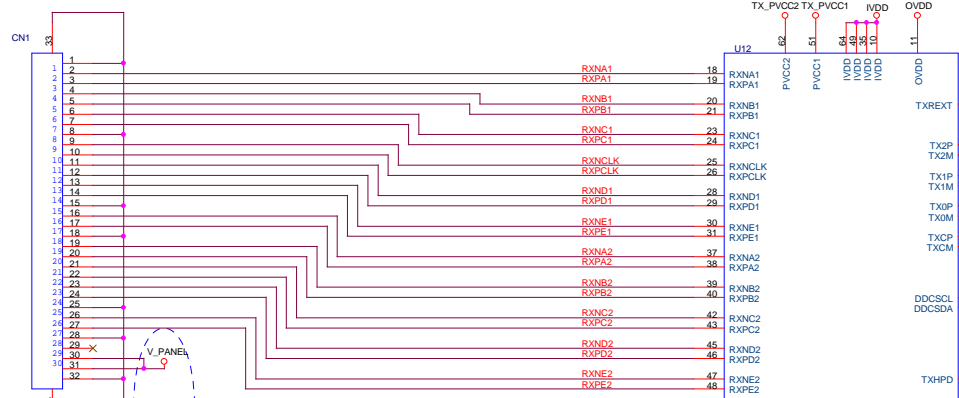
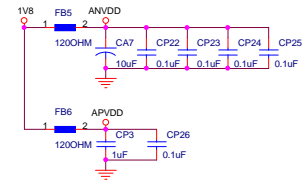
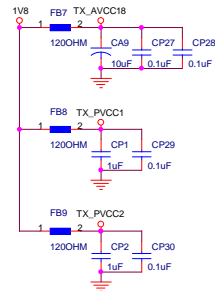
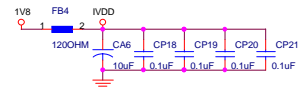
I2C control From PC



Title <Title>		
Size B	Document Number <Doc>	Rev <RevCode>
Date:	Thursday, June 10, 2010	Sheet 1 of 1



NOTES:
It is recommended to use same
OVDD for 6261 and mainchips
(LVDS and Audio transmitter).



Some LVDS source systems
provide a power to their
LVDS receiver systems.

AUDIO INTERFACE

Select audio interface to be I2S or SPDIF.

THERMAL PAD

IT6263

TXAVCC33

TXAVCC18

TXEMEM_VPP

SYSRSTN

AVCC

ANVDD

APVDD

TXALIN

TXALOUT

SCL_MCLK

WS_SPDIF

TX_I2S0

TX_I2S1

TX_I2S2

TX_I2S3

TX_I2S4

TX_I2S5

TX_I2S6

TX_I2S7

TX_I2S8

TX_I2S9

TX_I2S10

TX_I2S11

TX_I2S12

TX_I2S13

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TX_I2S198

TX_I2S199

TX_I2S200

TX_I2S201

TX_I2S202

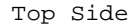
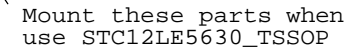
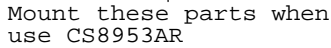
TX_I2S203

TX_I2S204

TX_I2S205

TX_I2S206

U23 and U24 are optional



Title			
IT6261 APPLICATION CIRCUIT			
Size B	Document Number <Doc>	Rev 1.5B	
Date:	Thursday, June 10, 2010	Sheet	3 of 3