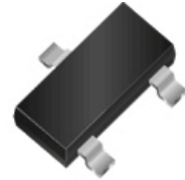
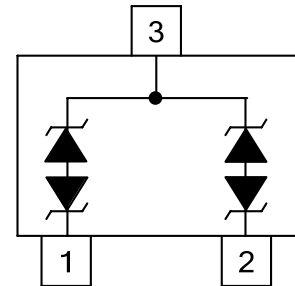


Features

- IEC61000-4-2 ESD 15KV Air, 8KV contact compliance
- SOT-23 surface mount package
- Protects bidirectional two I/O lines
- Peak power dissipation of 300W under 8/20 μ s waveform
- Working voltage: 24V
- Low leakage current
- Low operating and clamping voltages
- Solid-state silicon avalanche technology
- Lead Free/RoHS compliant
- Solder reflow temperature: Pure Tin-Sn, 260~270 $^{\circ}$ C
- Flammability rating UL 94V-0
- Meets MSL level 1, per J-STD-020


Applications

- RS-232 and RS-422 data lines
- Microprocessor based equipment
- LAN/WAN equipment
- Desktops PC and servers
- Notebook, Laptop and Palmtop computers
- Set Top Box
- Peripherals
- Serial and Parallel ports



Pin Configuration

Maximum Ratings

Rating	Symbol	Value	Unit
Peak pulse power (tp=8/20 μ s waveform)	P _{PP}	300	W
ESD voltage (Contact discharge)	V _{ESD}	± 8	kV
ESD voltage (Air discharge)		± 15	
Storage & operating temperature range	T _{STG} , T _J	-55~+150	$^{\circ}$ C

2-Line Bi-directional TVS Diode Array

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Reverse stand-off voltage	V_{RWM}				24	V
Reverse breakdown voltage	V_{BR}	$I_{BR}=1mA$	26.7			V
Reverse leakage current	I_R	$V_R=24V$ Each I/O pin			1	μA
Clamping voltage ($t_p=8/20\mu s$)	V_C	$I_{PP}=1A$			43	V
Clamping voltage ($t_p=8/20\mu s$)	V_C	$I_{PP}=5A$			52	V
Off state junction capacitance	C_J	0Vdc, f=1MHz Between I/O pins and GND		16		pF

Typical Characteristics Curves

Figure 1. Power Derating Curve

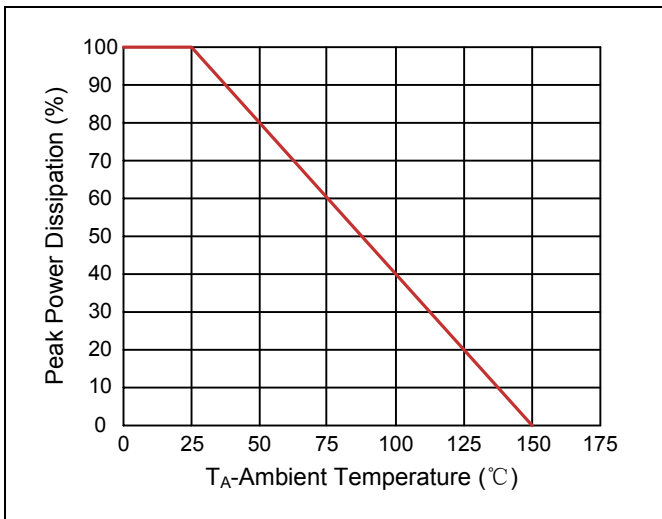


Figure 3. Non-Repetitive Peak Pulse vs. Pulse Time

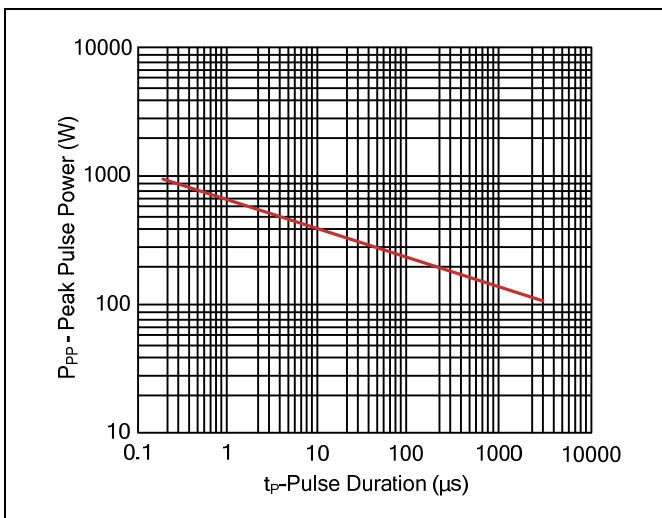


Figure 2. Pulse Waveforms

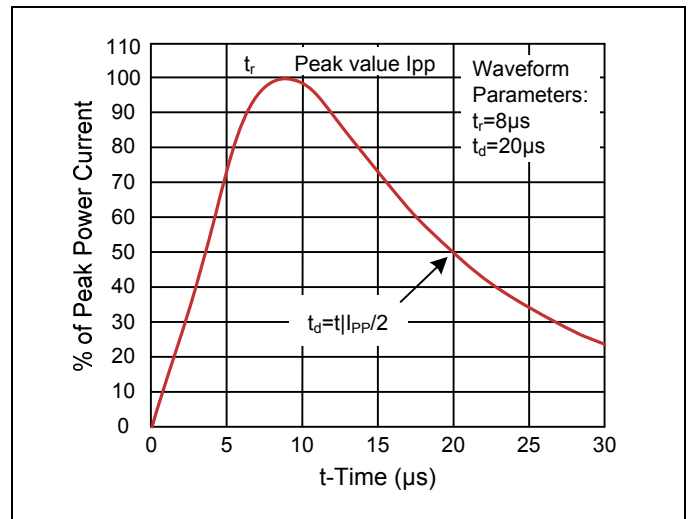
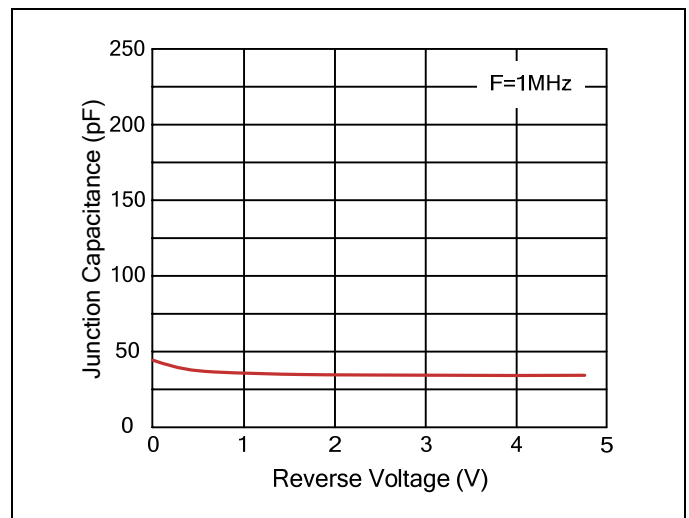
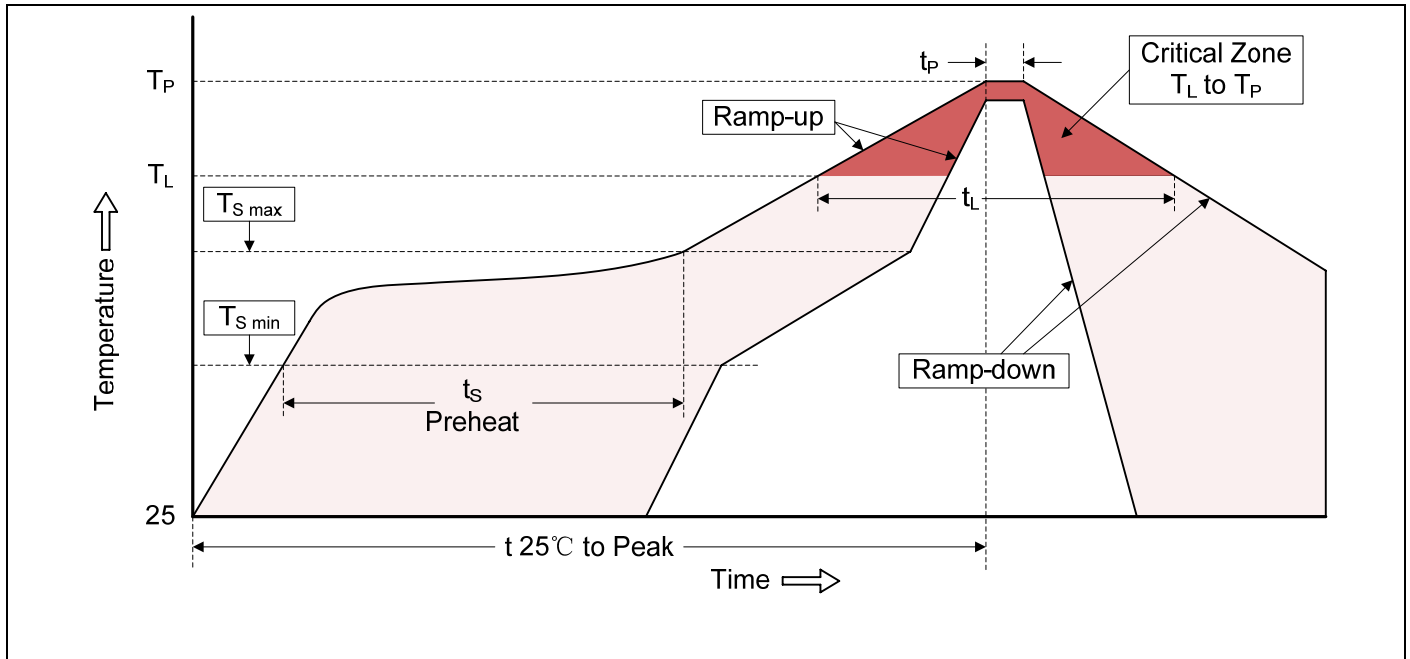


Figure 4. Normalized Capacitance vs. Reverse Voltage



Recommended Soldering Conditions

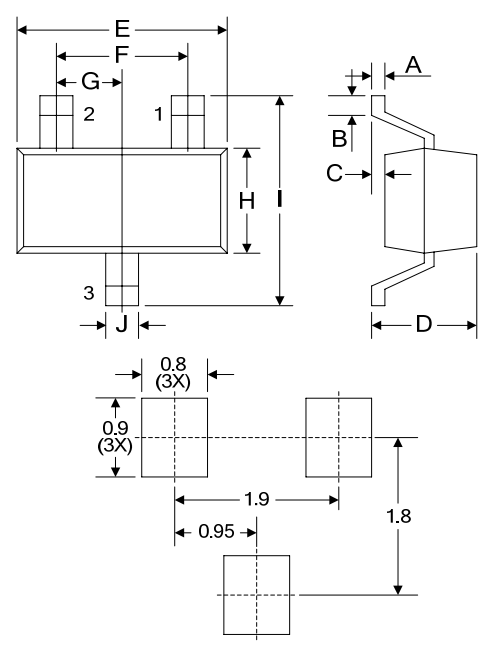


Recommended Conditions

Profile Feature	Pb-Free Assembly
Average ramp-up rate (TL to TP)	3°C/second max.
Preheat -Temperature Min (TSmin) -Temperature Max (TSmax) -Time (min to max) (ts)	150°C 200°C 60-180 seconds
TSmax to TL -Ramp-up Rate	3°C/second max.
Time maintained above: -Temperature (TL) -Time (tL)	217°C 60-150 seconds
Peak Temperature (TP)	260°C
Time within 5°C of actual Peak Temperature (tp)	20-40 seconds
Ramp-down Rate	6°C/second max.
Time 25°C to Peak Temperature	8 minutes max.

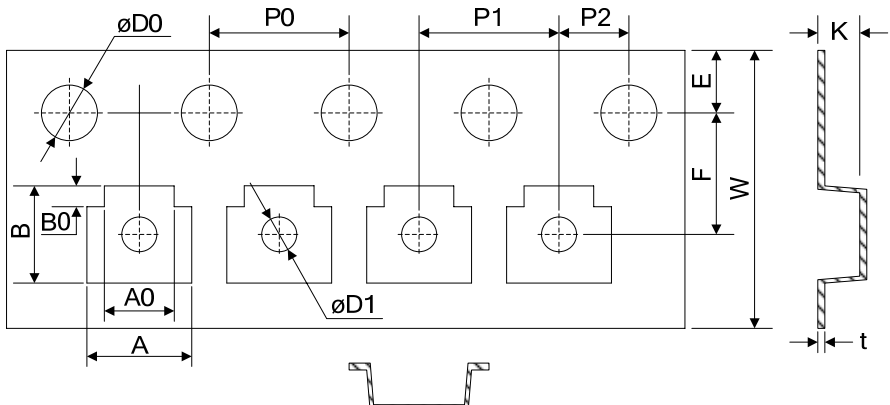
2-Line Bi-directional TVS Diode Array

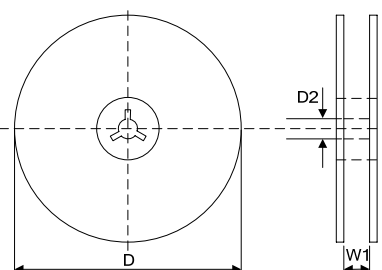
Symbol	Dimension SOT23			
	Millimeters		Inches	
	Min.	Max.	Min.	Max.
A	0.08	0.18	0.003	0.007
B	0.15	-	0.006	-
C	-	0.13	-	0.005
D	0.89	1.09	0.035	0.043
E	2.80	3.05	0.110	0.120
F	1.90		0.075	
G	0.95		0.037	
H	1.19	1.40	0.047	0.055
I	2.10	2.49	0.083	0.098
J	0.35	0.50	0.014	0.020



Recommended Soldering Pad Layout

Packaging

Tape	Symbol	Dimension (mm)
	W	8.00±0.30
	P0	4.00±0.10
	P1	4.00±0.10
	P2	2.00±0.10
	D0	Φ1.55±0.10
	D1	Φ1.00±0.05
	E	1.75±0.10
	F	3.50±0.10
	A	3.10±0.10
	A0	2.10±0.10
	B	2.75±0.10
	B0	0.65±0.10
	K	1.10±0.10
	t	0.20±0.05

Reel	Symbol	Dimension (mm)
	D	Φ178.0±2.0
	D2	Φ13.0
	W1	9.5
		Quantity: 3000PCS