

STPS2545C-Y

Automotive power Schottky rectifier

Datasheet - production data

Features

- Very small conduction losses
- Negligible switching losses
- Extremely fast switching
- Low thermal resistance
- Avalanche capability specified
- AEC-Q101 qualified

Description

Dual center tab Schottky rectifier suited for high frequency DC to DC converters.

This device is especially intended for use in low voltage, high frequency inverters, free-wheeling and polarity protection applications.

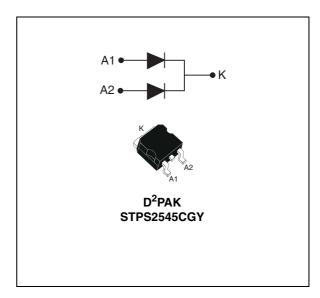


Table 1. Device summary

| I _{F(AV)} | 2 x 12.5 A |
|----------------------|------------|
| V_{RRM} | 45 V |
| T _{j (max)} | 175 °C |
| V _{F(max)} | 0.57 V |

Characteristics STPS2545C-Y

Characteristics 1

Absolute ratings (limiting values, per diode) Table 2.

| Symbol | Parameter | | | Value | Unit |
|---------------------|---|------|---|-------------|------|
| V_{RRM} | Repetitive peak reverse voltage | | | 45 | V |
| I _{F(RMS)} | Forward rms current | | | 30 | Α |
| I _{F(AV)} | Average forward current $\delta = 0.5$ | 12.5 | Α | | |
| I _{FSM} | Surge non repetitive forward current | 200 | Α | | |
| I _{RRM} | Repetitive peak reverse current | 1 | Α | | |
| I _{RSM} | Non repetitive peak reverse current | 2 | Α | | |
| P _{ARM} | Repetitive peak avalanche power | 4800 | W | | |
| T _{stg} | Storage temperature range | | | -65 to +175 | °C |
| T _j | Maximum operating junction temperature ⁽¹⁾ | | | -40 to +175 | °C |
| dV/dt | Critical rate of rise reverse voltage | | | 10000 | V/µs |

^{1.} $\frac{dPtot}{dT_j} < \frac{1}{Rth(j-a)}$ condition to avoid thermal runaway for a diode on its own heatsink

Table 3. Thermal resistances

| Symbol | Parameter | Value | Unit | |
|--|------------------|-----------|------|------|
| D | Junction to case | Per diode | 1.6 | °C/W |
| R _{th (j-c)} Junction to case | | Total | 1.1 | °C/W |
| R _{th (c)} | Coupling | | 0.6 | °C/W |

When the diodes 1 and 2 are used simultaneously:

 ΔT_j (diode 1) = P(diode1) x $R_{th(j-c)}$ (Per diode) + P(diode 2) x $R_{th(c)}$

Table 4. Static electrical characteristics (per diode)

| Symbol | Parameter | Tests conditions | | Min. | Тур. | Max. | Unit |
|---|--|-------------------------|-------------------------|------|------|------|------|
| I _R ⁽¹⁾ Reverse leakage current | T _j = 25 °C | V –V | i | - | 125 | μΑ | |
| 'R | I _R (1) Reverse leakage current | T _j = 125 °C | $V_R = V_{RRM}$ | - | 9 | 25 | mA |
| | V _F ⁽¹⁾ Forward voltage drop | T _j = 125 °C | I _F = 12.5 A | - | 0.50 | 0.57 | |
| V _F ⁽¹⁾ | | T _j = 25 °C | I _F = 25 A | - | - | 0.84 | V |
| | | T _j = 125 °C | I _F = 25 A | 1 | 0.65 | 0.72 | |

^{1.} Pulse test: t_p = 380 μ s, δ < 2%

To evaluate the conduction losses use the following equation: P = 0.42 x $I_{F(AV)}$ + 0.012 x I_{F}^{2} _(RMS)

$$P = 0.42 \text{ x } I_{F(AV)} + 0.012 \text{ x } I_{F^2(RMS)}$$

STPS2545C-Y Characteristics

Figure 1. Conduction losses versus average Figure 2. Average forward current versus current) ambient temperature (δ = 0.5)

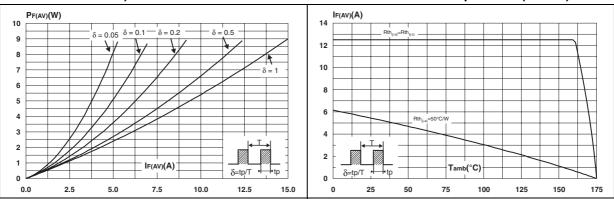


Figure 3. Normalized avalanche power derating versus pulse duration

Figure 4. Normalized avalanche power derating versus junction temperature

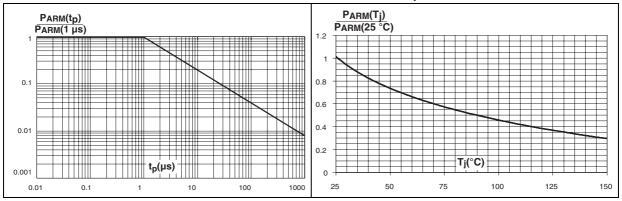
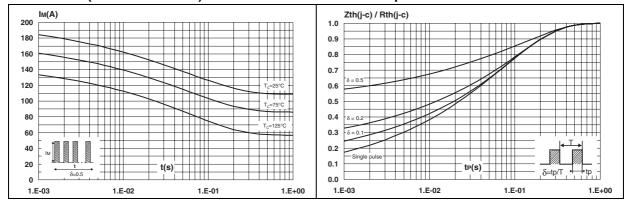


Figure 5. Non repetitive surge peak forward current versus overload duration (maximum values)

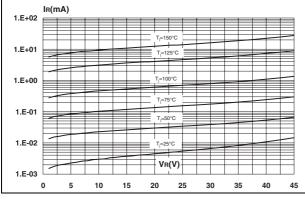
Figure 6. Relative variation of thermal impedance junction to case versus pulse duration



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Figure 7. Reverse leakage current versus reverse voltage applied (typical values)

Figure 8. Junction capacitance versus reverse voltage applied (typical values)



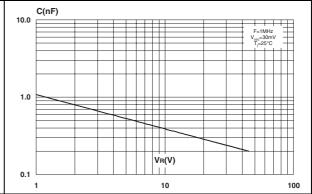
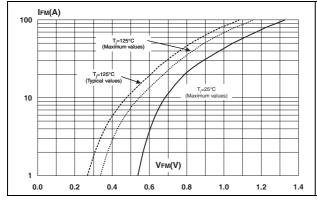
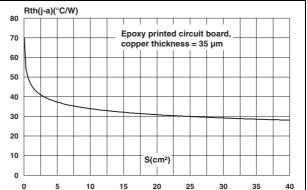


Figure 9. Forward voltage drop versus forward current

Figure 10. Thermal resistance junction to ambient versus copper surface under tab





STPS2545C-Y Package information

2 Package information

- Epoxy meets UL94, V0
- Lead-free package

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: www.st.com. ECOPACK[®] is an ST trademark.

Table 5. D²PAK dimensions

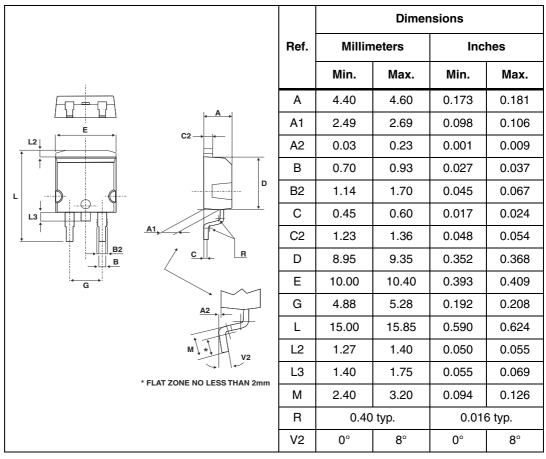
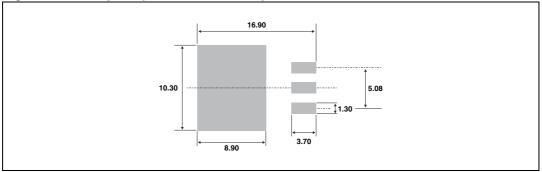


Figure 11. Footprint (dimensions in mm)



Ordering information STPS2545C-Y

3 Ordering information

Table 6. Ordering information

| Order code | Marking | Package | Weight | Base qty | Delivery mode |
|----------------|-------------|--------------------|--------|----------|---------------|
| STPS2545CGY-TR | STPS2545CGY | D ² PAK | 1.48 g | 1000 | Tape and reel |

4 Revision history

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Table 7. Document revision history

| Date | Revision | Changes | |
|-------------|----------|---|--|
| 03-Nov-2011 | 1 | Initial release. | |
| 28-Jun-2012 | 2 | Corrected typographical error in <i>Table 3</i> . | |

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