



DMG2305UX

Product Summary

BV _{DSS}	R _{DS(ON)} Max	Package	I _D Max T _A = +25°C
-20V	52mΩ @V _{GS} = -4.5V	SOT23	-5.0A
-200	$100m\Omega @V_{GS} = -2.5V$	30123	-3.6A

Description

This MOSFET is designed to minimize the on-state resistance $(R_{DS(ON)})$, yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Backlighting
- Power Management Functions
- DC-DC Converters
- Motor Control

Features

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)

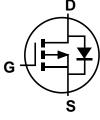
P-CHANNEL ENHANCEMENT MODE MOSFET

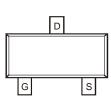
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- An Automotive-Compliant Part is Available Under Separate Datasheet (<u>DMG2305UXQ</u>)

Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 3
- Terminals Connections: See Diagram Below
- Weight: 0.009 grams (Approximate)







Top View

Top View

Internal Schematic

Ordering Information (Note 4)

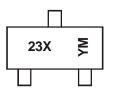
	Part Number	Compliance	Case	Packaging			
	DMG2305UX-7	Standard	SOT23	3,000/Tape & Reel			
	DMG2305UX-13	Standard	SOT23	10,000/Tape & Reel			
Notes:	otes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.						

No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



23X = Product Type Marking Code YM = Date Code Marking Y or \overline{Y} = Year (ex: F = 2018) M = Month (ex: 9 = September)

Date Code Key

Year	2009	~	2016	2017	2018	2019	2020	2021	2022	2023	2024	2025	2026
Code	W	~	D	E	F	G	Н		J	K	L	М	Ν
Mon	th	Jan	Feb	Mar	Apr	Mav	Jun	Jul	Aug	Sep	Oct	Nov	Dec



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage		V _{DSS}	-20	V	
Gate-Source Voltage			V _{GSS}	±8	V
	Steady State	T _A = +25°C T _A = +70°C	Ι _D	-4.2 -3.3	А
Continuous Drain Current (Note 5) $V_{GS} = -4.5V$	t<10s	T _A = +25°C T _A = +70°C	Ι _D	-5.0 -4.0	A
Pulsed Drain Current (10µs Pulse, Duty Cycle =	6)	IDM	-15	A	

Thermal Characteristics

Characteristic	Symbol	Value	Unit	
Power Dissipation (Note 5)	PD	1.4	W	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	D	90	°C/W
mermai Resistance, Junction to Ambient (Note 5)	t<10s	$R_{ extsf{ heta}JA}$	64	°C/W
Thermal Resistance, Junction to Case (Note 7)	R _{eJC}	33	°C/W	
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	С°	

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						·
Drain-Source Breakdown Voltage	BV _{DSS}	-20		_	V	$V_{GS} = 0V, I_D = -250\mu A$
Zero Gate Voltage Drain Current $T_J = +25^{\circ}C$	IDSS	_		-1.0	μA	$V_{DS} = -20V, V_{GS} = 0V$
Gate-Source Leakage	IGSS	_		±100	nA	$V_{GS} = \pm 8V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	-0.5	_	-0.9	V	$V_{DS} = V_{GS}$, $I_D = -250 \mu A$
		_	40	52		$V_{GS} = -4.5V, I_D = -4.2A$
Static Drain-Source On-Resistance	R _{DS(ON)}		52	100	mΩ	$V_{GS} = -2.5V, I_D = -3.4A$
			68	200		V _{GS} = -1.8V, I _D = -2A
Forward Transfer Admittance	Y _{FS}	_	9	_	S	$V_{DS} = -5V, I_{D} = -4A$
DYNAMIC CHARACTERISTICS (Note 8)						-
Input Capacitance	Ciss	_	808		pF	
Output Capacitance	Coss	_	85	_	pF	─V _{DS} = -15V, V _{GS} = 0V −f = 1.0MHz
Reverse Transfer Capacitance	Crss	_	77	—	pF	1 = 1.000112
Gate Resistance	Rg		15.2		Ω	$V_{GS} = 0V, V_{DS} = 0V, f = 1.0MHz$
SWITCHING CHARACTERISTICS (Note 8)						
Total Gate Charge	Qg	_	10.2	_	nC	
Gate-Source Charge	Qgs	_	1.3	—	nC	$V_{GS} = -4.5V, V_{DS} = -4V,$
Gate-Drain Charge	Q _{gd}	_	2.2	_	nC	$I_{\rm D} = -3.5 {\rm A}$
Turn-On Delay Time	t _{D(ON)}	_	10.8		ns	
Turn-On Rise Time	t _R	_	13.7	_	ns	$V_{DS} = -4V, V_{GS} = -4.5V,$
Turn-Off Delay Time	t _{D(OFF)}	_	79.3	_	ns	$R_g = 6\Omega, I_D = -1A$
Turn-Off Fall Time	t _F	_	34.7	_	ns	

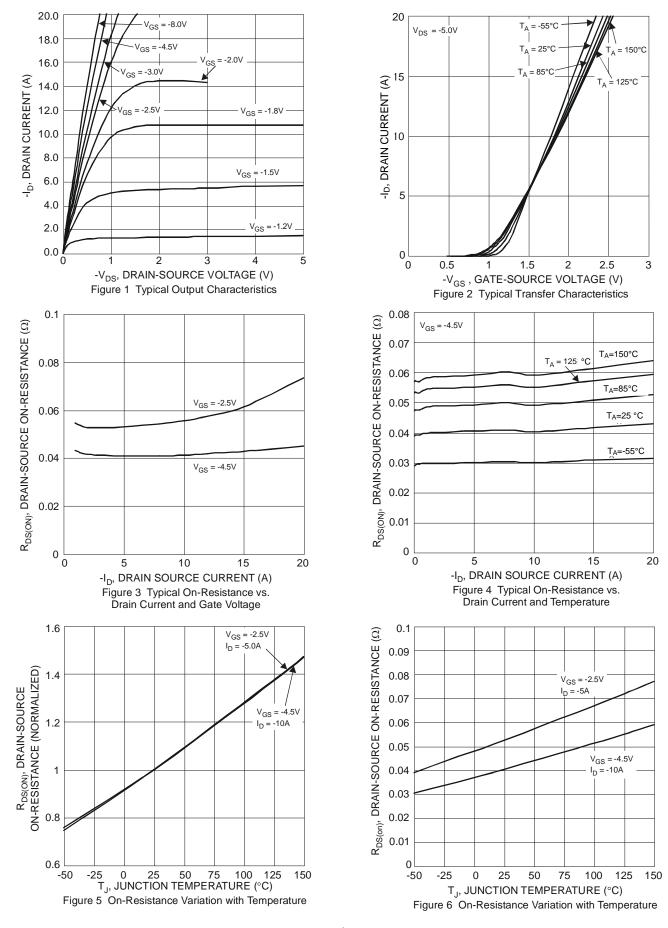
Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with 1-inch square copper plate.

Repetitive rating, pulse width limited by junction temperature.
 Short duration pulse test used to minimize self-heating effect.

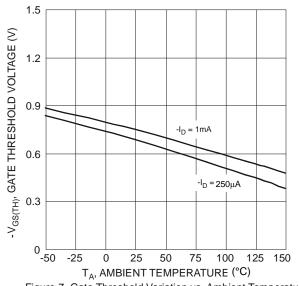
8. Guaranteed by design. Not subject to product testing.



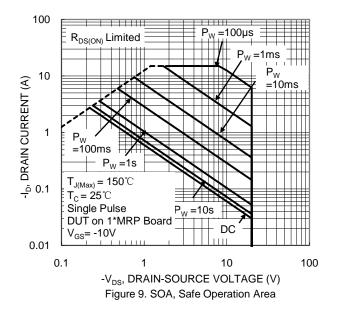
DMG2305UX

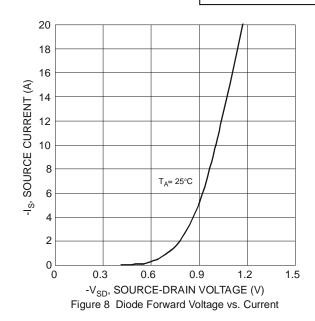








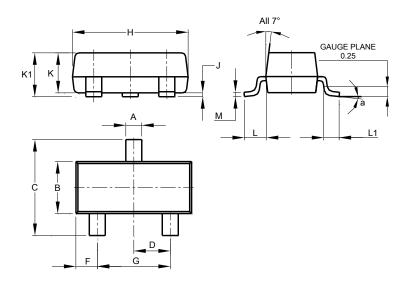






Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

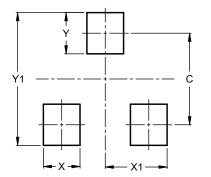


	SOT23							
Dim	Min	Max	Тур					
Α	0.37	0.51	0.40					
в	1.20	1.40	1.30					
С	2.30	2.50	2.40					
D	0.89	1.03	0.915					
F	0.45	0.60	0.535					
G	1.78	2.05	1.83					
Н	2.80	3.00	2.90					
J	0.013	0.10	0.05					
κ	0.890	1.00	0.975					
K1	0.903	1.10	1.025					
L	0.45	0.61	0.55					
L1	0.25	0.55	0.40					
Μ	0.085	0.150	0.110					
а	0°	8°	_					
All	Dimens	ions in	mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23



Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Y	0.9
Y1	2.9



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