General Description

The WSF70N10 is the highest performance trench N-Ch MOSFET with extreme high cell density , which provide excellent RDSON and gate charge for most of the synchronous buck converter applications .

The WSF70N10 meet the RoHS and Green Product requirement,100% EAS guaranteed with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

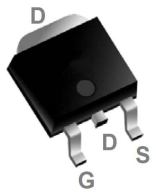
Product Summery

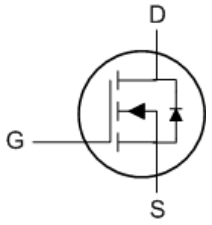
BV_{DSS}	R_{DSON}	I_D
100V	10mΩ	70A

Applications

- Power Management in TV Converter.
- DC-DC Converter
- LED TV Back Light

TO-252 Pin Configuration





Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	100	V
V_{GS}	Gate-Source Voltage	±25	V
I_D@T_C=25℃	Continuous Drain Current, V_{GS} @ 10V[1]	70	A
I_D@T_C=100℃	Continuous Drain Current, V_{GS} @ 10V[1]	35	A
I_D@T_A=25℃	Continuous Drain Current, V_{GS} @ 10V[1]	8.2	A
I_D@T_A=70℃	Continuous Drain Current, V_{GS} @ 10V[1]	6.6	A
I_{DM}	Pulsed Drain Current[2],T_C=25°C	150	A
EAS	Avalanche Energy, Single pulse,L=0.5mH	169	mJ
I_{AS}	Avalanche Current, Single pulse,L=0.5mH	26	A
P_D@T_C=25℃	Total Power Dissipation[4]	113	W
P_D@T_C=100℃	Total Power Dissipation[4]	45	W
T_{STG}	Storage Temperature Range	-55 to 150	℃
T_J	Operating Junction Temperature Range	150	℃

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient [1]	---	50	℃/W
$R_{\theta JC}$	Thermal Resistance Junction-Case[1]	---	1.1	℃/W

Electrical Characteristics (T_J=25℃, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V , I_D=250uA	100	---	---	V
$\triangle BV_{DSS}/\triangle T_J$	BV_{DSS} Temperature Coefficient	Reference to 25℃ , I_D=1mA	---	0.096	---	V/℃
$R_{DS(ON)}$	Static Drain-Source On-Resistance[2]	V_{GS}=10V , I_D=30A	---	10	13	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS}=V_{DS} , I_D =250uA	2.0	3.0	4.0	V
$\triangle V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	-5.5	---	mV/℃
I_{DSS}	Drain-Source Leakage Current	V_{DS}=80V , V_{GS}=0V , T_J=25℃	---	---	1	uA
		V_{DS}=80V , V_{GS}=0V , T_J=55℃	---	---	5	
I_{GSS}	Gate-Source Leakage Current	V_{GS}=±20V , V_{DS}=0V	---	---	±100	nA
gfs	Forward Transconductance	V_{DS}=5V , I_D=30A	---	27	---	S
R_g	Gate Resistance	V_{DS}=0V , V_{GS}=0V , f=1MHz	---	1.0	1.8	Ω
Q_g	Total Gate Charge (10V)	V_{DS}=80V , V_{GS}=10V , I_D=30A	---	42	---	nC
Q_{gs}	Gate-Source Charge		---	12	---	
Q_{gd}	Gate-Drain Charge		---	12	---	
$T_{d(on)}$	Turn-On Delay Time	V_{DD}=50V , V_{GS}=10V , R_G=3Ω, I_D=1A	---	19	---	ns
T_r	Rise Time		---	9	---	
$T_{d(off)}$	Turn-Off Delay Time		---	36	---	
T_f	Fall Time		---	22	---	
C_{iss}	Input Capacitance	V_{DS}=15V , V_{GS}=0V , f=1MHz	---	2100	---	pF
C_{oss}	Output Capacitance		---	255	---	
C_{rss}	Reverse Transfer Capacitance		---	100	---	

Guaranteed Avalanche Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
EAS	Single Pulse Avalanche Energy[5]	V_{DD}=25V , L=0.5mH , I_{AS}=26A	150	---	---	mJ

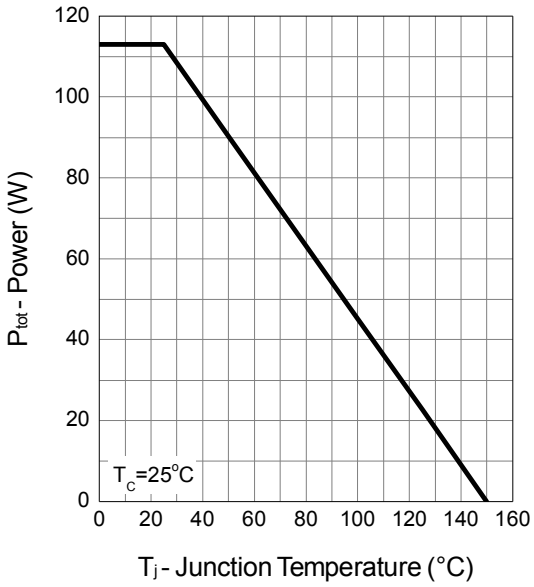
Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current[1,6]	V_G=V_D=0V , Force Current	---	---	30	A
I_{SM}	Pulsed Source Current[2,6]		---	---	60	A
V_{SD}	Diode Forward Voltage[2]	V_{GS}=0V , I_S=15A , T_J=25℃	---	---	1.3	V
t_{rr}	Reverse Recovery Time	IF=15A,dI/dt=100A/μs,T_J=25℃	---	42	---	nS
Q_{rr}	Reverse Recovery Charge		---	90	---	nC

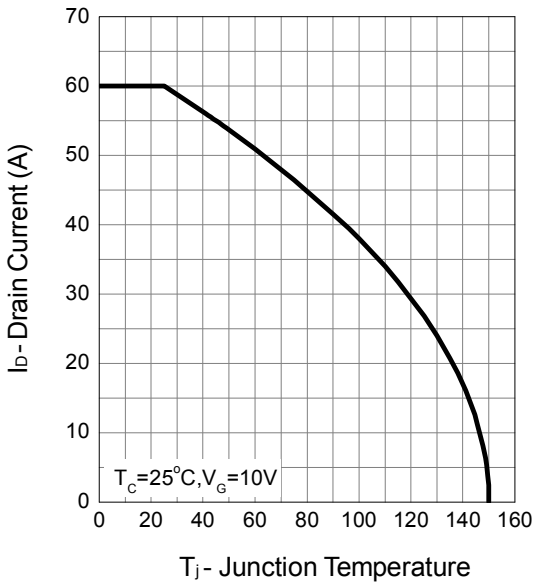
Note :
1.The data tested by surface mounted on a 1 inch^2 FR-4 board with 2OZ copper,t<10sec.
2.The data tested by pulsed , pulse width ≦ 300us , duty cycle ≦ 2%
3.The EAS data shows Max. rating . The test condition is V_{DS}=25V,V_{GS}=10V,L=0.5mH,I_{AS}=26A
4.The power dissipation is limited by 150℃ junction temperature
5.The Min. value is 100% EAS tested guarantee.
6.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

Typical Operating Characteristics

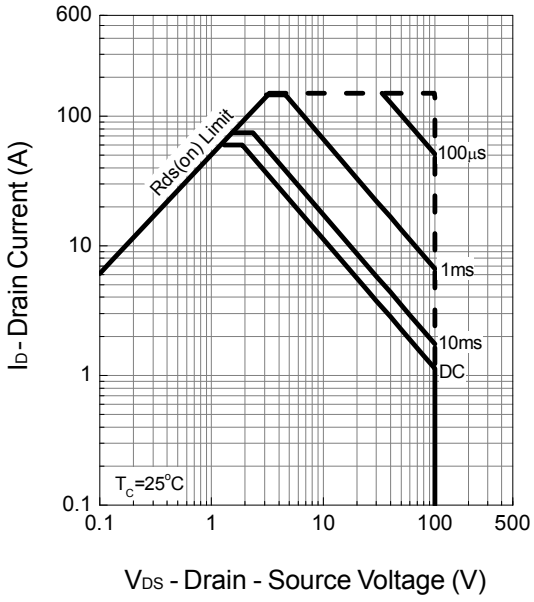
Power Dissipation



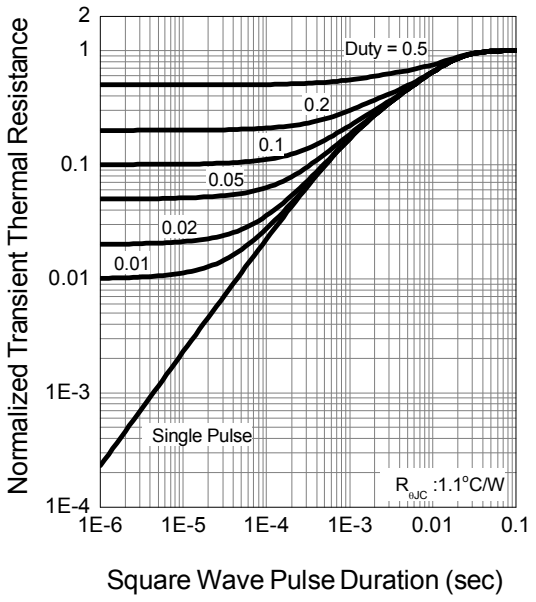
Drain Current



Safe Operation Area

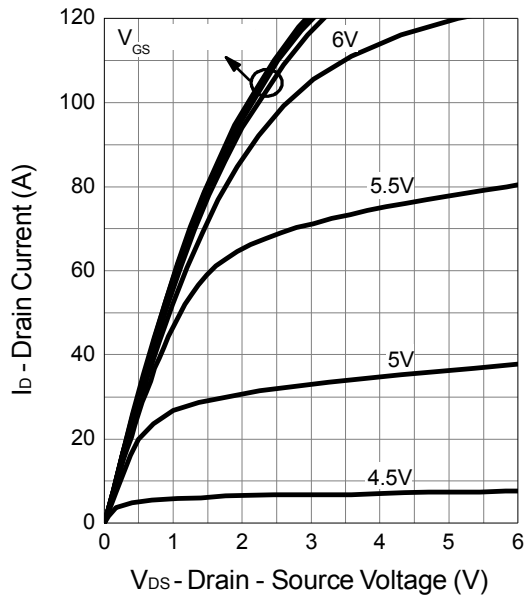


Thermal Transient Impedance

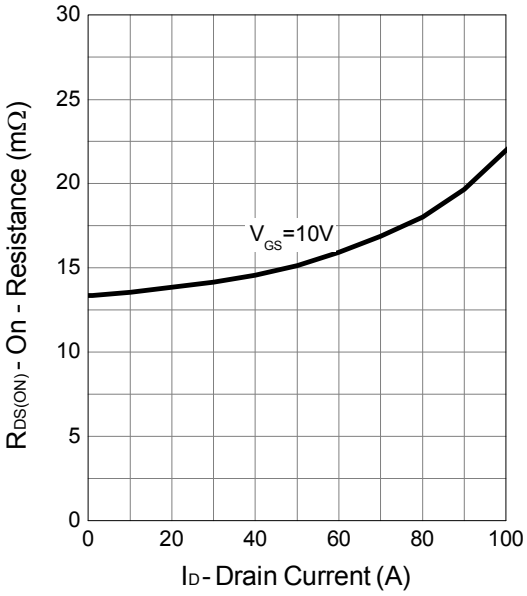


Typical Operating Characteristics (Cont.)

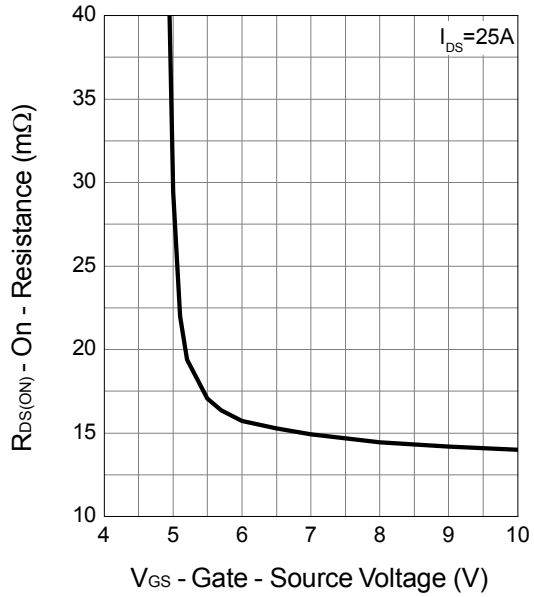
Output Characteristics



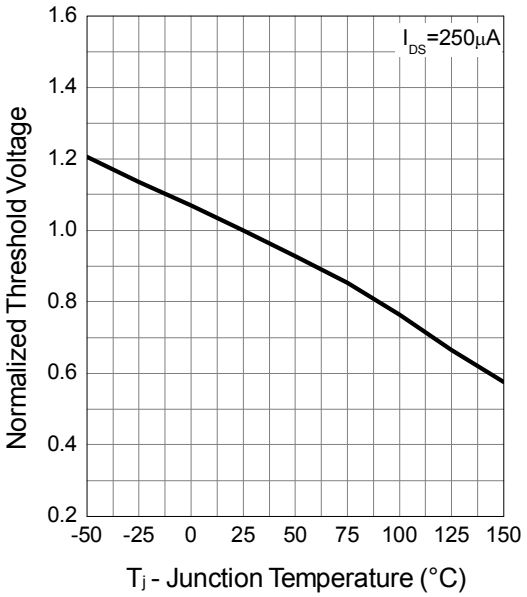
Drain-Source On Resistance



Gate-Source On Resistance

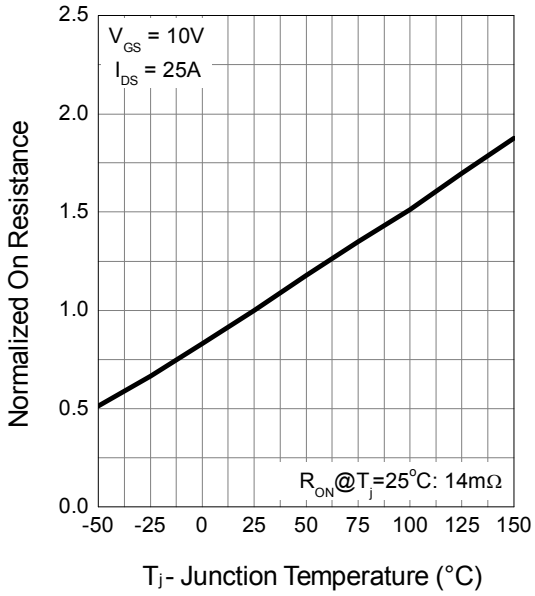


Gate Threshold Voltage

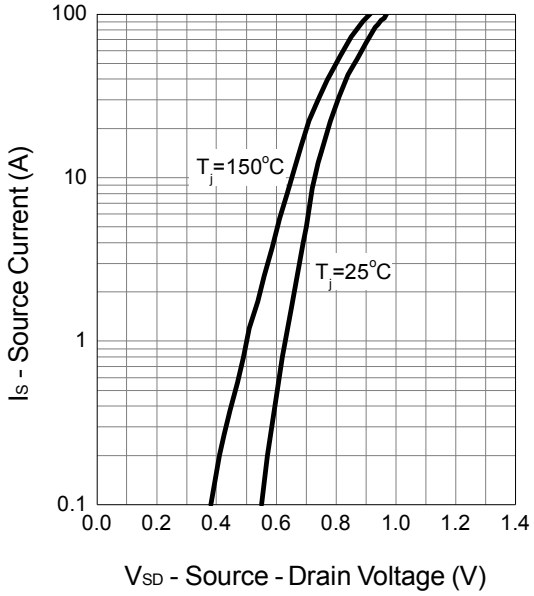


Typical Operating Characteristics (Cont.)

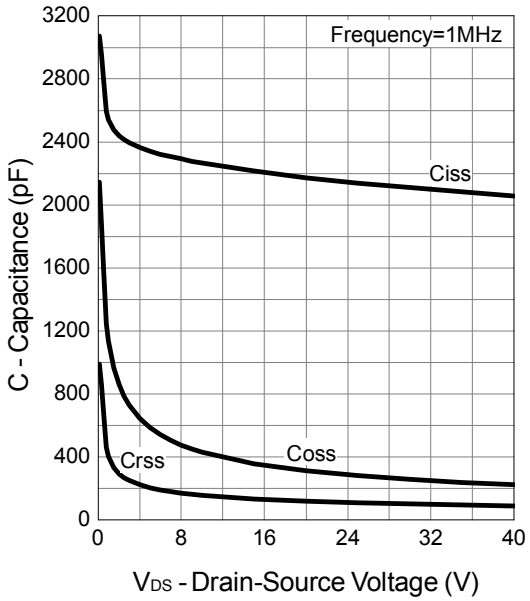
Drain-Source On Resistance



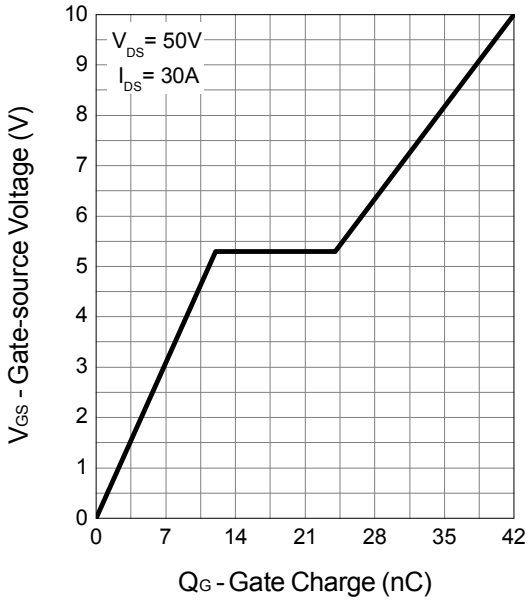
Source-Drain Diode Forward



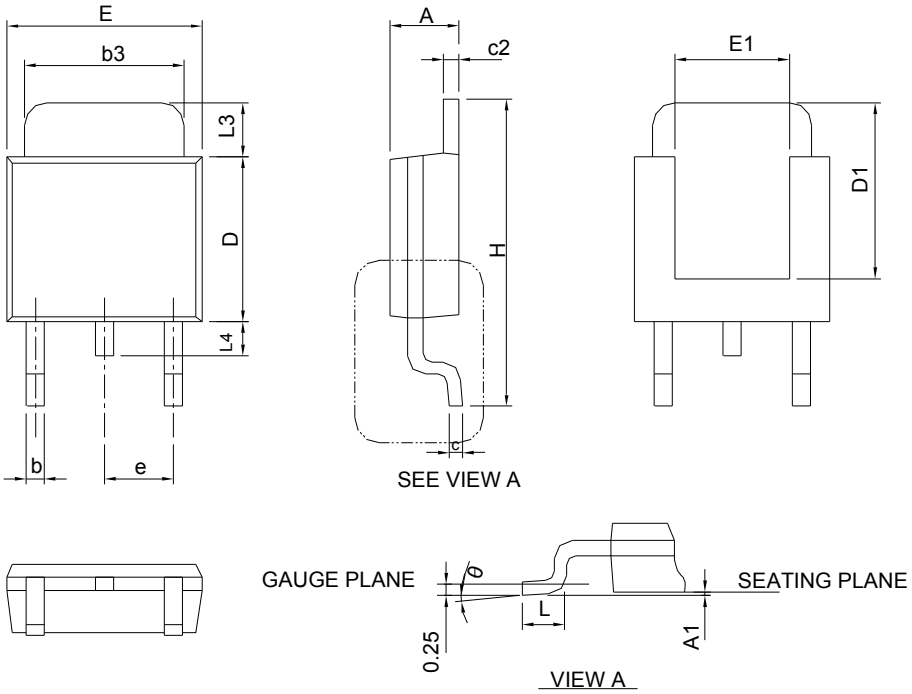
Capacitance



Gate Charge

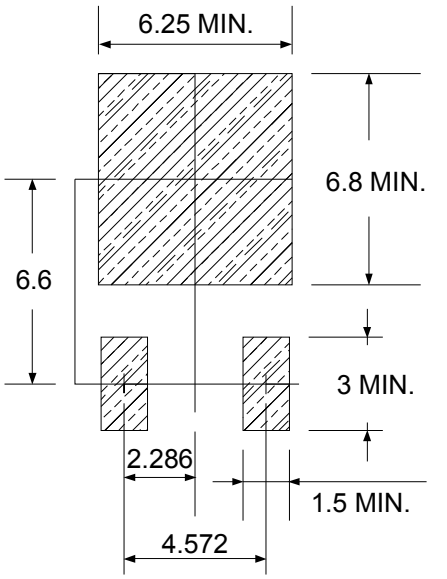


Package Information: TO-252



SYMBOLS	TO-252			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	2.18	2.39	0.086	0.094
A1	-	0.13	-	0.005
b	0.50	0.89	0.020	0.035
b3	4.95	5.46	0.195	0.215
c	0.46	0.61	0.018	0.024
c2	0.46	0.89	0.018	0.035
D	5.33	6.22	0.210	0.245
D1	4.57	6.00	0.180	0.236
E	6.35	6.73	0.250	0.265
E1	3.81	6.00	0.150	0.236
e	2.29 BSC		0.090 BSC	
H	9.40	10.41	0.370	0.410
L	0.90	1.78	0.035	0.070
L3	0.89	2.03	0.035	0.080
L4	-	1.02	-	0.040
θ	0°	8°	0°	8°

RECOMMENDED LAND PATTERN



UNIT: mm

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