





N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)}	I _D T _A = +25°C
-01/	2.0Ω @ V _{GS} = 5.0V	300mA
50V	2.5Ω @ V _{GS} = 2.5V	200mA

Features and Benefits

- Low On-Resistance
- Very Low Gate Threshold Voltage (1.0V max)
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected Up To 2kV
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Description and Applications

This new generation 50V N-channel enhancement mode MOSFET is designed to minimize RDS(on) yet maintain superior switching performance. This device is ideal for use in notebook battery power management and load switch.

- Load Switches
- Level Switches

Mechanical Data

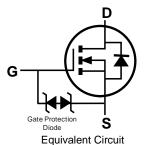
- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish—Matte Tin Annealed over Alloy 42 Leadframe.
 Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Weight: 0.008 grams (Approximate)

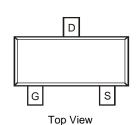




SOT23

Top View





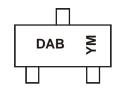
Ordering Information (Note 4)

Part Number	Qualification	Case	Packaging
DMN5L06K-7	Commercial	SOT23	3000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



DAB = Product Type Marking Code YM = Date Code Marking Y = Year (ex: G = 2019) M = Month (ex: 9 = September)

Date Code Key

Year	2006	~	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028
Code	Т	1	G	Η		7	K	L	М	N	0	Р
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

	Characteristic	Symbol	Value	Unit
Drain Source Voltage		V_{DSS}	50	V
Gate-Source Voltage		V_{GSS}	±20	V
Drain Current (Note 5)	Continuous Pulsed (Note 6)	l _D	300 800	mA

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P_{D}	350	mW
Thermal Resistance, Junction to Ambient	R _{OJA}	357	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-65 to +150	°C

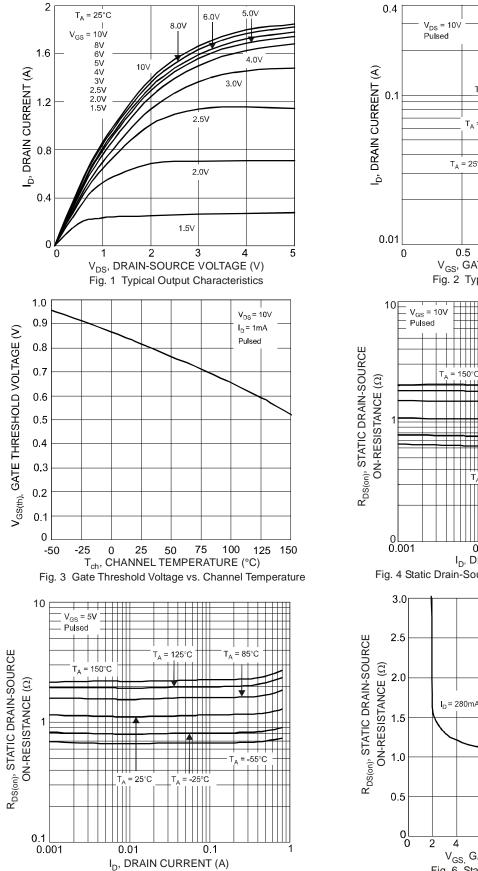
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

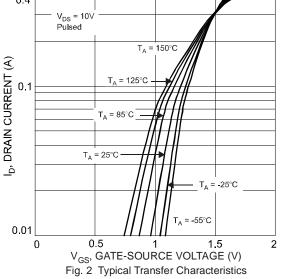
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS (Note 7)					•	•		
Drain-Source Breakdown Voltage		BV _{DSS}	50	_	_	V	$V_{GS} = 0V, I_D = 10\mu A$	
Zero Gate Voltage Drain Current	@ T _C = +25°C	I _{DSS}	_	_	60	nA	$V_{DS} = 50V, V_{GS} = 0V$	
					1	μA	$V_{GS} = \pm 12V, V_{DS} = 0V$	
Gate-Body Leakage		I_{GSS}	_	_	500	nА	$V_{GS} = \pm 10V$, $V_{DS} = 0V$	
					50	nA	$V_{GS} = \pm 5V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)								
Gate Threshold Voltage		V _{GS(th)}	0.49	_	1.0	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
			_	_	3.0		$V_{GS} = 1.8V, I_D = 50mA$	
Static Drain-Source On-Resistance		R _{DS(ON)}	_	_	2.5	Ω	$V_{GS} = 2.5V, I_D = 50mA$	
		, ,	_	_	2.0		$V_{GS} = 5.0V, I_D = 50mA$	
On-State Drain Current		$I_{D(ON)}$	0.5	1.4	_	Α	$V_{GS} = 10V, V_{DS} = 7.5V$	
Forward Transconductance		Y _{fs}	200	_	_	mS	$V_{DS} = 10V, I_{D} = 0.2A$	
Source-Drain Diode Forward Voltage		V _{SD}	0.5	_	1.4	V	$V_{GS} = 0V, I_{S} = 115mA$	
DYNAMIC CHARACTERISTICS								
Input Capacitance		Ciss	_	_	50	pF	\\ 05\\\\\ 0\\	
Output Capacitance		Coss	_	_	25	pF	$V_{DS} = 25V, V_{GS} = 0V$	
Reverse Transfer Capacitance		Crss	_	_	5.0	pF	f = 1.0MHz	

Notes:

- 5. Device mounted on FR-4 PCB
- 6. Pulse width ≤10mS, Duty Cycle ≤1%.7. Short duration pulse test used to minimize self-heating effect.







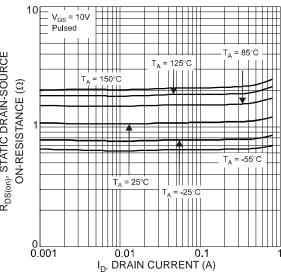


Fig. 4 Static Drain-Source On-Resistance vs. Drain Current

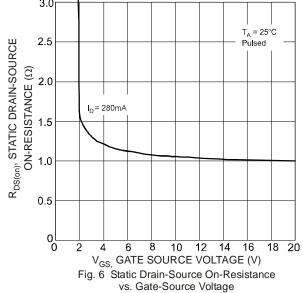


Fig. 5 Static Drain-Source On-Resistance vs. Drain Current



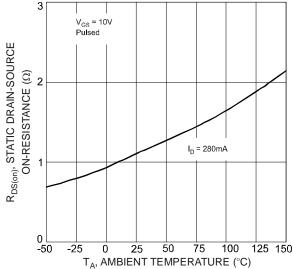


Fig. 7 Static Drain-Source On-State Resistance vs. Ambient Temperature

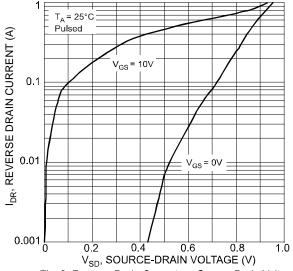
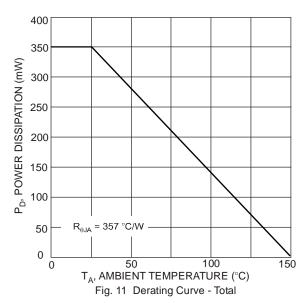


Fig. 9 Reverse Drain Current vs. Source-Drain Voltage



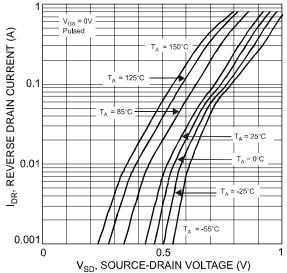


Fig. 8 Reverse Drain Current vs. Source-Drain Voltage

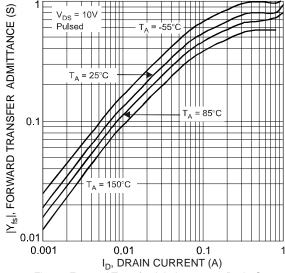
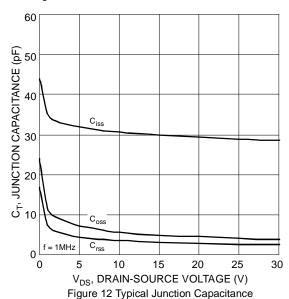


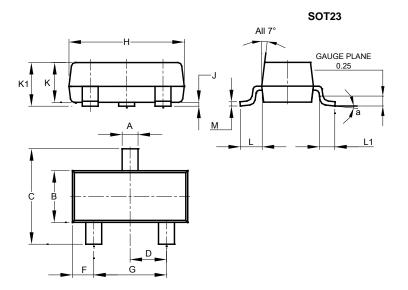
Fig.10 Forward Transfer Admittance vs. Drain Current





Package Outline Dimensions

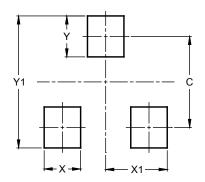
Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT23						
Dim	Min	Max	Тур			
Α	0.37	0.51	0.40			
В	1.20	1.40	1.30			
С	2.30	2.50	2.40			
D	0.89	1.03	0.915			
F	0.45	0.60	0.535			
G	1.78	2.05	1.83			
Н	2.80	3.00	2.90			
J	0.013	0.10	0.05			
K	0.890	1.00	0.975			
K1	0.903	1.10	1.025			
L	0.45	0.61	0.55			
L1	0.25	0.55	0.40			
М	0.085	0.150	0.110			
а	0°	8°				
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT23

Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Y	0.9
Y1	2.9



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DMN5L06K Document number: DS30929 Rev. 9 - 2