

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74LCX240F, TC74LCX240FK

Low-Voltage Octal Bus Buffer (inverted) with 5-V Tolerant Inputs and Outputs

The TC74LCX240 is a high-performance CMOS octal bus buffer. Designed for use in 3.3-V systems, it achieves high-speed operation while maintaining the CMOS low power dissipation.

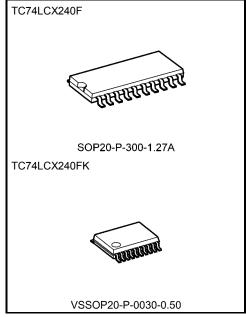
The device is designed for low-voltage $(3.3\ V)\ V_{CC}$ applications, but it could be used to interface to 5-V supply environment for both inputs and outputs.

The 74LCX240 is an inverting 3-state buffer having two active-low output enables. This device is designed to be used with 3-state memory address drivers, etc.

All inputs are equipped with protection circuits against static discharge.

Features

- Low-voltage operation: $V_{CC} = 1.65$ to 3.6 V
- High-speed operation: $t_{pd} = 6.5 \text{ ns (max) (VCC} = 3.0 \text{ to } 3.6 \text{ V)}$
- Ouput current: |IOH|/IOL = 24 mA (min) (VCC = 3.0 V)
- Latch-up performance: $> \pm 500 \text{ mA}$
- Available in JEITA SOP, VSSOP (US)
- Power-down protection provided on all inputs and outputs
- Pin and function compatible with the 74 series (74AC/VHC/HC/F/ALS/LS etc.) 240 type



Weight

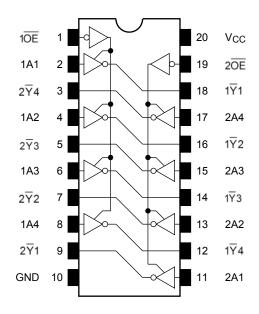
SOP20-P-300-1.27A : 0.22 g (typ.) VSSOP20-P-0030-0.50 : 0.03 g (typ.)

Note: The Electrical Characteristics of V_{CC} = 1.8 \pm 0.15 V is only applicable for products which manufactured from January 2009 onward.

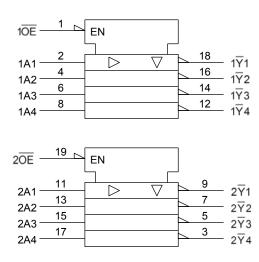
Start of commercial production 1994-03



Pin Assignment (top view)



IEC Logic Symbol



Truth Table

Inp	uts	Outputs
ŌĒ	An	Outputs
L	L	Н
L	Н	L
Н	Х	Z

X: Don't care

Z: High impedance

Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Power supply voltage	Vcc	-0.5 to 7.0	V
DC input voltage	V _{IN}	−0.5 to 7.0	V
		–0.5 to 7.0 (Note 2)	
DC output voltage	Vоит	-0.5 to V _{CC} + 0.5 (Note 3)	V
Input diode current	lıĸ	-50	mA
Output diode current	lok	±50 (Note 4)	mA
DC output current	lout	±50	mA
Power dissipation	PD	180	mW
DC V _{CC} /ground current	I _{CC} /I _{GND}	±100	mA
Storage temperature	T _{stg}	-65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: Output in OFF state

Note 3: High or low state. IOUT absolute maximum rating must be observed.

Note 4: VOUT < GND, VOUT > VCC



Operating Ranges (Note 1)

Characteristics	Symbol	Rating	Unit	
Dower ownsky voltage	Vac	1.65 to 3.6	V	
Power supply voltage	Vcc	1.5 to 3.6 (Note 2)	V	
Input voltage	VIN	0 to 5.5	V	
Output voltage	Vout	0 to 5.5 (Note 3)	V	
Output voltage		0 to V _{CC} (Note 4)	V	
Output ourropt	IOH/IOL	±24 (Note 5)	mA	
Output current	IOH/IOL	±12 (Note 6)	ША	
Operating temperature	Topr	-40 to 85	°C	
Input rise and fall time	dt/dv	0 to 10 (Note 7)	ns/V	

- Note 1: The operating ranges must be maintained to ensure the normal operation of the device.

 Unused inputs must be tied to either VCC or GND.
- Note 2: Data retention only
 Note 3: Output in OFF state
 Note 4: High or low state
 Note 5: VCC = 3.0 to 3.6 V
 Note 6: VCC = 2.7 to 3.0 V
- Note 7: VIN = 0.8 to 2.0 V, VCC = 3.0 V



Electrical Characteristics

DC Characteristics (Ta = -40 to 85°C)

Characteris	stics	Symbol	Test Condition		Min	Max	Unit	
	1				Vcc (V)			
					1.65 to 2.3	V _{CC} ×0.9	_	
	H-level	VIH	_		2.3 to 2.7	1.7	_	
Input voltage					2.7 to 3.6	2.0	_	V
input voltage					1.65 to 2.3		V _{CC} × 0.1	v
	L-level	VIL	_	-	2.3 to 2.7	_	0.7	
					2.7 to 3.6	_	0.8	
			-	$IOH = -100 \mu A$	1.65 to 3.6	Vcc-0.2	_	
				IOH = -4 mA	1.65	1.05	_	
				I _{OH} = -8 mA	2.3	1.7	_	V
	H-level	Voh	VIN = VIH or VIL	I _{OH} = -12 mA	2.7	2.2	_	
				IOH = -18 mA	3.0	2.4	_	
Output voltage				IOH = -24 mA	3.0	2.2	_	
Output voltage			VOL VIN = VIH or VIL	I _{OL} = 100 μA	1.65 to 3.6	_	0.2	
				IOL = 4 mA	1.65	_	0.45	
		\/-·		I _{OL} = 8 mA	2.3	_	0.7	
	L-level	VoL		I _{OL} = 12 mA	2.7	_	0.4	
				I _{OL} = 16 mA	3.0	_	0.4	
				I _{OL} = 24 mA	3.0	_	0.55	
Input leakage current		liN	V _{IN} = 0 to 5.5 V	V _{IN} = 0 to 5.5 V		_	±5.0	μΑ
3-state output OFF sta	-state output OFF state current IOZ $VIN = VIH \text{ or } VIL$ $VOUT = 0 \text{ to } 5.5 \text{ V}$		V	1.65 to 3.6	_	±5.0	μА	
Power-off leakage curr	ent	loff	VIN/VOUT = 5.5 V		0	_	10.0	μΑ
		loo	VIN = VCC or GND		1.65 to 3.6	_	10.0	
Quiescent supply curre	erit.	Icc	VIN/VOUT = 3.6 to 5.5 V		1.65 to 3.6	_	±10.0	μΑ
Increase in ICC per inp	ut	Δlcc	VIH = VCC - 0.6	V (per 1 input)	2.7 to 3.6	_	500	



AC Characteristics ($Ta = -40 \text{ to } 85^{\circ}\text{C}$)

Characteristics	Symbol	Test Condition V _{CC} (V)		Min	Max	Unit
			1.8 ± 0.15	_	25.0	
Dronagation delay time	tpLH	Figure 1 Figure 2	2.5 ± 0.2		8.5	
Propagation delay time	tpHL	Figure 1, Figure 2	2.7	_	7.5	ns
			3.3 ± 0.3	1.5	6.5	
	^t pZL Fig		1.8 ± 0.15		32.0	- ns
Output anable time		Figure 1, Figure 3	2.5 ± 0.2	_	16.0	
Output enable time			2.7	_	9.0	
			3.3 ± 0.3	1.5	8.0	
			1.8 ± 0.15	_	30.0	
Outrout disable times	tpLZ tpHZ	Figure 1, Figure 3	2.5 ± 0.2	_	15.0	
Output disable time			2.7	_	8.0	ns
			3.3 ± 0.3	1.5	7.0	
	tosLH		2.7	_	_	20
Output to output skew	tosHL	(Note)	3.3 ± 0.3	_	1.0	ns

Note: Parameter guaranteed by design.

(tosLH = |tpLHm - tpLHn|, tosHL = |tpHLm - tpHLn|)

Dynamic Switching Characteristics (Ta = 25°C, input: tr = tf = 2.5 ns, CL = 50 pF, RL = 500 Ω)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Quiet output maximum dynamic VoL	Volp	$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	3.3	0.8	V
Quiet output minimum dynamic VOL	Volv	$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	3.3	0.8	V

Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Input capacitance	CIN	_	3.3	7	pF
Output capacitance	Cout	_	3.3	8	pF
Power dissipation capacitance	CPD	f _{IN} = 10 MHz (Note)	3.3	25	pF

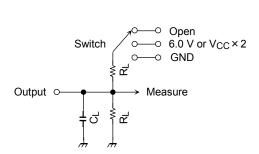
Note: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption.

Average operating current can be obtained by the equation:

 $ICC (opr) = CPD \cdot VCC \cdot fIN + ICC/8 (per bit)$



AC Test Circuit



Parameter	Switch
t _{pLH} , t _{pHL}	Open
t t	6.0 V @ V _{CC} = 3.3 ± 0.3 V @ V _{CC} = 2.7 V
t _{pLZ} , t _{pZL}	V _{CC} × 2 @ V _{CC} = 2.5 ± 0.2 V @ V _{CC} = 1.8 ± 0.15 V
t _{pHZ} , t _{pZH}	GND

Figuare 1



AC Waveform

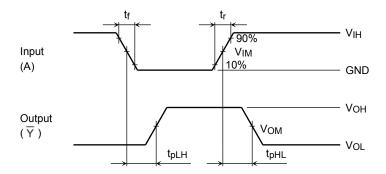


Figure 2 t_{pLH}, t_{pHL}

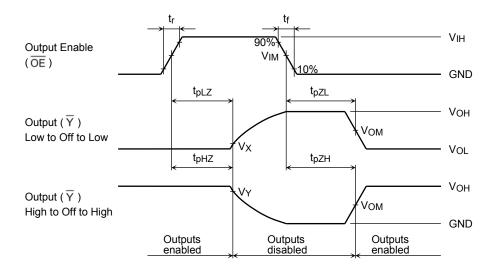


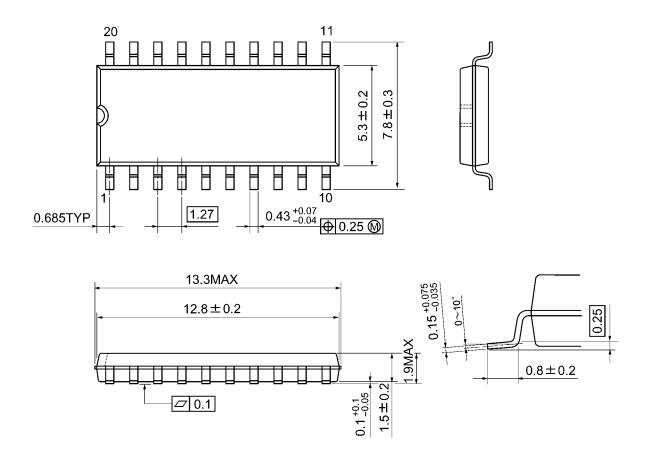
Figure 3 t_{pLZ} , t_{pHZ} , t_{pZL} , t_{pZH}

			Vcc	
	Symbol	3.3 ± 0.3 V 2.7 V	2.5 ± 0.2 V	1.8 ± 0.15 V
Input	VIH	2.7 V	Vcc	Vcc
	VIM	1.5 V	V _{CC} /2	V _{CC} /2
	t _r , t _f	2.5 ns	2.0 ns	2.0 ns
Output	V _{OM}	1.5 V	V _{OH} /2	V _{OH} /2
	VX	V _{OL} +0.3 V	V _{OL} +0.15 V	V _{OL} +0.15 V
	VY	V _{OH} -0.3 V	V _{OH} -0.15 V	V _{OH} -0.15 V
Load	CL	50 pF	30 pF	30 pF
	RL	500 Ω	500 Ω	1 kΩ



Package Dimensions

SOP20-P-300-1.27A Unit: mm

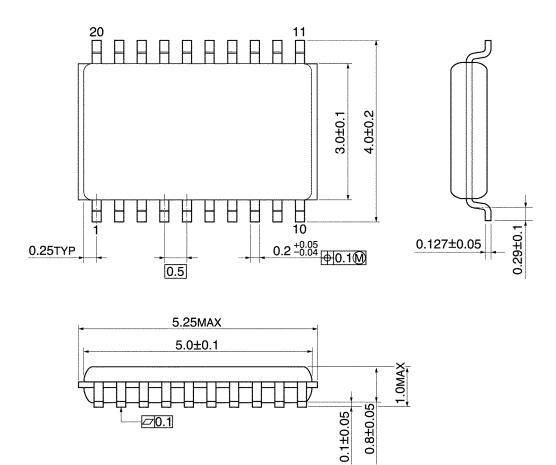


Weight: 0.22 g (typ.)



Package Dimensions

VSSOP20-P-0030-0.50 Unit: mm



Weight: 0.03 g (typ.)



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