TOSHIBA PHOTOCOUPLER GaAs IRED & PHOTO-TRANSISTOR

TLP291

Power Supplies Programmable Controllers Hybrid ICs

TLP291 consists of photo transistor, optically coupled to a gallium arsenide infrared emitting diode. TLP291 is housed in the SO4 package, very small and thin coupler.

Since TLP291 is guaranteed wide operating temperature (Ta=-55 to 110 $^{\circ}$ C) and high isolation voltage (3750Vrms), it's suitable for high-density surface mounting applications such as small switching power supplies and programmable controllers.

Collector-Emitter Voltage : 80 V (min)
 Current Transfer Ratio : 50% (min)
 Rank GB : 100% (min)
 Isolation Voltage : 3750 Vrms (min)
 Operation temperature : -55 to 110 °C
 UL (Under application) : UL1577

cUL (Under application) : CSA Component Acceptance Service No.5A

SEMKO (Under application): EN 60065: 2002,

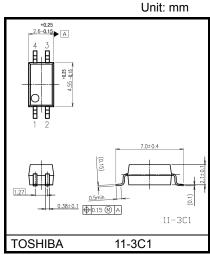
EN 60950-1: 2001, EN 60335-1: 2002

BSI (Under application) : BS EN 60065: 2002,

: BS EN 60950-1: 2006

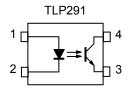
Option (V4)

VDE (Under application) : EN60747-5-2



Weight: 0.05 g (typ.)

Pin Configuration



1:ANODE 2:CATHODE 3:EMITTER 4:COLLECTOR

Current Transfer Ratio (CTR) Rank (Unless otherwise specified, Ta = 25°C)

TYPE	Classification (Note1)		sfer Ratio (%) / I _F)	
		I _F = 5 mA, V _{CE} = 5 V, Ta = 25°C		Marking of Classification
		Min	Max	
	Blank	50	400	Blank, YE, Y+, GR, GB, G, G+,B
	Rank Y	50	150	YE
TLP291	Rank GR	100	300	GR
	Rank GB	100	400	GB
12.201	Rank YH	75	150	Y+
	Rank GRL	100	200	G
	Rank GRH	150	300	G+
	Rank BLL	200	400	В

Note1: Ex. rank GB: TLP291 (GB,E

Note: Application type name for certification test, please use standard product type name, i.e.

TLP291 (GB,E: TLP291

Absolute Maximum Ratings (Note)(Unless otherwise specified, Ta = 25°C)

CHARACTERISTIC		SYMBOL	NOTE	RATING	UNIT
Input forward current		I _F		50	mA
	Input forward current derating (Ta≥90°C)	ΔI _F / Δ Ta		-1.5	mA /°C
	Input forward current (pulsed)	I _{FP}	(Note 2)	1	Α
Input reverse voltage		V _R		5	V
	Input power dissipation	P _D		100	mW
	Input power dissipation derating (Ta ≥ 90°C)	ΔΡ _D /ΔΤα		-3.0	mW/°C
	Junction temperature	Tj		125	°C
	Collector-emitter voltage	V _{CEO}		80	V
~	Emitter-collector voltage	V _{ECO}		7	V
DETECTOR	Collector current	Ic		50	mA
ETE(Collector power dissipation	PC		150	mW
□	Collector power dissipation derating(Ta≥25°C)	ΔΡ _С /ΔΤα		-1.5	mW /°C
	Junction temperature	Tj		125	°C
Operating temperature range		T _{opr}		-55 to 110	°C
Storage temperature range		T _{stg}		-55 to 125	°C
Lead soldering temperature		T _{sol}		260 (10s)	°C
Total package power dissipation		P _T		200	mW
Tota	al package power dissipation derating(Ta≥25°C)	ΔΡ _Τ /ΔΤα		-2.0	mW /°C
Isolation voltage		BV _S	(Note3)	3750	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note2: Pulse width $\leq 100 \mu s$, frequency 100Hz

Note3: AC, 1 minute, R.H.≤60%, Device considered a two terminal device: LED side pins shorted together and DETECTOR side pins shorted together.

Electrical Characteristics (Unless otherwise specified, Ta = 25°C)

CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
	Input forward voltage	V _F	I _F = 10 mA	1.1	1.25	1.4	V
E	Input reverse current	I _R	V _R = 5 V	1	_	5	μΑ
	Input capacitance	C _T	V = 0, f = 1 MHz		30	-	pF
	Collector-emitter breakdown voltage	V _(BR) CEO	I _C = 0.5 mA	80	_	_	٧
OR	Emitter-collector breakdown voltage	V _{(BR) ECO}	I _E = 0.1 mA	7	1	1	V
DETECTOR -	Dark current	I _{CEO}	V _{CE} = 48 V	1	0.01	0.08	μΑ
			V _{CE} = 48 V, Ta = 85°C	1	2	50	μΑ
	Collector-emitter capacitance	C _{CE}	V = 0, f = 1 MHz	1	10		pF

Coupled Electrical Characteristics (Unless otherwise specified, Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Current transfer ratio	I _C / I _F	I _F = 5 mA, V _{CE} = 5 V	50	_	400	- %
Current transfer fatto		Rank GB	100	_	400	
Saturated current transfer ratio	I _C / I _{F (sat)}	I _F = 1 mA, V _{CE} = 0.4 V		60	_	. %
Saturated current transfer ratio		Rank GB	30	_	_	/0
	V _{CE} (sat)	I _C = 2.4 mA, I _F = 8 mA	_	_	0.3	
Collector-emitter saturation voltage		I _C = 0.2 mA, I _F = 1 mA	_	0.2	_	V
		Rank GB	_	_	0.3	
OFF-state collector current	I _{C (off)}	V _F = 0.7 V, V _{CE} = 48 V	1	_	10	μА

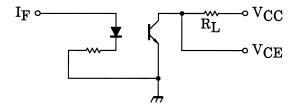
Isolation Characteristics (Unless otherwise specified, Ta = 25°C)

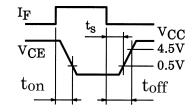
CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Total capacitance (input to output)	CS	V _S = 0 V, f = 1 MHz	_	0.8	_	pF
Isolation resistance	R _S	V _S = 500 V, R.H.≤60%	1×10 ¹²	10 ¹⁴	_	Ω
	BVS	AC , 1 minute	3750	-	_	Vrms
Isolation voltage		AC , 1 second, in OIL	_	10000	_	VIIIIS
		DC , 1 minute, in OIL	_	10000	_	Vdc

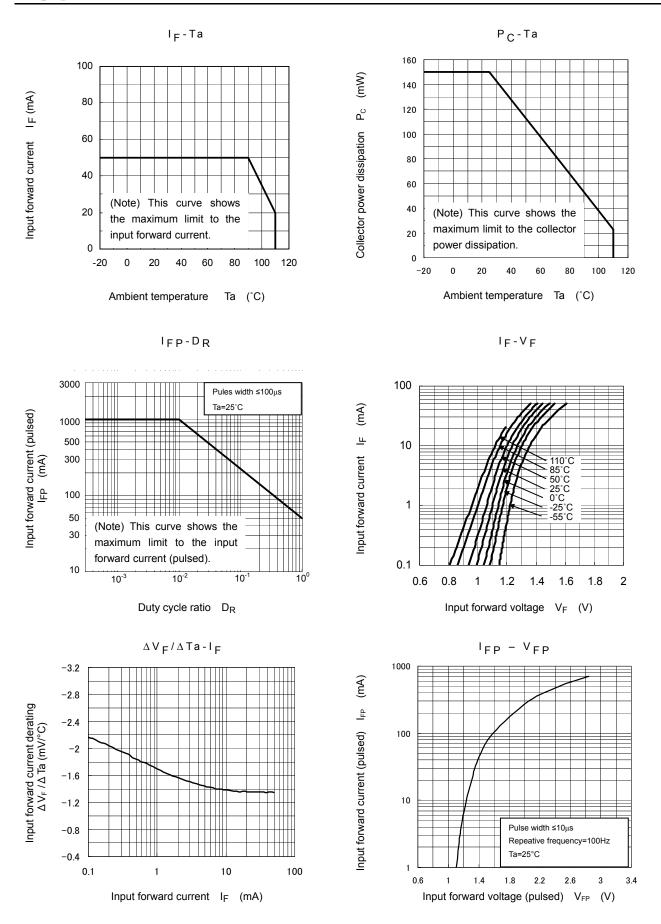
Switching Characteristics (Unless otherwise specified, Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Rise time	t _r		_	4	_	
Fall time	t _f	V _{CC} = 10 V, I _C = 2 mA	_	7	_	μS
Turn-on time	t _{on}	$R_L = 100\Omega$	_	7	_	
Turn-off time	t _{off}		_	7	_	
Turn-on time	t _{on}		_	2	_	
Storage time	ts	$R_L = 1.9 \text{ k}\Omega$ (Fig.1 $V_{CC} = 5 \text{ V}, I_F = 16 \text{ mA}$	_	30	_	μS
Turn-off time	t _{off}		_	60	_	

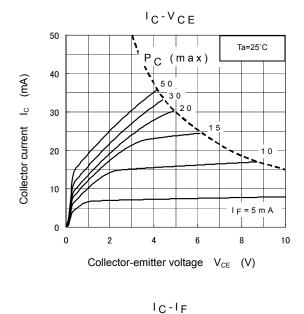
(Fig.1) Switching Time Test Circuit

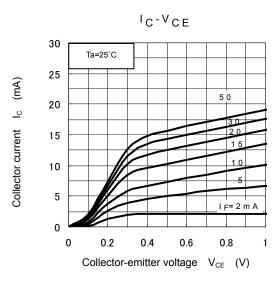


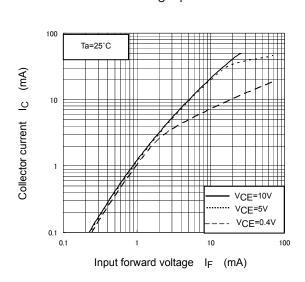


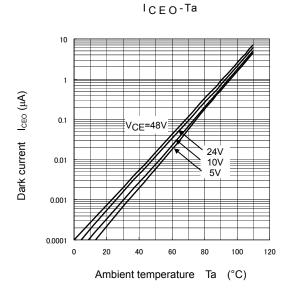


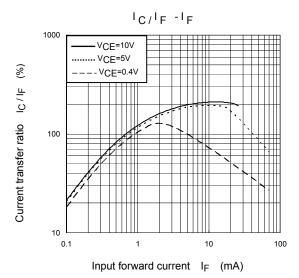
Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



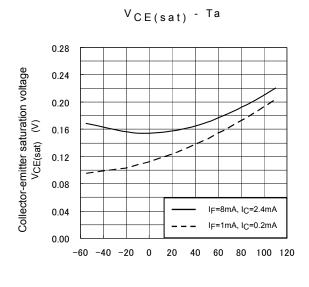




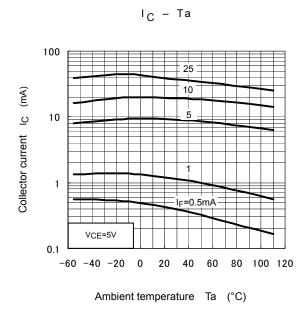


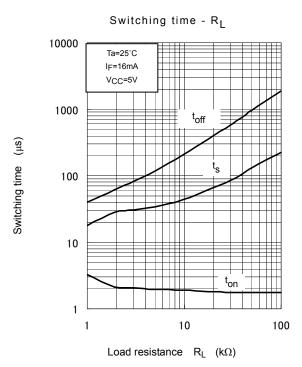


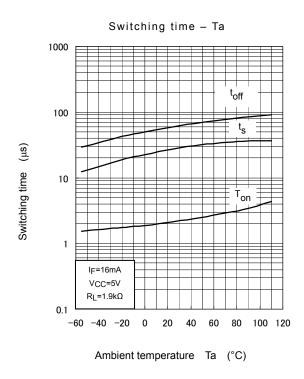
Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



Ambient temperature Ta (°C)







Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

Soldering and Storage

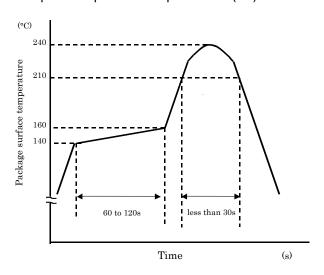
1. Soldering

1.1 Soldering

When using a soldering iron or medium infrared ray/hot air reflow, avoid a rise in device temperature as much as possible by observing the following conditions.

1) Using solder reflow

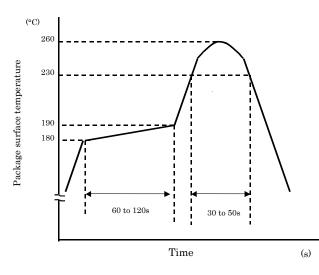
·Temperature profile example of lead (Pb) solder



This profile is based on the device's maximum heat resistance guaranteed value.

Set the preheat temperature/heating temperature to the optimum temperature corresponding to the solder paste type used by the customer within the described profile.

·Temperature profile example of using lead (Pb)-free solder



This profile is based on the device's maximum heat resistance guaranteed value.

Set the preheat temperature/heating temperature to the optimum temperature corresponding to the solder paste type used by the customer within the described profile.

- 2) Using solder flow (for lead (Pb) solder, or lead (Pb)-free solder)
 - · Please preheat it at 150°C between 60 and 120 seconds.
 - · Complete soldering within 10 seconds below 260°C. Each pin may be heated at most once.
- 3) Using a soldering iron

Complete soldering within 10 seconds below 260°C, or within 3 seconds at 350°C. Each pin may be heated at most once.

2. Storage

- 1) Avoid storage locations where devices may be exposed to moisture or direct sunlight.
- 2) Follow the precautions printed on the packing label of the device for transportation and storage.
- 3) Keep the storage location temperature and humidity within a range of 5°C to 35°C and 45% to 75%, respectively.
- 4) Do not store the products in locations with poisonous gases (especially corrosive gases) or in dusty conditions.
- 5) Store the products in locations with minimal temperature fluctuations. Rapid temperature changes during storage can cause condensation, resulting in lead oxidation or corrosion, which will deteriorate the solderability of the leads.
- 6) When restoring devices after removal from their packing, use anti-static containers.
- 7) Do not allow loads to be applied directly to devices while they are in storage.
- 8) If devices have been stored for more than two years under normal storage conditions, it is recommended that you check the leads for ease of soldering prior to use.

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