Dual inverter Rev. 3 — 25 January 2019

**Product data sheet** 

### 1. General description

The 74LVC2G04-Q100 provides the dual inverting buffer.

Inputs can be driven from either 3.3 V or 5 V devices. These features allow the use of these devices in a mixed 3.3 V and 5 V environment.

This device is fully specified for partial power-down applications using  $I_{OFF}$ . The  $I_{OFF}$  circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

This product has been qualified to the Automotive Electronics Council (AEC) standard Q100 (Grade 1) and is suitable for use in automotive applications.

### 2. Features and benefits

- Automotive product qualification in accordance with AEC-Q100 (Grade 1)
  Specified from -40 °C to +85 °C and from -40 °C to +125 °C
- Wide supply voltage range from 1.65 V to 5.5 V
- 5 V tolerant inputs for interfacing with 5 V logic
- High noise immunity
- Complies with JEDEC standard:
  - JESD8-7 (1.65 V to 1.95 V)
  - JESD8-5 (2.3 V to 2.7 V)
  - JESD8B/JESD36 (2.7 V to 3.6 V)
- ESD protection:
  - MIL-STD-883, method 3015 exceeds 2000 V
  - HBM JESD22-A114F exceeds 2000 V
  - MM JESD22-A115-A exceeds 200 V (C = 200 pF, R = 0 Ω)
- $\pm 24$  mA output drive (V<sub>CC</sub> = 3.0 V)
- CMOS low power consumption
- Latch-up performance exceeds 250 mA
- Direct interface with TTL levels
- Inputs accept voltages up to 5 V
- Multiple package options



### 3. Ordering information

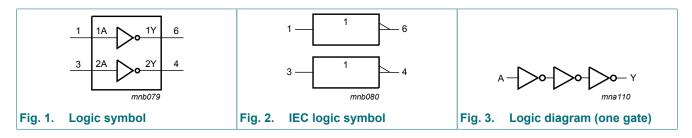
Type number	Package							
	Temperature range	Name	Description	Version				
74LVC2G04GW-Q100	-40 °C to +125 °C	SC-88	plastic surface-mounted package; 6 leads	SOT363				
74LVC2G04GV-Q100	-40 °C to +125 °C	SC-74	plastic surface-mounted package (TSOP6); 6 leads	SOT457				
74LVC2G04GS-Q100	-40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 1.0 x 1.0 x 0.35 mm	SOT1202				

### 4. Marking

Table 2. Marking					
Type number	Marking code[1]				
74LVC2G04GW-Q100	V4				
74LVC2G04GV-Q100	V04				
74LVC2G04GS-Q100	V4				

[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

### 5. Functional diagram



### 6. Pinning information

### 6.1. Pinning



### 6.2. Pin description

Table 3. Pin description					
Symbol	Pin	Description			
1A	1	data input			
GND	2	ground (0 V)			
2A	3	data input			
2Y	4	data output			
V <sub>CC</sub>	5	supply voltage			
1Y	6	data output			

### 7. Functional description

#### Table 4. Function table [1]

Input	Output
nA	nY
L	Н
Н	L

[1] H = HIGH voltage level; L = LOW voltage level.

### 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CC</sub>	supply voltage			-0.5	+6.5	V
I <sub>IK</sub>	input clamping current	V <sub>1</sub> < 0 V		-50	-	mA
VI	input voltage		[1]	-0.5	+6.5	V
I <sub>OK</sub>	output clamping current	$V_{\rm O}$ > $V_{\rm CC}$ or $V_{\rm O}$ < 0 V		-	±50	mA
Vo	output voltage	Active mode	[1] [2]	-0.5	V <sub>CC</sub> + 0.5	V
		Power-down mode	[1] [2]	-0.5	+6.5	V
lo	output current	$V_{O} = 0 V \text{ to } V_{CC}$		-	±50	mA
I <sub>CC</sub>	supply current			-	100	mA
I <sub>GND</sub>	ground current			-100	-	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> = -40 °C to +125 °C	[3]	-	250	mW
T <sub>stg</sub>	storage temperature			-65	+150	°C

[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] When V<sub>CC</sub> = 0 V (Power-down mode), the output voltage can be 5.5 V in normal operation.

[3] For SC-88 and SC-74 packages: above 87.5 °C the value of P<sub>tot</sub> derates linearly with 4.0 mW/K. For XSON6 packages: above 118 °C the value of P<sub>tot</sub> derates linearly with 7.8 mW/K.

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### 9. Recommended operating conditions

Table 6. Recommended operating conditions						
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CC</sub>	supply voltage		1.65	-	5.5	V
VI	input voltage		0	-	5.5	V
Vo	output voltage	Active mode	0	-	V <sub>CC</sub>	V
		Power-down mode; $V_{CC}$ = 0 V	0	-	5.5	V
T <sub>amb</sub>	ambient temperature		-40	-	+125	°C
Δt/ΔV	input transition rise and fall rate	V <sub>CC</sub> = 1.65 V to 2.7 V	-	-	20	ns/V
		V <sub>CC</sub> = 2.7 V to 5.5 V	-	-	10	ns/V

### 10. Static characteristics

#### Table 7. Static characteristics

At recommended operating conditions. Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	-40	-40 °C to +85 °C			-40 °C to +125 °C		
			Min	Typ <mark>[1]</mark>	Мах	Min	Мах	-	
	HIGH-level input	V <sub>CC</sub> = 1.65 V to 1.95 V	0.65V <sub>CC</sub>	-	-	0.65V <sub>CC</sub>	-	V	
	voltage	V <sub>CC</sub> = 2.3 V to 2.7 V	1.7	-	-	1.7	-	V	
		V <sub>CC</sub> = 2.7 V to 3.6 V	2.0	-	-	2.0	-	V	
		V <sub>CC</sub> = 4.5 V to 5.5 V	0.7V <sub>CC</sub>	-	-	0.7V <sub>CC</sub>	-	V	
V <sub>IL</sub>	LOW-level input	V <sub>CC</sub> = 1.65 V to 1.95 V	-	-	0.35V <sub>CC</sub>	-	0.35V <sub>CC</sub>	V	
	voltage	V <sub>CC</sub> = 2.3 V to 2.7 V	-	-	0.7	-	0.7	V	
		V <sub>CC</sub> = 2.7 V to 3.6 V	-	-	0.8	-	0.8	V	
		V <sub>CC</sub> = 4.5 V to 5.5 V	-	-	0.3V <sub>CC</sub>	-	0.3V <sub>CC</sub>	V	
V <sub>OH</sub>	HIGH-level	$V_{I} = V_{IH} \text{ or } V_{IL}$							
	output voltage	I <sub>O</sub> = -100 μA; V <sub>CC</sub> = 1.65 V to 5.5 V	V <sub>CC</sub> - 0.1	-	-	V <sub>CC</sub> - 0.1	-	V	
		I <sub>O</sub> = -4 mA; V <sub>CC</sub> = 1.65 V	1.2	-	-	0.95	-	V	
		I <sub>O</sub> = -8 mA; V <sub>CC</sub> = 2.3 V	1.9	-	-	1.7	-	V	
		I <sub>O</sub> = -12 mA; V <sub>CC</sub> = 2.7 V	2.2	-	-	1.9	-	V	
		I <sub>O</sub> = -24 mA; V <sub>CC</sub> = 3.0 V	2.3	-	-	2.0	-	V	
		I <sub>O</sub> = -32 mA; V <sub>CC</sub> = 4.5 V	3.8	-	-	3.4	-	V	
V <sub>OL</sub>	LOW-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$							
		I <sub>O</sub> = 100 μA; V <sub>CC</sub> = 1.65 V to 5.5 V	-	-	0.10	-	0.10	V	
		I <sub>O</sub> = 4 mA; V <sub>CC</sub> = 1.65 V	-	-	0.45	-	0.70	V	
		I <sub>O</sub> = 8 mA; V <sub>CC</sub> = 2.3 V	-	-	0.30	-	0.45	V	
		I <sub>O</sub> = 12 mA; V <sub>CC</sub> = 2.7 V	-	-	0.40	-	0.60	V	
		I <sub>O</sub> = 24 mA; V <sub>CC</sub> = 3.0 V	-	-	0.55	-	0.80	V	
		I <sub>O</sub> = 32 mA; V <sub>CC</sub> = 4.5 V	-	-	0.55	-	0.80	V	
I	input leakage current	V <sub>I</sub> = 5.5 V or GND; V <sub>CC</sub> = 0 V to 5.5 V	-	±0.1	±1	-	±1	μA	
I <sub>OFF</sub>	power-off leakage current	$V_{CC}$ = 0 V; V <sub>1</sub> or V <sub>0</sub> = 5.5 V	-	±0.1	±2	-	±2	μA	

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Symbol	Parameter	Conditions	-40 °C to +85 °C			-40 °C to	Unit	
			Min	Typ[1]	Max	Min	Мах	
I <sub>CC</sub>	supply current	V <sub>I</sub> = 5.5 V or GND; I <sub>O</sub> = 0 A; V <sub>CC</sub> = 1.65 V to 5.5 V	-	0.1	4	-	4	μA
∆I <sub>CC</sub>	additional supply current	per pin; $V_{CC}$ = 2.3 V to 5.5 V; V <sub>I</sub> = V <sub>CC</sub> - 0.6 V; I <sub>O</sub> = 0 A	-	5	500	-	500	μA
CI	input capacitance	$V_{CC}$ = 3.3 V; $V_{I}$ = GND to $V_{CC}$	-	2.5	-	-	-	pF

[1] All typical values are measured at V<sub>CC</sub> = 3.3 V and T<sub>amb</sub> = 25 °C.

### **11. Dynamic characteristics**

#### Table 8. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see Fig. 7.

Symbol	Parameter	Conditions	-40 °C to +85 °C			-40 °C to +125 °C		Unit
			Min	Typ [1]	Max	Min	Max	1
t <sub>pd</sub>	propagation delay	nA to nY; see Fig. 6 [2]						
		V <sub>CC</sub> = 1.65 V to 1.95 V	1.0	3.5	8.0	1.0	9.5	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.0	2.2	4.4	1.0	5.4	ns
		V <sub>CC</sub> = 2.7 V	1.0	2.7	5.2	1.0	7.0	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	0.5	2.7	4.1	0.5	5.5	ns
		V <sub>CC</sub> = 4.5 V to 5.5 V	1.0	1.9	3.2	1.0	3.8	ns
C <sub>PD</sub>	power dissipation capacitance	$V_{I} = GND$ to $V_{CC}$ ; $V_{CC} = 3.3 V$ [3]	-	13.5	-	-	-	pF

[1] Typical values are measured at  $T_{amb}$  = 25 °C and  $V_{CC}$  = 1.8 V, 2.5 V, 2.7 V, 3.3 V and 5.0 V respectively.

[2]  $t_{pd}$  is the same as  $t_{PLH}$  and  $t_{PHL}$ .

[3]  $C_{PD}$  is used to determine the dynamic power dissipation (P<sub>D</sub> in µW).

 $P_{D} = C_{PD} \times V_{CC}^{2} \times f_{i} \times N + \sum (C_{L} \times V_{CC}^{2} \times f_{o}) \text{ where:}$ 

f<sub>i</sub> = input frequency in MHz;

f<sub>o</sub> = output frequency in MHz;

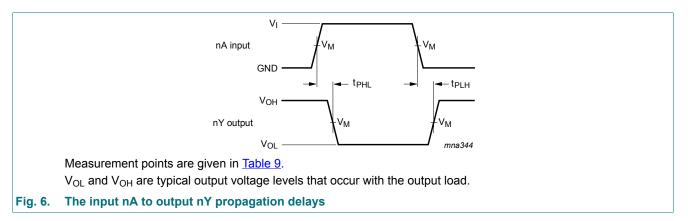
 $C_L$  = output load capacitance in pF;

V<sub>CC</sub> = supply voltage in V;

N = number of inputs switching;

 $\sum (C_L \times V_{CC}^2 \times f_0)$  = sum of outputs.

#### 11.1. Waveforms and test circuit

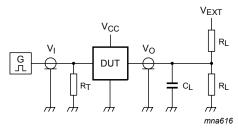


74LVC2G04\_Q100

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#### Table 9. Measurement points

Supply voltage	Input	Output
V <sub>cc</sub>	V <sub>M</sub>	V <sub>M</sub>
1.65 V to 1.95 V	0.5V <sub>CC</sub>	0.5V <sub>CC</sub>
2.3 V to 2.7 V	0.5V <sub>CC</sub>	0.5V <sub>CC</sub>
2.7 V	1.5 V	1.5 V
3.0 V to 3.6 V	1.5 V	1.5 V
4.5 V to 5.5 V	0.5V <sub>CC</sub>	0.5V <sub>CC</sub>



Test data is given in Table 10.

Definitions for test circuit:

R<sub>L</sub> = Load resistance.

 $C_L$  = Load capacitance including jig and probe capacitance.

 $R_T$  = Termination resistance should be equal to the output impedance  $Z_0$  of the pulse generator.

 $V_{EXT}$  = External voltage for measuring switching times.

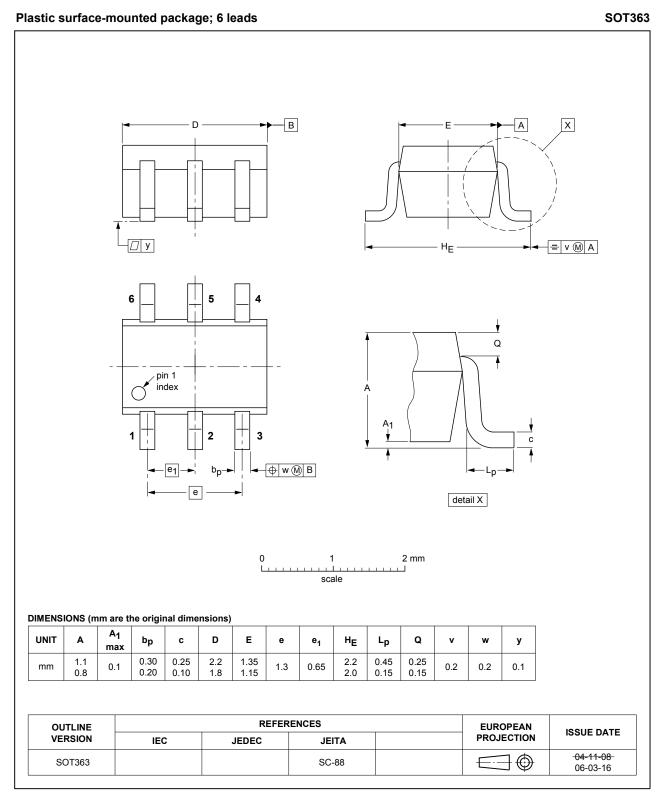
#### Fig. 7. Test circuit for measuring switching times

#### Table 10. Test data

Supply voltage	Input	Input		Load		
V <sub>cc</sub>	Vi	t <sub>r</sub> = t <sub>f</sub>	CL	RL	t <sub>PLH</sub> , t <sub>PHL</sub>	
1.65 V to 1.95 V	V <sub>CC</sub>	≤ 2.0 ns	30 pF	1 kΩ	open	
2.3 V to 2.7 V	V <sub>CC</sub>	≤ 2.0 ns	30 pF	500 Ω	open	
2.7 V	2.7 V	≤ 2.5 ns	50 pF	500 Ω	open	
3.0 V to 3.6 V	2.7 V	≤ 2.5 ns	50 pF	500 Ω	open	
4.5 V to 5.5 V	V <sub>CC</sub>	≤ 2.5 ns	50 pF	500 Ω	open	

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### 12. Package outline



#### Fig. 8. Package outline SOT363 (SC-88)

#### **Dual inverter**

SOT457

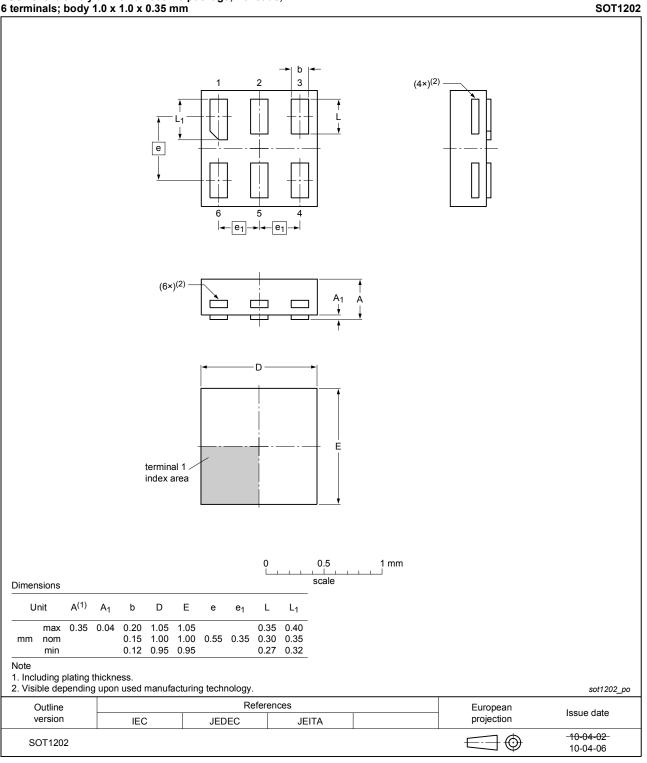
Plastic, surface-mounted package (SC-74; TSOP6); 6 leads

В D Е A Х \_ у  $H_{\text{E}}$ = v M A 6 5 4 Q A pin 1  $\bigcirc$ index A<sub>1</sub> ł С 4 4 2 3 1 -n detail X е bp-► 0 1 2 mm scale Dimensions (mm are the original dimensions) Е Unit А  $A_1$ bp С D е  $\mathsf{H}_\mathsf{E}$ Lp Q v w у 1.1 0.1 0.40 0.26 3.1 1.7 3.0 0.6 0.33 max 0.95 0.2 0.2 0.1 mm nom 0.9 0.013 0.25 0.10 2.7 1.3 2.5 0.2 0.23 min sot457\_po References Outline European Issue date projection version IEC JEDEC JEITA 06-03-16  $\bigcirc$ SOT457 SC-74 18-11-27

Fig. 9. Package outline SOT457 (SC-74)

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XSON6: extremely thin small outline package; no leads;	
6 terminals; body 1.0 x 1.0 x 0.35 mm	





### 13. Abbreviations

Acronym	Description
CMOS	Complementary Metal-Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
HBM	Human Body Model
MIL	Military
MM	Machine Model
TTL	Transistor-Transistor Logic

### 14. Revision history

#### Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes	
74LVC2G04_Q100 v.3	20190125	Product data sheet	-	74LVC2G04_Q100 v.2	
Modifications:	Nexperia. • Legal texts have • Pin 6 description • Added type numb	s data sheet has been red been adapted to the new updated. <u>Section 6.2</u> ber 74LVC2G04GS-Q100 drawing <u>SOT457</u> (SC-74)	company name where (SOT1202/XSON6).	h the identity guidelines of appropriate.	
74LVC2G04_Q100 v.2	20161213	Product data sheet	-	74LVC2G04_Q100 v.1	
Modifications:	• <u>Table 7</u> : The maximum limits for leakage current and supply current have changed.				
74LVC2G04_Q100 v.1	20140520	Product data sheet	-	-	

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### 15. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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[2] The term 'short data sheet' is explained in section "Definitions".

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