

60 V, N-channel Trench MOSFET

29 April 2019

1. General description

N-channel enhancement mode Field-Effect Transistor (FET) in a small SOT457 (SC-74) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

2. Features and benefits

- Logic-level compatible
- Extended temperature range T_i = 175 °C
- Trench MOSFET technology
- ElectroStatic Discharge (ESD) protection > 2 kV HBM (class H2)
- AEC-Q101 qualified

3. Applications

- Relay driver
- High-speed line driver
- Low-side load switch
- Switching circuits

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	-	60	V
V _{GS}	gate-source voltage			-20	-	20	V
I _D	drain current	V _{GS} = 10 V; T _{amb} = 25 °C	[1]	-	-	2.5	А
Static chara	acteristics		•				
R _{DSon}	drain-source on-state resistance	V _{GS} = 10 V; I _D = 2.5 A; T _j = 25 °C		-	96	123	mΩ

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm².



5. Pinning information

Table 2. F	Table 2. Pinning information							
Pin	Symbol	Description	Simplified outline	Graphic symbol				
1	D	drain		D				
2	D	drain						
3	G	gate		G ↓ ↓ ↓ ↓				
4	S	source	TSOP6 (SOT457)					
5	D	drain						
6	D	drain	1					
				017aaa255				

6. Ordering information

Table 3. Ordering information

Type number			
	Name	Description	Version
PMN120ENEA	TSOP6	plastic surface-mounted package (TSOP6); 6 leads	SOT457

7. Marking

Table 4. Marking codes

Type number	Marking code
PMN120ENEA	J5

8. Limiting values

Table 5. Limiting values

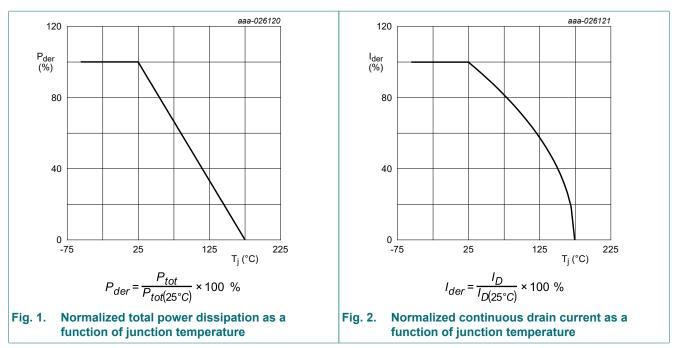
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	60	V
V _{GS}	gate-source voltage			-20	20	V
I _D	drain current	V _{GS} = 10 V; T _{amb} = 25 °C	[1]	-	2.5	А
		V _{GS} = 10 V; T _{amb} = 100 °C	[1]	-	1.8	А
I _{DM}	peak drain current	T_{amb} = 25 °C; single pulse; $t_p \le 10 \ \mu s$		-	10	А
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	670	mW
			[1]	-	1.7	W
		T _{sp} = 25 °C		-	7.5	W
Tj	junction temperature			-55	175	°C
T _{amb}	ambient temperature			-55	175	°C
T _{stg}	storage temperature			-65	175	°C
Source-drain	n diode			I		
I _S	source current	T _{amb} = 25 °C	[1]	-	1.7	А
ESD maximu	um rating					_
V _{ESD}	electrostatic discharge voltage	НВМ	[3]	-	2000	V
Avalanche r	uggedness					
E _{DS(AL)S}	non-repetitive drain- source avalanche energy	T _{j(init)} = 25 °C; I _D = 0.3 A; DUT in v avalanche (unclamped)		-	9	mJ
		1				

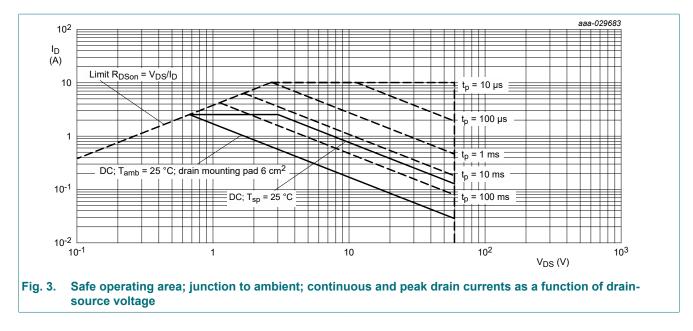
[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm².

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[3] Measured between all pins.



60 V, N-channel Trench MOSFET

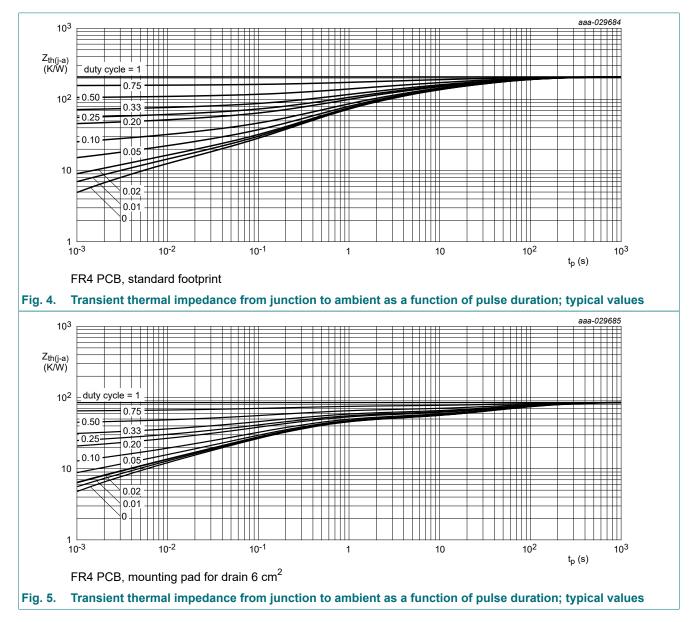


9. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)} thermal resistance from junction to ambient	thermal resistance from	in free air	[1]	-	195	225	K/W
		[2]	-	78	90	K/W	
R _{th(j-sp)}	thermal resistance from junction to solder point			-	15	20	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

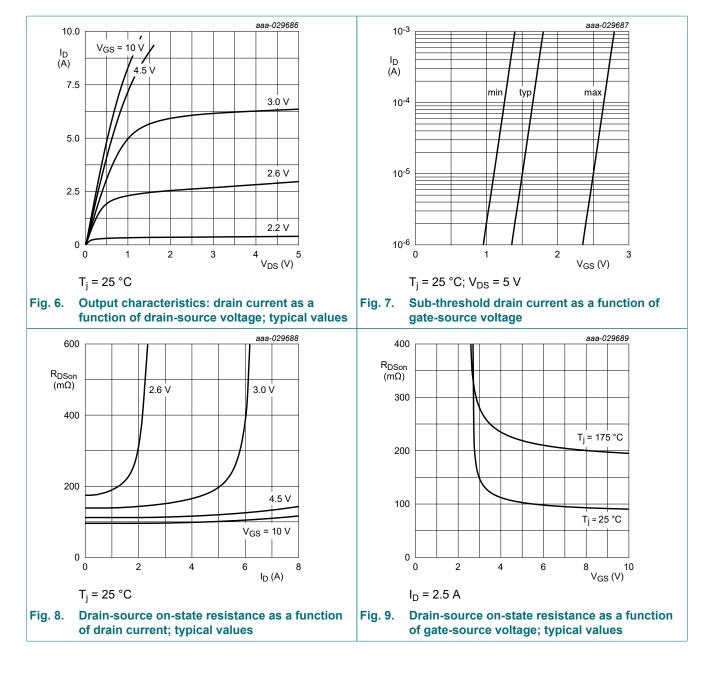
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm².



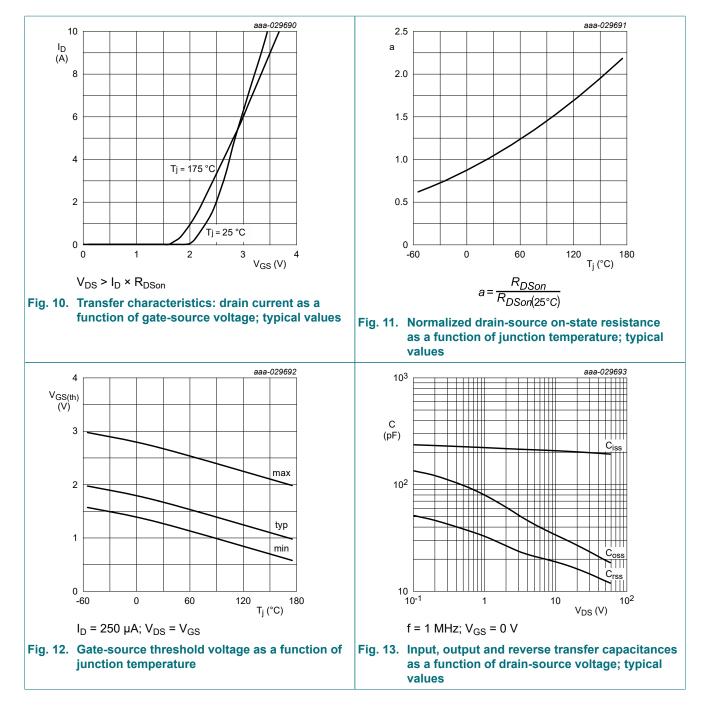
10. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara				- 76		
V _{(BR)DSS}	drain-source breakdown voltage	I _D = 250 μA; V _{GS} = 0 V; T _j = 25 °C	60	-	-	V
V _{GSth}	gate-source threshold voltage	I _D = 250 μA; V _{DS} =V _{GS} ; T _j = 25 °C	1.3	1.7	2.7	V
I _{DSS}	drain leakage current	V _{DS} = 60 V; V _{GS} = 0 V; T _j = 25 °C	-	-	1	μA
I _{GSS}	gate leakage current	V _{GS} = 20 V; V _{DS} = 0 V; T _j = 25 °C	-	-	10	μA
		V _{GS} = -20 V; V _{DS} = 0 V; T _j = 25 °C	-	-	-10	μA
		V _{GS} = 10 V; V _{DS} = 0 V; T _j = 25 °C	-	-	1	μA
		V _{GS} = -10 V; V _{DS} = 0 V; T _j = 25 °C	-	-	-1	μA
R _{DSon}	drain-source on-state resistance	V _{GS} = 10 V; I _D = 2.5 A; T _j = 25 °C	-	96	123	mΩ
		V _{GS} = 10 V; I _D = 2.5 A; T _j = 175 °C	-	208	267	mΩ
		V _{GS} = 4.5 V; I _D = 2.3 A; T _j = 25 °C	-	108	146	mΩ
9 _{fs}	forward transconductance	V _{DS} = 10 V; I _D = 2 A; T _j = 25 °C	-	8.5	-	S
R _G	gate resistance	f = 1 MHz; T _j = 25 °C	-	11	-	Ω
Dynamic ch	aracteristics		I			
Q _{G(tot)}	total gate charge	V_{DS} = 30 V; I _D = 2.5 A; V _{GS} = 10 V; T _j = 25 °C	-	4	6	nC
Q _{GS}	gate-source charge	V_{DS} = 30 V; I _D = 2.5 mA; V _{GS} = 10 V;	-	0.5	-	nC
Q _{GD}	gate-drain charge	T _j = 25 °C	-	0.9	-	nC
C _{iss}	input capacitance	V _{DS} = 30 V; f = 1 MHz; V _{GS} = 0 V;	-	196	-	pF
C _{oss}	output capacitance	T _j = 25 °C	-	24	-	pF
C _{rss}	reverse transfer capacitance		-	15	-	pF
t _{d(on)}	turn-on delay time	V_{DS} = 30 V; I _D = 2.5 A; V _{GS} = 10 V;	-	3	-	ns
t _r	rise time	$R_{G(ext)} = 6 \Omega; T_j = 25 °C$	-	6	-	ns
t _{d(off)}	turn-off delay time	1	-	8	-	ns
t _f	fall time	1	-	3	-	ns
Source-drai	n diode	· ·				
V _{SD}	source-drain voltage	I _S = 1.7 A; V _{GS} = 0 V; T _j = 25 °C	-	0.8	1.2	V
t _{rr}	reverse recovery time	I _S = 0.8 A; dI _S /dt = -100 A/μs;	-	11	-	ns
Q _r	recovered charge	V _{GS} = 0 V; V _{DS} = 30 V; T _j = 25 °C	-	4	-	nC

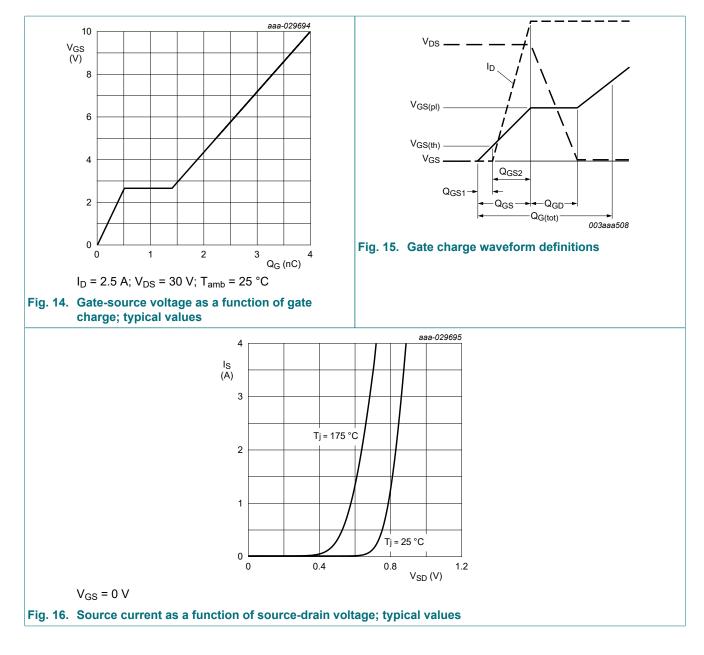
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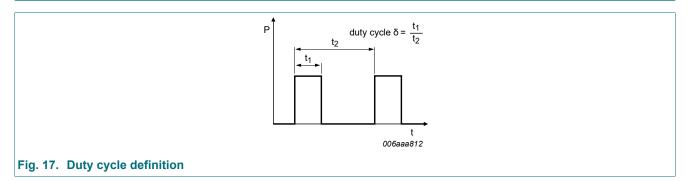


60 V, N-channel Trench MOSFET



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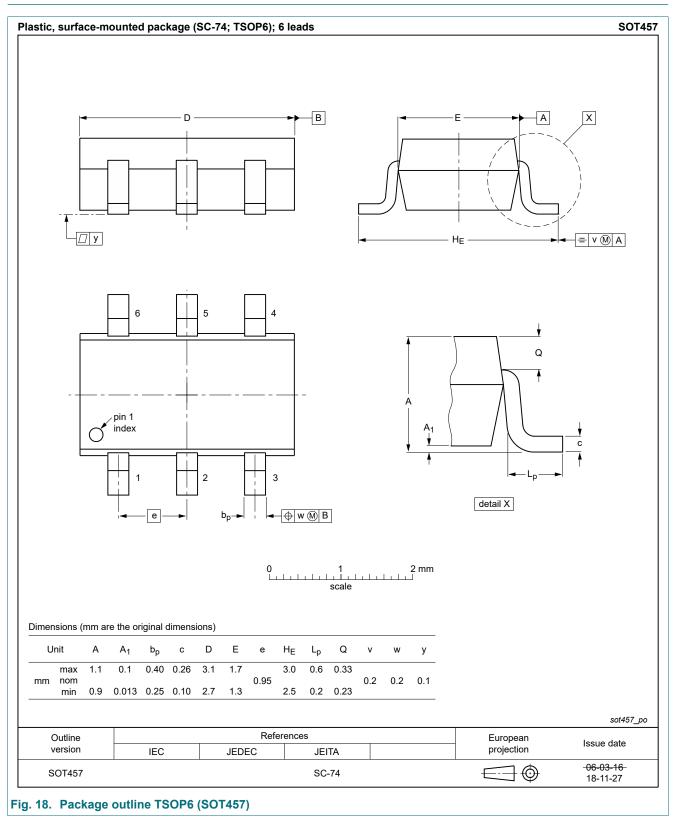
11. Test information



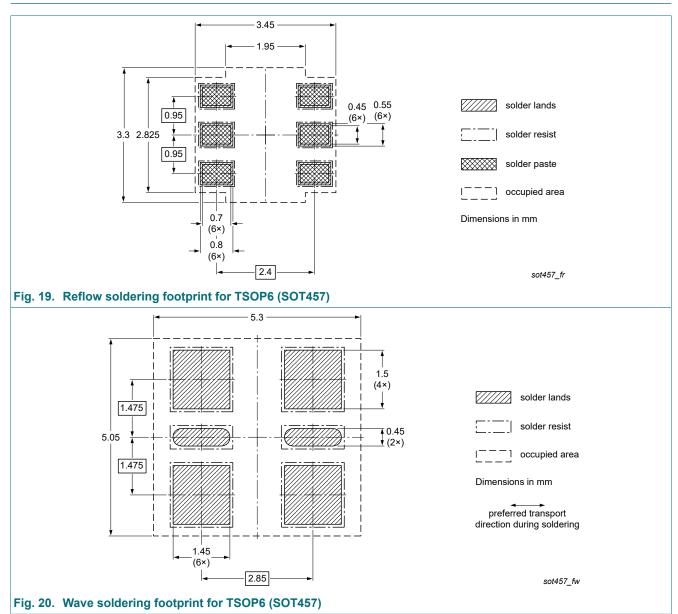
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



13. Soldering



14. Revision history

Table 8. Revision history							
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PMN120ENEA v.1	20190429	Product data sheet	-	-			

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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Contents

1. Gene	ral description	1
2. Featu	res and benefits	1
3. Appli	cations	1
4. Quick	reference data	1
5. Pinnii	ng information	2
6. Order	ring information	2
7. Marki	ing	2
8. Limiti	ing values	3
9. Thern	nal characteristics	5
10. Chai	racteristics	6
11. Test	information	10
12. Pack	kage outline	11
	lering	
14. Revi	ision history	13
	al information	
-		

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