

Product data sheet

1. General description

N-channel enhancement mode Field-Effect Transistor (FET) in a leadless medium power DFN2020MD-6 (SOT1220) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

2. Features and benefits

- Extended temperature range T_i = 175 °C
- Small and leadless ultra thin SMD plastic package: 2 x 2 x 0.65 mm
- Tin-plated 100 % solderable side pads for optical solder inspection
- ElectroStatic Discharge (ESD) protection > 2 kV HBM
- Trench MOSFET technology

3. Applications

- Relay driver
- High-speed line driver
- Low-side load switch
- Switching circuits

4. Quick reference data

Table 1. Quicl	k reference data				_		
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	-	30	V
V _{GS}	gate-source voltage			-20	-	20	V
I _D	drain current	V _{GS} = 10 V; T _{amb} = 25 °C; t ≤ 5 s	[1]	-	-	10	А
Static charac	cteristics			·	·		·
R _{DSon}	drain-source on-state resistance	V _{GS} = 10 V; I _D = 7.2 A; T _j = 25 °C		-	17	24	mΩ

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 6 cm².

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5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	D	drain		D
2	D	drain		
3	G	gate		G ← → 🛱 本 \
4	S	source		
5	D	drain	Transparent top view	
6	D	drain	DFN2020MD-6 (SOT1220)	' S
7	D	drain		017aaa255
8	S	source		

6. Ordering information

Table 3. Ordering information

Type number	Package	ackage						
	Name	Description	Version					
PMPB25ENE	DFN2020MD-6	DFN2020MD-6: plastic thermal enhanced ultra thin small outline package; no leads; 6 terminals	SOT1220					

7. Marking

Table 4. Marking codes

Type number	Marking code
PMPB25ENE	3V

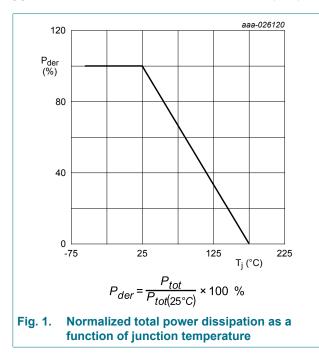
8. Limiting values

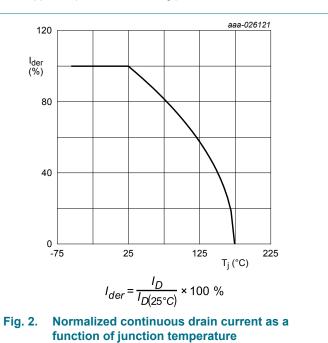
Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	30	V
V _{GS}	gate-source voltage	_		-20	20	V
ID	drain current	V _{GS} = 10 V; T _{amb} = 25 °C; t ≤ 5 s	[1]	-	10	А
		V _{GS} = 10 V; T _{amb} = 25 °C	[1]	-	7.2	А
		V _{GS} = 10 V; T _{amb} = 100 °C	[1]	-	4.6	А
I _{DM}	peak drain current	T_{amb} = 25 °C; single pulse; $t_p \le 10 \ \mu s$		-	29	А
P _{tot}	total power dissipation	T _{amb} = 25 °C	[1]	-	2.1	W
		T _{amb} = 25 °C; t ≤ 5 s	[1]	-	4.1	W
		T _{sp} = 25 °C		-	12.5	W
Tj	junction temperature			-55	175	°C
T _{amb}	ambient temperature			-55	175	°C
T _{stg}	storage temperature			-65	175	°C
Source-drai	n diode		·	·		
I _S	source current	T _{amb} = 25 °C	[1]	-	2.1	А

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 6 cm².

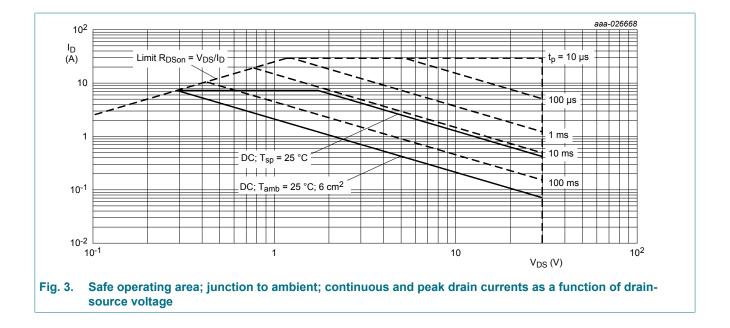




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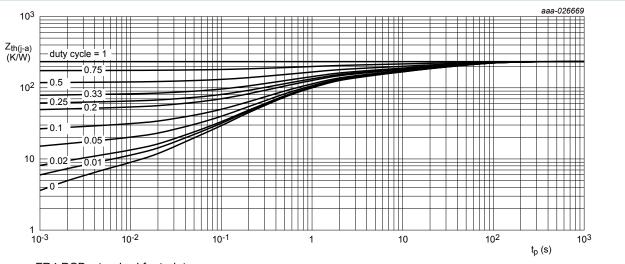
9. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance		[1]	-	231	265	K/W
	from junction to ambient		[2]	-	63	72	K/W
		in free air; t ≤ 5 s	[2]	-	32	37	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	9	12	K/W

Table 6. Thermal characteristics

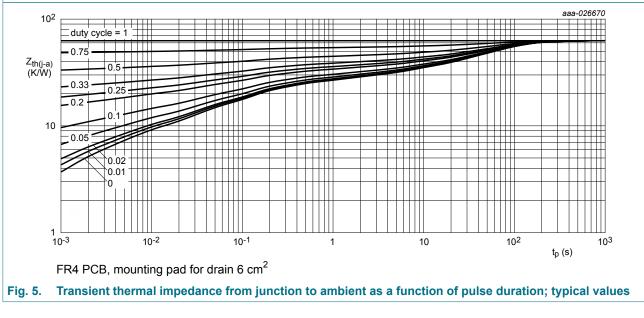
[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for drain 6 cm².



FR4 PCB, standard footprint

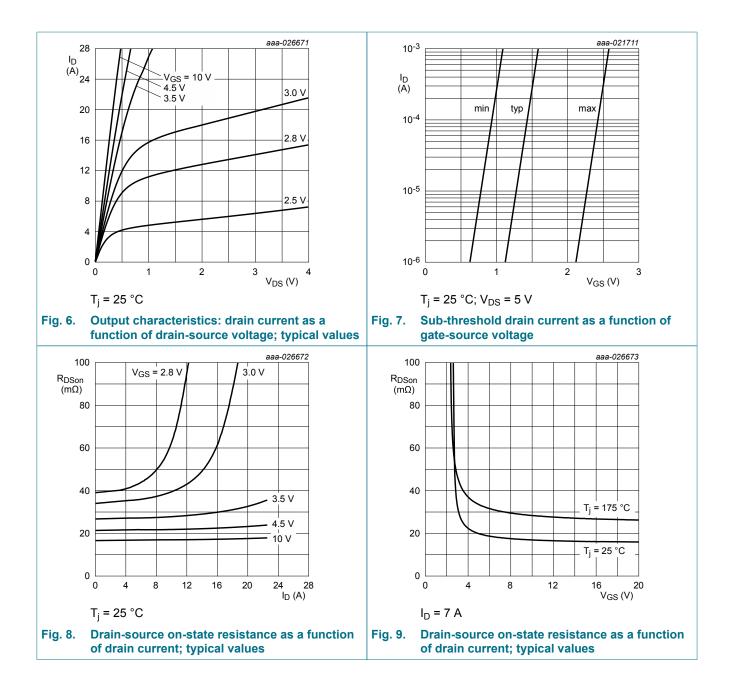




10. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
Static chara	acteristics	1				
V _{(BR)DSS}	drain-source breakdown voltage	I_D = 250 µA; V_{GS} = 0 V; T_j = 25 °C	30	-	-	V
V _{GSth}	gate-source threshold voltage	I_D = 250 µA; V_{DS} = V_{GS} ; T_j = 25 °C	1	1.5	2.5	V
I _{DSS}	drain leakage current	V_{DS} = 30 V; V_{GS} = 0 V; T_j = 25 °C	-	-	1	μA
I _{GSS}	gate leakage current	V_{GS} = 20 V; V_{DS} = 0 V; T_j = 25 °C	-	-	10	μA
		V _{GS} = -20 V; V _{DS} = 0 V; T _j = 25 °C	-	-	-10	μA
		V_{GS} = 4.5 V; V_{DS} = 0 V; T_j = 25 °C	-	-	200	nA
		V _{GS} = -4.5 V; V _{DS} = 0 V; T _j = 25 °C	-	-	-200	nA
00011	drain-source on-state	V _{GS} = 10 V; I _D = 7.2 A; T _j = 25 °C	-	17	24	mΩ
	resistance	V _{GS} = 10 V; I _D = 7.2 A; T _j = 175 °C	-	28	40	mΩ
		V_{GS} = 4.5 V; I _D = 6.2 A; T _j = 25 °C	-	28	32	mΩ
9 _{fs}	forward transconductance	V _{DS} = 10 V; I _D = 7.2 A; T _j = 25 °C	-	25	-	S
R _G	gate resistance	f = 1 MHz	-	6.8	-	Ω
Dynamic ch	naracteristics					
Q _{G(tot)}	total gate charge	V _{DS} = 15 V; I _D = 7 A; V _{GS} = 10 V;	-	13	19	nC
Q _{GS}	gate-source charge	T _j = 25 °C	-	1.5	-	nC
Q _{GD}	gate-drain charge		-	2.8	-	nC
C _{iss}	input capacitance	V _{DS} = 15 V; f = 1 MHz; V _{GS} = 0 V;	-	607	-	pF
C _{oss}	output capacitance	T _j = 25 °C	-	113	-	pF
C _{rss}	reverse transfer capacitance		-	88	-	pF
t _{d(on)}	turn-on delay time	V_{DS} = 15 V; I _D = 7 A; V _{GS} = 10 V;	-	6	-	ns
t _r	rise time	$R_{G(ext)} = 6 \Omega; T_j = 25 °C$	-	29	-	ns
t _{d(off)}	turn-off delay time		-	28	-	ns
t _f	fall time		-	12	-	ns
Source-drai	in diode	· · · · ·				
V _{SD}	source-drain voltage	I _S = 2.1 A; V _{GS} = 0 V; T _j = 25 °C	-	0.8	1.2	V

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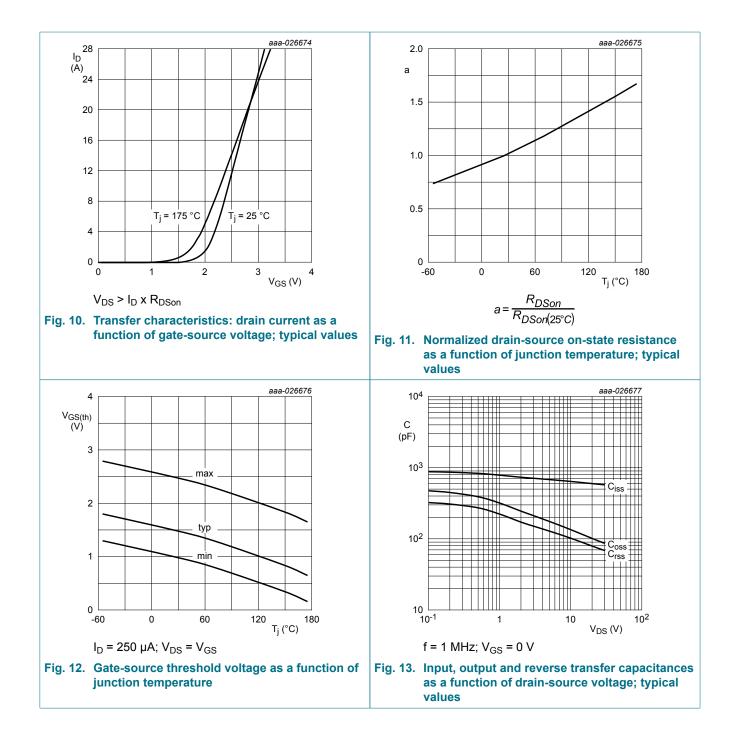


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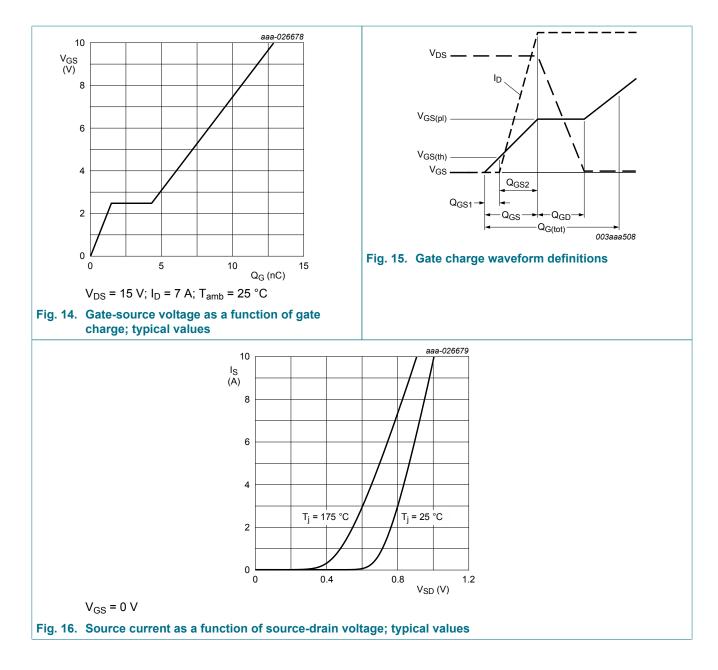
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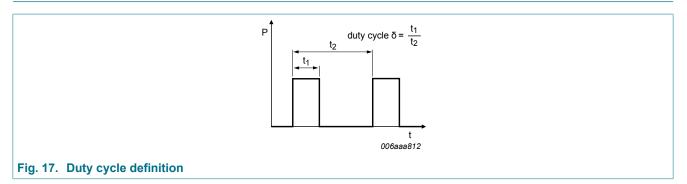
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11. Test information



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12. Package outline

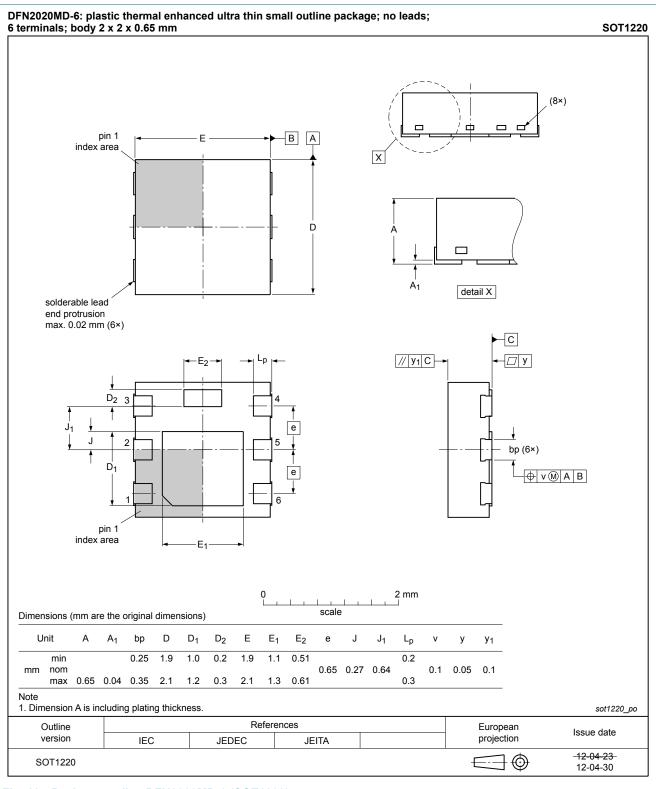
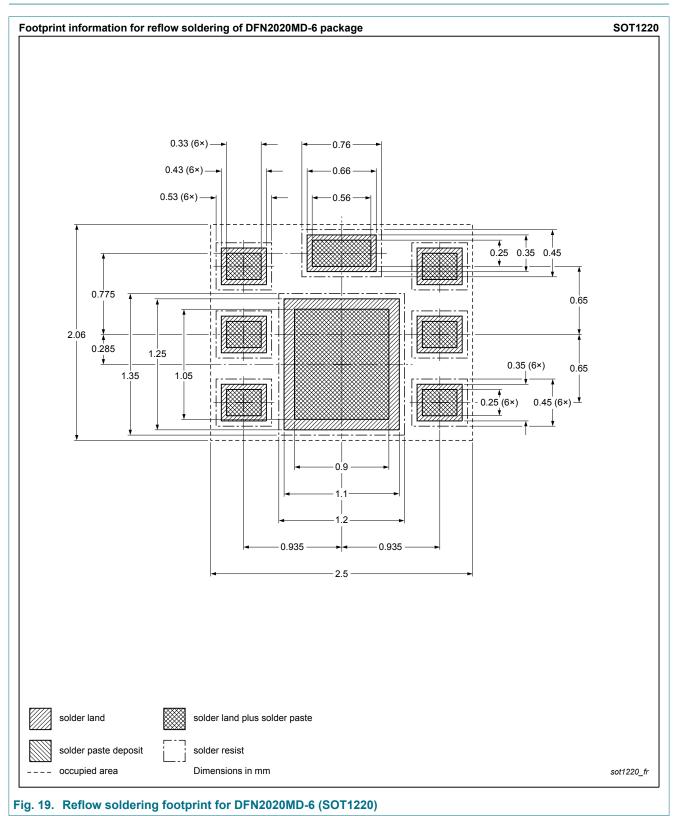


Fig. 18. Package outline DFN2020MD-6 (SOT1220)

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13. Soldering



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14. Revision history

Table 8. Revision history						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
PMPB25ENE v.1	20180426	Product data sheet	-	-		

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

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