74AXP1G14 Low-power Schmitt trigger inverter Rev. 1 – 28 August 2014

**Product data sheet** 

### 1. General description

The 74AXP1G14 is a single inverter with Schmitt trigger input. It transforms slowly changing input signals into sharply defined, jitter-free output signals.

This device ensures very low static and dynamic power consumption across the entire  $V_{CC}$  range from 0.7 V to 2.75 V. It is fully specified for partial power down applications using  $I_{OFF}$ . The  $I_{OFF}$  circuitry disables the output, preventing the potentially damaging backflow current through the device when it is powered down.

### 2. Features and benefits

- Wide supply voltage range from 0.7 V to 2.75 V
- Low input capacitance; C<sub>I</sub> = 0.5 pF (typical)
- Low output capacitance; C<sub>O</sub> = 1.0 pF (typical)
- Low dynamic power consumption; C<sub>PD</sub> = 2.4 pF at V<sub>CC</sub> = 1.2 V (typical)
- Low static power consumption; I<sub>CC</sub> = 0.6 μA (85 °C maximum)
- High noise immunity
- Complies with JEDEC standard:
  - JESD8-12A.01 (1.1 V to 1.3 V)
  - JESD8-11A.01 (1.4 V to 1.6 V)
  - JESD8-7A (1.65 V to 1.95 V)
  - JESD8-5A.01 (2.3 V to 2.7 V)
- ESD protection:
  - HBM ANSI/ESDA/JEDEC JS-001 Class 2 exceeds 2 kV
  - CDM JESD22-C101E exceeds 1000 V
- Latch-up performance exceeds 100 mA per JESD 78 Class II
- Inputs accept voltages up to 2.75 V
- Low noise overshoot and undershoot < 10 % of V<sub>CC</sub>
- I<sub>OFF</sub> circuitry provides partial Power-down mode operation
- Multiple package options
- Specified from –40 °C to +85 °C

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# 3. Ordering information

Table 1.	Ordering information
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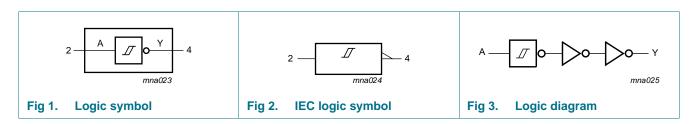
Type number	Package							
	Temperature range	Name	Description	Version				
74AXP1G14GM	–40 °C to +85 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 $\times$ 1.45 $\times$ 0.5 mm	SOT886				
74AXP1G14GN	–40 °C to +85 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body $0.9 \times 1.0 \times 0.35$ mm	SOT1115				
74AXP1G14GS	–40 °C to +85 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body $1.0 \times 1.0 \times 0.35$ mm	SOT1202				
74AXP1G14GX	–40 °C to +85 °C	X2SON5	X2SON5: plastic thermal enhanced extremely thin small outline package; no leads; 5 terminals; body $0.8 \times 0.8 \times 0.35$ mm	SOT1226				

### 4. Marking

Table 2.   Marking	
Type number	Marking code <sup>[1]</sup>
74AXP1G14GM	rF
74AXP1G14GN	rF
74AXP1G14GS	rF
74AXP1G14GX	rF

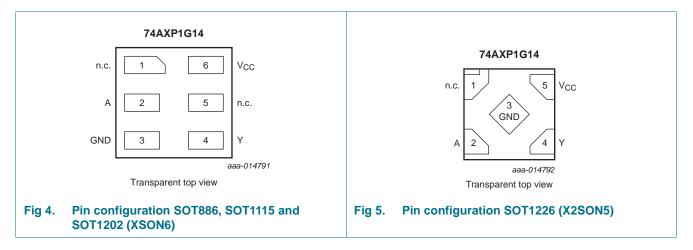
[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

# 5. Functional diagram



### 6. Pinning information

### 6.1 Pinning



### 6.2 Pin description

Symbol	Pin		Description
	X2SON5	XSON6	
n.c.	1	1	not connected
٩	2	2	data input
GND	3	3	ground (0 V)
Y	4	4	data output
1.C.	-	5	not connected
V <sub>CC</sub>	5	6	supply voltage

# 7. Functional description

#### Table 4. Function table<sup>[1]</sup>

Input	Output
A	Y
L	Н
Н	L

[1] H = HIGH voltage level; L = LOW voltage level.

# 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CC</sub>	supply voltage			-0.5	+3.3	V
I <sub>IK</sub>	input clamping current	V <sub>I</sub> < 0 V		-50	-	mA
VI	input voltage		[1]	-0.5	+3.3	V
I <sub>ОК</sub>	output clamping current	V <sub>O</sub> < 0 V		-50	-	mA
Vo	output voltage		[1]	-0.5	+3.3	V
lo	output current	$V_{O} = 0 V$ to $V_{CC}$		-	±20	mA
I <sub>CC</sub>	supply current			-	50	mA
I <sub>GND</sub>	ground current			-50	-	mA
T <sub>stg</sub>	storage temperature			-65	+150	°C
P <sub>tot</sub>	total power dissipation	$T_{amb} = -40 \ ^{\circ}C \ to \ +85 \ ^{\circ}C$		-	250	mW

[1] The minimum input and output voltage ratings may be exceeded if the input and output current ratings are observed.

# 9. Recommended operating conditions

#### Table 6. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CC</sub>	supply voltage		0.7	2.75	V
VI	input voltage		0	2.75	V
Vo	output voltage	Active mode	0	V <sub>CC</sub>	V
		Power-down mode; $V_{CC} = 0 V$	0	2.75	V
T <sub>amb</sub>	ambient temperature		-40	+85	°C

# **10. Static characteristics**

### Table 7. Static characteristics

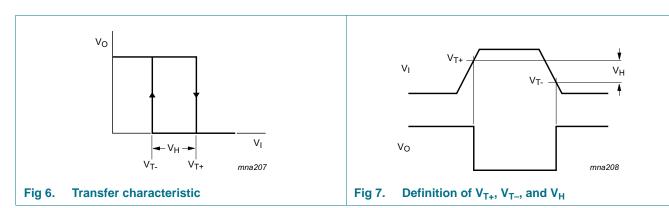
At recommended operating conditions, unless otherwise specified; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions			T <sub>amb</sub> = -40	°C to +85 °C	;	Unit
			Min	Тур 25 °С	Max 25 °C	Max 85 °C		
V <sub>T+</sub>	positive-going	see Figure 6 and Figure 7		I	1	1	1	
	threshold voltage	$V_{CC} = 0.75 \text{ V} \text{ to } 0.85 \text{ V}$		$0.3V_{CC}$	-	0.8V <sub>CC</sub>	0.8V <sub>CC</sub>	V
		V <sub>CC</sub> = 1.1 V to 1.95 V		$0.4V_{CC}$	-	0.7V <sub>CC</sub>	0.7V <sub>CC</sub>	V
		$V_{CC}$ = 2.3 V to 2.7 V		0.9	-	1.7	1.7	V
V <sub>T-</sub>	negative-going	see Figure 6 and Figure 7						
	threshold voltage	$V_{CC} = 0.75 \text{ V} \text{ to } 0.85 \text{ V}$		$0.2V_{CC}$	-	$0.7V_{CC}$	0.7V <sub>CC</sub>	V
		V <sub>CC</sub> = 1.1 V to 1.95 V		0.3V <sub>CC</sub>	-	0.6V <sub>CC</sub>	0.6V <sub>CC</sub>	V
		$V_{CC}$ = 2.3 V to 2.7 V		0.7	-	1.5	1.5	V
V <sub>H</sub>	hysteresis	see Figure 6 and Figure 7						
	voltage	$V_{CC} = 0.75 \text{ V} \text{ to } 0.85 \text{ V}$		$0.06V_{CC}$	-	$0.5V_{CC}$	$0.5V_{CC}$	V
		V <sub>CC</sub> = 1.1 V to 1.95 V		0.1V <sub>CC</sub>	-	0.4V <sub>CC</sub>	0.4V <sub>CC</sub>	V
	$V_{CC}$ = 2.3 V to 2.7 V		0.2	-	1.0	1.0	V	
V <sub>OH</sub>	V <sub>OH</sub> HIGH-level output voltage	$I_0 = -20 \ \mu A; V_{CC} = 0.7 \ V$		-	0.69	-	-	V
		$I_0 = -100 \ \mu A; V_{CC} = 0.75 \ V$		0.65	-	-	-	V
		$I_0 = -2 \text{ mA}; V_{CC} = 1.1 \text{ V}$		0.825	-	-	-	V
		$I_0 = -3 \text{ mA}; V_{CC} = 1.4 \text{ V}$		1.05	-	-	-	V
		$I_0 = -4.5 \text{ mA}; V_{CC} = 1.65 \text{ V}$		1.2	-	-	-	V
		$I_0 = -8 \text{ mA}; V_{CC} = 2.3 \text{ V}$		1.7	-	-	-	V
V <sub>OL</sub>	LOW-level	$I_0 = 20 \ \mu A; \ V_{CC} = 0.7 \ V$		-	0.01	-	-	V
	output voltage	$I_0 = 100 \ \mu A; V_{CC} = 0.75 \ V$		-	-	0.1	0.1	V
		$I_0 = 2 \text{ mA}; V_{CC} = 1.1 \text{ V}$		-	-	0.275	0.275	V
		$I_0 = 3 \text{ mA}; V_{CC} = 1.4 \text{ V}$		-	-	0.35	0.35	V
		I <sub>O</sub> = 4.5 mA; V <sub>CC</sub> = 1.65 V		-	-	0.45	0.45	V
		$I_0 = 8 \text{ mA}; V_{CC} = 2.3 \text{ V}$		-	-	0.7	0.7	V
lı	input leakage current	$V_{I} = 0 V \text{ to } 2.75 V;$ $V_{CC} = 0 V \text{ to } 2.75 V$	[1]	-	0.001	±0.1	±0.5	μΑ
I <sub>OFF</sub>	power-off leakage current	$V_{I}$ or $V_{O} = 0$ V to 2.75 V; $V_{CC} = 0$ V	<u>[1]</u>	-	0.01	±0.1	±0.5	μΑ
∆l <sub>OFF</sub>	additional power-off leakage current	$V_1 \text{ or } V_0 = 0 \text{ V or } 2.75 \text{ V};$ $V_{CC} = 0 \text{ V to } 0.1 \text{ V}$	[1]	-	0.02	±0.1	±0.5	μA
I <sub>CC</sub>	supply current	$V_I = 0 V \text{ or } V_{CC}; I_O = 0 A$	<u>[1]</u>	-	0.01	0.3	0.6	μΑ
$\Delta I_{CC}$	additional supply current			-	2	100	150	μΑ
		1						

[1] Typical values are measured at V<sub>CC</sub> = 1.2 V.

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### 10.1 Waveform transfer characteristics

### **11. Dynamic characteristics**

#### Table 8.Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit, see <u>Figure 14</u>.

Symbol	Parameter	Conditions	Τ <sub>ε</sub>	amb = 25	°C	T <sub>amb</sub> = -4	Unit		
			Min	Typ[1]	Max	Min	Max		
t <sub>pd</sub> propagation delay		A to Y; see Figure 8 [2][3]							
		$V_{CC} = 0.75 \text{ V} \text{ to } 0.85 \text{ V}$	3	12	35	2	114	ns	
		V <sub>CC</sub> = 1.1 V to 1.3 V	2.0	4.6	7.2	1.8	7.5	ns	
		$V_{CC} = 1.4 \text{ V to } 1.6 \text{ V}$	1.6	3.5	5.0	1.4	5.3	ns	
		$V_{CC} = 1.65 \text{ V} \text{ to } 1.95 \text{ V}$	1.4	2.9	4.1	1.2	4.5	ns	
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$	1.2	2.3	3.2	1.0	3.5	ns	
t <sub>t</sub>	transition time	V <sub>CC</sub> = 2.7 V; see <u>Figure 8</u> [4]	-	-	-	1.0	-	ns	
CI	input capacitance	$V_{I} = 0 \text{ V or } V_{CC};$ $V_{CC} = 0 \text{ V to } 2.75 \text{ V}$	-	0.5	-	-	-	pF	
Co	output capacitance	$V_{O} = 0 V; V_{CC} = 0 V$	-	1.0	-	-	-	pF	
C <sub>PD</sub>	power dissipation	$f_i = 1 \text{ MHz}; V_i = 0 \text{ V to } V_{CC}$ [5]	1	1		1			
	capacitance	$V_{CC} = 0.75 \text{ V} \text{ to } 0.85 \text{ V}$	-	2.3	-	-	-	pF	
		$V_{CC} = 1.1 \text{ V to } 1.3 \text{ V}$	-	2.4	-	-	-	pF	
		$V_{CC} = 1.4 \text{ V to } 1.6 \text{ V}$	-	2.5	-	-	-	pF	
		$V_{CC} = 1.65 \text{ V} \text{ to } 1.95 \text{ V}$	-	2.6	-	-	-	pF	
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$	-	3.0	-	-	-	pF	

[1] All typical values are measured at nominal  $V_{CC}$ .

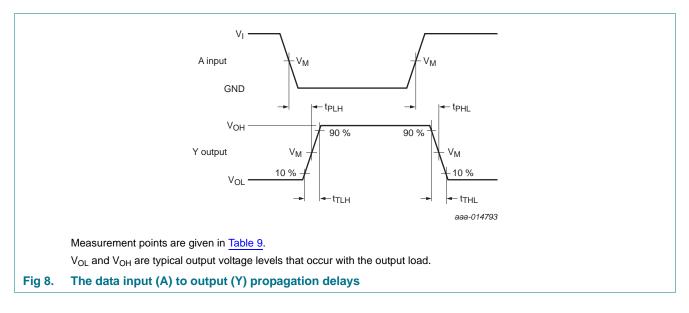
- [2]  $t_{pd}$  is the same as  $t_{PLH}$  and  $t_{PHL}$ .
- [3] For additional propagation delay values at different load capacitances, see Figure 9 to Figure 13.
- [4]  $t_t$  is the same as  $t_{THL}$  and  $t_{TLH}$ .
- [5]  $C_{PD}$  is used to determine the dynamic power dissipation (P<sub>D</sub> in  $\mu$ W).
  - $P_{D} = C_{PD} \times V_{CC}^{2} \times f_{i} \times N + C_{L} \times V_{CC}^{2} \times f_{o} \text{ where:}$

 $f_i$  = input frequency in MHz;

- $f_o = output frequency in MHz;$
- $C_L$  = output load capacitance in pF;
- $V_{CC}$  = supply voltage in V;

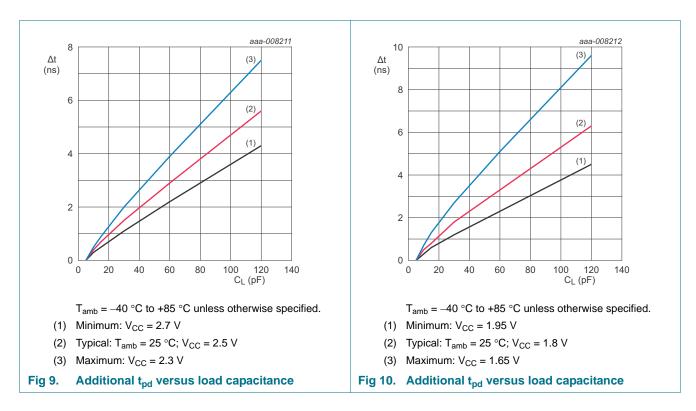
N = number of inputs switching.

### 12. Waveforms



#### Table 9. Measurement points

Supply voltage	Input	Output		
V <sub>CC</sub>	V <sub>M</sub>	VI	$\mathbf{t}_{r} = \mathbf{t}_{f}$	V <sub>M</sub>
0.75 V to 2.7 V	0.5V <sub>CC</sub>	V <sub>CC</sub>	≤ 3.0 ns	0.5V <sub>CC</sub>

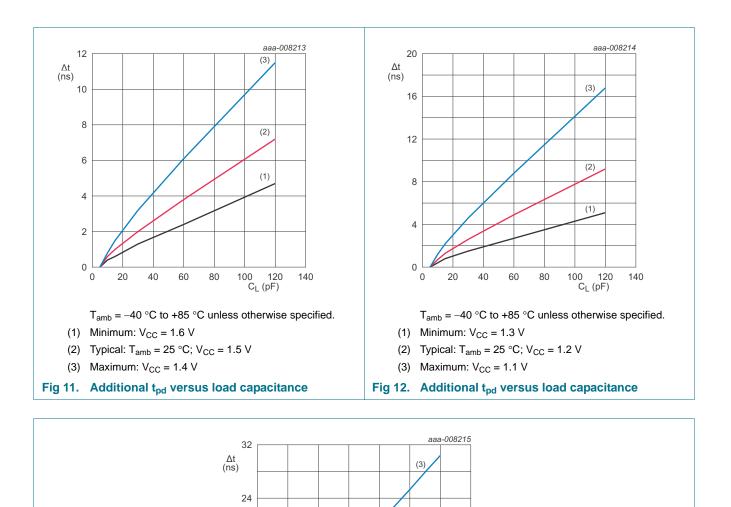


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 $T_{amb} = -40 \text{ °C to +85 °C unless otherwise specified.}$ 

16

8

- (1) Minimum:  $V_{CC} = 0.85 V$
- (2) Typical:  $T_{amb} = 25 \text{ °C}; V_{CC} = 0.8 \text{ V}$
- (3) Maximum:  $V_{CC} = 0.75 V$
- Fig 13. Additional t<sub>pd</sub> versus load capacitance

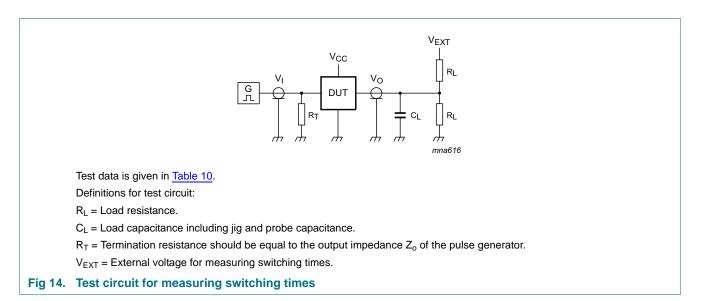
(2)

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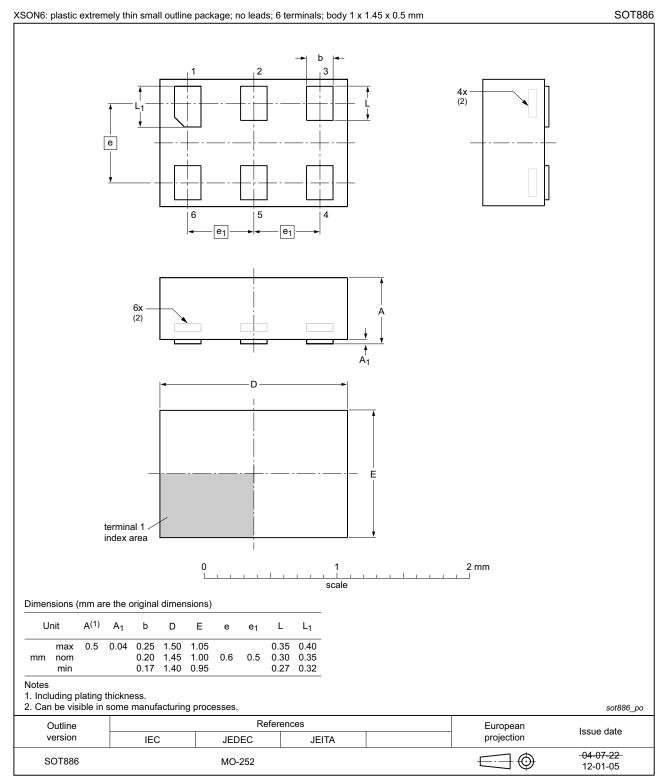
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#### Table 10. Test data

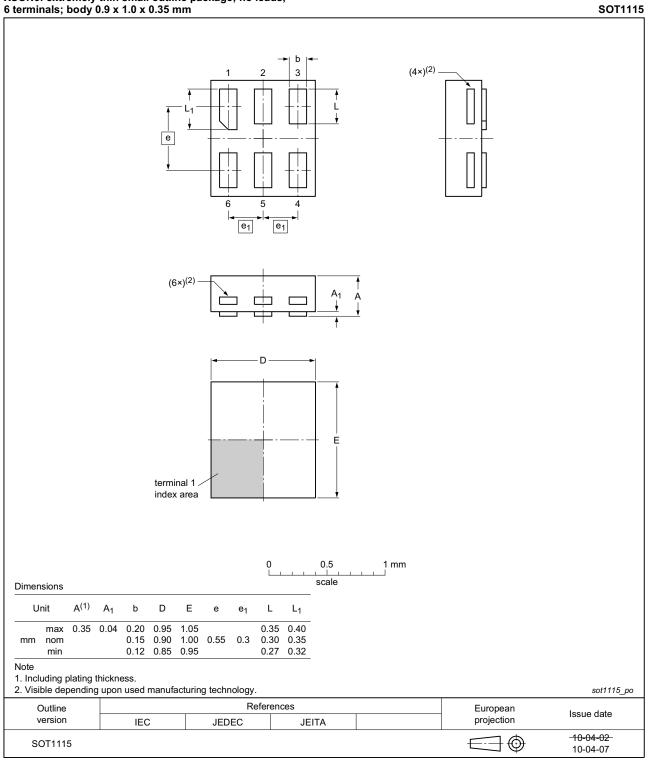
Supply voltage	Load		V <sub>EXT</sub>			
V <sub>cc</sub>	C <sub>L</sub> R <sub>L</sub>		t <sub>PLH</sub> , t <sub>PHL</sub>	t <sub>PZH</sub> , t <sub>PHZ</sub>	t <sub>PZL</sub> , t <sub>PLZ</sub>	
0.75 V to 2.7 V	5 pF	10 kΩ	0 V	0 V	$2 \times V_{CC}$	

# 13. Package outline



### Fig 15. Package outline SOT886 (XSON6)

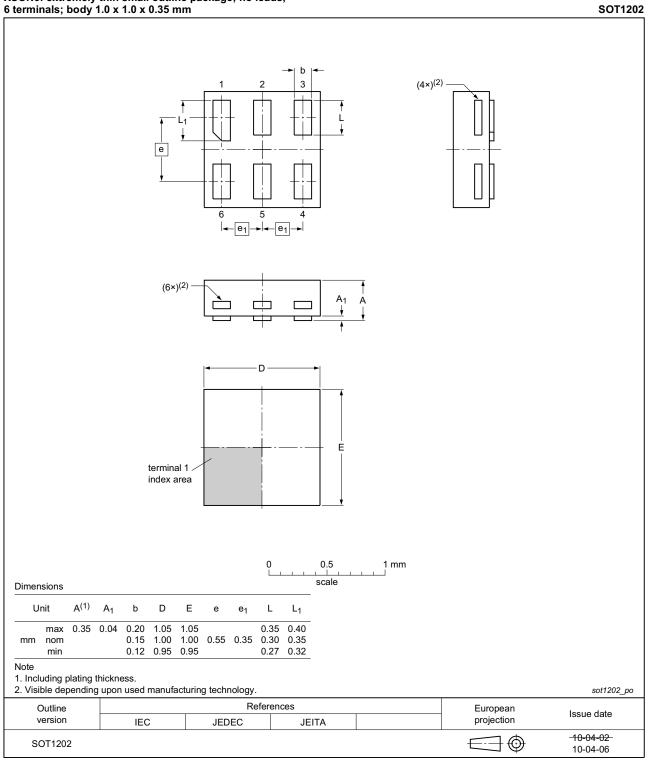
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# XSON6: extremely thin small outline package; no leads; 6 terminals; body 0.9 x 1.0 x 0.35 mm

Fig 16. Package outline SOT1115 (XSON6)

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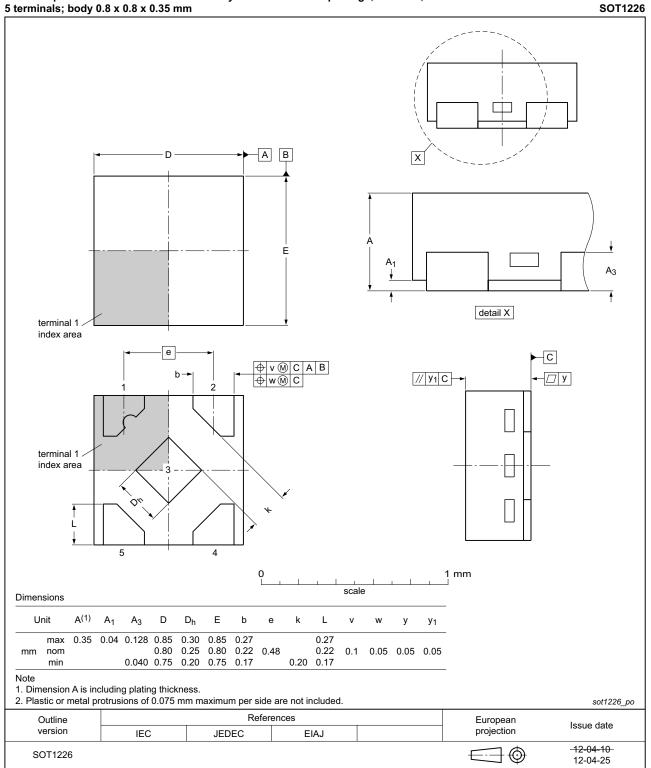
# XSON6: extremely thin small outline package; no leads; 6 terminals; body 1.0 x 1.0 x 0.35 mm

Fig 17. Package outline SOT1202 (XSON6)

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### X2SON5: plastic thermal enhanced extremely thin small outline package; no leads; 5 terminals; body 0.8 x 0.8 x 0.35 mm

#### Fig 18. Package outline SOT1226 (X2SON5)

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# 14. Abbreviations

Table 11. Abbreviations			
Acronym	Description		
CDM	Charged Device Model		
DUT	Device Under Test		
ESD	ElectroStatic Discharge		
НВМ	Human Body Model		

# **15. Revision history**

Table 12.	Revision	historv	

Document ID	Release date	Data sheet status	Change notice	Supersedes
74AXP1G14 v.1	20140828	Product data sheet	-	-

### **16. Legal information**

### 16.1 Data sheet status

Document status[1][2]	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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