Dual supply buffer/line driver; 3-state Rev. 2 — 22 March 2019

nexperia

# 1. General description

The 74AXP1T125 is a dual supply non-inverting buffer/line driver with 3-state output. It features one input (A), an output (Y), an output enable input ( $\overline{OE}$ ) and dual supply pins (V<sub>CCI</sub> and V<sub>CCO</sub>). A HIGH level at pin  $\overline{OE}$  causes the output to assume a high-impedance OFF-state. The inputs are referenced to V<sub>CCI</sub> and the output is referenced to V<sub>CCO</sub>. All inputs can be connected directly to V<sub>CCI</sub> or GND. V<sub>CCI</sub> can be supplied at any voltage between 0.7 V and 2.75 V and V<sub>CCO</sub> can be supplied at any voltage between 1.2 V and 5.5 V. This feature allows voltage level translation.

Schmitt-trigger action at all inputs makes the circuit tolerant of slower input rise and fall times.

This device ensures very low static and dynamic power consumption across the entire supply range and is fully specified for partial power down applications using  $I_{OFF}$ . The  $I_{OFF}$  circuitry disables the output, preventing the potentially damaging backflow current through the device when it is powered down.

# 2. Features and benefits

- Wide supply voltage range:
  - V<sub>CCI</sub>: 0.7 V to 2.75 V
  - V<sub>CCO</sub>: 1.2 V to 5.5 V
- Low input capacitance; C<sub>I</sub> = 0.6 pF (typical)
- Low output capacitance; C<sub>O</sub> = 1.8 pF (typical)
- Low dynamic power consumption;  $C_{PD} = 0.4 \text{ pF}$  at  $V_{CCI} = 1.2 \text{ V}$  (typical)
- Low dynamic power consumption; C<sub>PD</sub> = 7.1 pF at V<sub>CCO</sub> = 3.3 V (typical)
- Low static power consumption; I<sub>CCI</sub> = 0.5 µA (85 °C maximum)
- Low static power consumption; I<sub>CCO</sub> = 1.8 μA (85 °C maximum)
- High noise immunity
- Complies with JEDEC standard:
  - JESD8-12A.01 (1.1 V to 1.3 V; A, OE inputs)
  - JESD8-11A.01 (1.4 V to 1.6 V)
  - JESD8-7A (1.65 V to 1.95 V)
  - JESD8-5A.01 (2.3 V to 2.7 V)
  - JESD8-C (2.7 V to 3.6 V; Y output)
  - JESD12-6 (4.5 V to 5.5 V; Y output)
- ESD protection:
  - HBM ANSI/ESDA/JEDEC JS-001 Class 2 exceeds 2 kV
  - CDM JESD22-C101E exceeds 1000 V
- Latch-up performance exceeds 100 mA per JESD78D Class II
- Inputs accept voltages up to 2.75 V
- Low noise overshoot and undershoot < 10% of V<sub>CCO</sub>
- I<sub>OFF</sub> circuitry provides partial power-down mode operation
- Multiple package options
- Specified from -40 °C to +85 °C

# 3. Ordering information

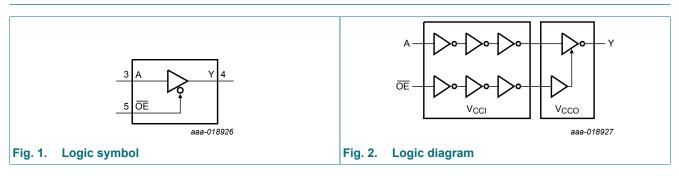
Type number	Package			
	Temperature range	Name	Description	
74AXP1T125GM	-40 °C to +85 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 x 1.45 x 0.5 mm	SOT886
74AXP1T125GN	-40 °C to +85 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 0.9 x 1.0 x 0.35 mm	SOT1115
74AXP1T125GS	-40 °C to +85 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 1.0 x 1.0 x 0.35 mm	SOT1202

# 4. Marking

Table 2. Marking							
Type number	Marking code[1]						
74AXP1T125GM	rN						
74AXP1T125GN	rN						
74AXP1T125GS	rN						

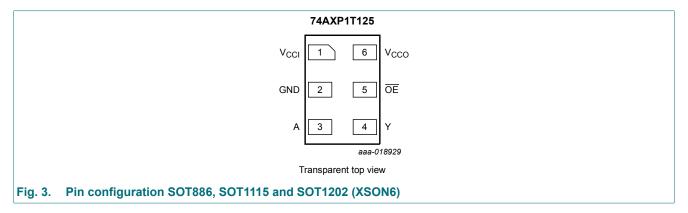
[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

# 5. Functional diagram



# 6. Pinning information

# 6.1. Pinning



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# 6.2. Pin description

Table 3. Pin description		
Symbol	Pin	Description
V <sub>CCI</sub>	1	input supply voltage
GND	2	ground (0 V)
A	3	data input A
Y	4	data output Y
OE	5	output enable input
V <sub>cco</sub>	6	output supply voltage

# 7. Functional description

### Table 4. Function table

H = HIGH voltage level; L = LOW voltage level; X = Don't care; Z = high-impedance OFF-state.

Supply voltage		Input		Output
V <sub>CCI</sub>	V <sub>cco</sub>	OE	Α	Y
0.7 V to 2.75 V	1.2 V to 5.5 V	L	L	L
0.7 V to 2.75 V	1.2 V to 5.5 V	L	Н	Н
0.7 V to 2.75 V	1.2 V to 5.5 V	Н	Х	Z
GND	1.2 V to 5.5 V	Х	Х	Z
0.7 V to 2.75 V	GND	Х	Х	Z
GND	GND	Х	Х	Z

# 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CCI</sub>	input supply voltage			-0.5	3.3	V
V <sub>CCO</sub>	output supply voltage			-0.5	6.0	V
I <sub>IK</sub>	input clamping current	V <sub>I</sub> < 0 V		-50	-	mA
VI	input voltage		[1]	-0.5	3.3	V
I <sub>ОК</sub>	output clamping current	V <sub>O</sub> < 0 V		-50	-	mA
Vo	output voltage	Active mode	[1][2]	-0.5	V <sub>CCO</sub> + 0.5	V
		Power-down or 3-state mode	[1]	-0.5	6.0	V
I <sub>O</sub>	output current	$V_{O} = 0 V$ to $V_{CCO}$		-	±25	mA
I <sub>CCI</sub>	input supply current			-	50	mA
I <sub>CCO</sub>	output supply current			-	50	mA
I <sub>GND</sub>	ground current			-50	-	mA
T <sub>stg</sub>	storage temperature			-65	+150	°C
P <sub>tot</sub>	total power dissipation	$T_{amb}$ = -40 °C to +85 °C	[3]	-	250	mW

[1] The minimum input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2]  $V_{CCO}$  + 0.5 V should not exceed 6.0 V.

[3] For SOT886 and SOT1202 packages: above 74 °C the value of Ptot derates linearly with 3.3 mW/K.

For SOT1115 package: above 70 °C the value of Ptot derates linearly with 3.2 mW/K.

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# 9. Recommended operating conditions

### Table 6. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CCI</sub>	input supply voltage		0.7	2.75	V
V <sub>CCO</sub>	output supply voltage		1.2	5.5	V
VI	input voltage		0	2.75	V
Vo	output voltage	Active mode	0	V <sub>CCO</sub>	V
		Power-down or 3-state mode	0	V <sub>CCO</sub> V           5.5         V	
T <sub>amb</sub>	ambient temperature		-40	+85	°C
Δt/ΔV	input transition rise and fall rate	V <sub>CCI</sub> = 0.7 V to 2.75 V	0	200	ns/V

# **10. Static characteristics**

## Table 7. Static characteristics

At recommended operating conditions, unless otherwise specified; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Ta	<sub>mb</sub> = 25	°C	T <sub>amb</sub> = -40 °	°C to +85 °C	Unit
				Min	Тур	Max	Min	Max	
VIH	HIGH-level	V <sub>CCI</sub> = 0.75 V to 0.85 V	0.	.75V <sub>CCI</sub>	-	-	0.75V <sub>CCI</sub>	-	V
	input voltage	V <sub>CCI</sub> = 1.1 V to 1.95 V	0.	.65V <sub>CCI</sub>	-	-	0.65V <sub>CCI</sub>	-	V
		V <sub>CCI</sub> = 2.3 V to 2.7 V		1.6	-	-	1.6	-	V
V <sub>IL</sub>	LOW-level	V <sub>CCI</sub> = 0.75 V to 0.85 V		-	-	$0.25V_{CCI}$	-	0.25V <sub>CCI</sub>	V
	input voltage	V <sub>CCI</sub> = 1.1 V to 1.95 V		-	-	$0.35V_{CCI}$	-	0.35V <sub>CCI</sub>	V
		V <sub>CCI</sub> = 2.3 V to 2.7 V		-	-	0.7	-	0.7	V
V <sub>OH</sub>	HIGH-level	I <sub>O</sub> = -2 mA; V <sub>CCO</sub> = 1.2 V	[1]	-	1.05	-	-	-	V
	output voltage	I <sub>O</sub> = -3 mA; V <sub>CCO</sub> = 1.4 V		1.05	-	-	1.05	-	V
		I <sub>O</sub> = -4.5 mA; V <sub>CCO</sub> = 1.65 V		1.2	-	-	1.2	-	V
		$I_0$ = -8 mA; $V_{CCO}$ = 2.3 V		1.7	-	-	1.7	-	V
		I <sub>O</sub> = -10 mA; V <sub>CCO</sub> = 3.0 V		2.2	-	-	2.2	-	V
		I <sub>O</sub> = -12 mA; V <sub>CCO</sub> = 4.5 V		3.7	-	-	3.7	-	V
V <sub>OL</sub>	LOW-level	I <sub>O</sub> = 2 mA; V <sub>CCO</sub> = 1.2 V	[1]	-	0.18	-	-	-	V
	output voltage	I <sub>O</sub> = 3 mA; V <sub>CCO</sub> = 1.4 V		-	-	0.35	-	0.35	V
		I <sub>O</sub> = 4.5 mA; V <sub>CCO</sub> = 1.65 V		-	-	0.45	-	0.45	V
		I <sub>O</sub> = 8 mA; V <sub>CCO</sub> = 2.3 V		-	-	0.7	-	0.7	V
		I <sub>O</sub> = 10 mA; V <sub>CCO</sub> = 3.0 V		-	-	0.8	-	0.8	V
		I <sub>O</sub> = 12 mA; V <sub>CCO</sub> = 4.5 V		-	-	0.8	-	0.8	V
l <sub>l</sub>	input leakage current	V <sub>I</sub> = 0 V to 2.75 V; V <sub>CCI</sub> = 0 V to 2.75 V	[1]	-	±0.001	±0.1	-	±0.5	μA
I <sub>OZ</sub>	OFF-state output current	V <sub>O</sub> = 0 V to 5.5 V; V <sub>CCO</sub> = 1.2 V to 5.5 V		-	±0.001	±0.1	-	±0.5	μA

## Dual supply buffer/line driver; 3-state

Symbol	Parameter	Conditions		т	<sub>amb</sub> = 25 °	°C	T <sub>amb</sub> = -40 °	°C to +85 °C	Unit
				Min	Тур	Max	Min	Max	
I <sub>OFF</sub>	power-off leakage current	inputs; $V_1 = 0 V$ to 2.75 V; $V_{CC1} = 0 V$ ; $V_{CCO} = 0 V$ to 5.5 V	[1]	-	±0.01	±0.1	-	±0.5	μA
		output; $V_0 = 0 V$ to 5.5 V; $V_{CC0} = 0 V$ ; $V_{CC1} = 0 V$ to 2.75 V; $V_1 = 0 V$ to 2.75 V	[1]	-	±0.01	±0.1	-	±0.5	μA
	additional power-off leakage	inputs; $V_1 = 0 V \text{ or } 2.75 V$ ; $V_{CC1} = 0 V \text{ to } 0.1 V$ ; $V_{CCO} = 0 V \text{ to } 5.5 V$	[1]	-	±0.02	±0.1	-	±0.5	μA
	current	output; $V_0 = 0 V \text{ or } 5.5 V$ ; $V_{CC0} = 0 V \text{ to } 0.1 V$ ; $V_{CC1} = 0 V \text{ to } 2.75 V$ ; $V_1 = 0 V \text{ or } 2.75 V$	[1]	-	±0.02	±0.1	-	±0.5	μA

[1] Typical values are measured at  $V_{CCI} = V_{CCO} = 1.2$  V unless otherwise specified.

## Table 8. Static characteristics supply current

At recommended operating conditions, unless otherwise specified; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		T <sub>amb</sub> = -40 °	°C to +85 °C		Unit
			Typ 25 °C	Max 25 °C	Typ 85 °C	Max 85 °C	
I <sub>CCI</sub>	input supply	$V_{I} = 0 V \text{ or } V_{CCI};$					
	current	V <sub>CCI</sub> = 0.7 V to 1.3 V [1	1	100	10	300	nA
		V <sub>CCI</sub> = 1.3 V to 2.75 V [2	1	100	20	500	nA
		V <sub>CCI</sub> = 2.75 V; V <sub>CCO</sub> = 0 V	1	100	20	500	nA
		V <sub>CCI</sub> = 0 V; V <sub>CCO</sub> = 5.5 V	1	100	1	100	nA
I <sub>CCO</sub>	output supply current	$V_I = 0 V \text{ or } V_{CCI}; I_O = 0 A;$ see <u>Table 9</u>					
		V <sub>CCO</sub> = 1.2 V to 3.6 V [1	0.001	1.0	0.01	1.2	μA
		V <sub>CCO</sub> = 3.6 V to 5.5 V [3	0.8	1.5	1.0	1.8	μA
		V <sub>CCI</sub> = 2.75 V; V <sub>CCO</sub> = 0 V	0.001	0.1	0.003	0.2	μA
		V <sub>CCI</sub> = 0 V; V <sub>CCO</sub> = 3.6 V	0.2	0.6	0.3	0.8	μA
		V <sub>CCI</sub> = 0 V; V <sub>CCO</sub> = 5.5 V	0.4	0.8	0.5	1.0	μA
∆I <sub>CCI</sub>	additional input supply current	$V_{I} = V_{CCI} - 0.5 V; V_{CCI} = 2.5 V$	2	100	14	150	μA

Typical values are measured at V<sub>CCI</sub> = V<sub>CCO</sub> = 1.2 V unless otherwise specified. Typical values are measured at V<sub>CCI</sub> = V<sub>CCO</sub> = 2.5 V. Typical values are measured at V<sub>CCI</sub> = 1.2 V and V<sub>CCO</sub> = 5.0 V. [1]

[2]

[3]

### Table 9. Typical output supply current (I<sub>CCO</sub>)

V <sub>CCI</sub>	V <sub>cco</sub>	V <sub>cco</sub>								
	0 V	1.2 V	1.5 V	1.8 V	2.5 V	3.3 V	5.0 V			
0 V	0	1	5	20	100	200	400	nA		
0.8 V	1	10	150	200	300	500	800	nA		
1.2 V	1	1	5	200	300	500	800	nA		
1.5 V	1	1	5	100	300	500	800	nA		
1.8 V	1	1	5	100	300	500	800	nA		
2.5 V	1	1	5	100	100	500	800	nA		

# **11. Dynamic characteristics**

## Table 10. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see <u>Fig. 12</u>; for waveform see <u>Fig. 4</u> and <u>Fig. 5</u>.

Symbol	Parameter	Conditions				V <sub>cco</sub>				Unit
			1.2 V	1	.5 V ± 0.1	۷	1.	8 V ± 0.15	V	
			Typ[1]	Min	Typ[1]	Max	Min	Typ[1]	Max	
T <sub>amb</sub> = 2	25 °C									
t <sub>pd</sub>	propagation	A to Y [2]								
	delay	V <sub>CCI</sub> = 0.75 V to 0.85 V	22	3	16	61	3	15	57	ns
		V <sub>CCI</sub> = 1.1 V to 1.3 V	16.2	3.1	10.3	19.8	2.8	8.2	15.8	ns
		V <sub>CCI</sub> = 1.4 V to 1.6 V	15.4	2.8	9.5	18.2	2.5	7.4	13.2	ns
		V <sub>CCI</sub> = 1.65 V to 1.95 V	15.0	2.7	9.1	17.4	2.4	7.0	11.9	ns
		V <sub>CCI</sub> = 2.3 V to 2.7 V	14.7	2.5	8.7	16.9	2.2	6.6	11.1	ns
t <sub>en</sub>	enable time	OE to Y [3]								
		V <sub>CCI</sub> = 0.75 V to 0.85 V	25	3	20	76	3	18	72	ns
		V <sub>CCI</sub> = 1.1 V to 1.3 V	17.9	3.1	11.3	18.9	2.8	9.0	15.5	ns
		V <sub>CCI</sub> = 1.4 V to 1.6 V	16.9	2.8	10.3	17.5	2.5	8.1	13.9	ns
		V <sub>CCI</sub> = 1.65 V to 1.95 V	16.5	2.7	9.9	16.9	2.4	7.6	13.3	ns
		V <sub>CCI</sub> = 2.3 V to 2.7 V	16.0	2.5	9.4	16.4	2.2	7.1	12.7	ns
t <sub>dis</sub>	disable time	OE to Y [4]								
		V <sub>CCI</sub> = 0.75 V to 0.85 V	25	3	20	76	3	20	72	ns
		V <sub>CCI</sub> = 1.1 V to 1.3 V	17.1	3.1	12.0	18.3	2.8	11.3	17.4	ns
		V <sub>CCI</sub> = 1.4 V to 1.6 V	16.1	2.8	11.1	17.4	2.5	10.3	16.0	ns
		V <sub>CCI</sub> = 1.65 V to 1.95 V	15.6	2.7	10.6	16.5	2.4	9.9	15.5	ns
		V <sub>CCI</sub> = 2.3 V to 2.7 V	15.0	2.5	10.0	16.3	2.2	9.4	15.0	ns
T <sub>amb</sub> = -	40 °C to +85 °	°C						-		
t <sub>pd</sub>	propagation	A to Y [2]								
	delay	V <sub>CCI</sub> = 0.75 V to 0.85 V	22	3	16	136	3	15	133	ns
		V <sub>CCI</sub> = 1.1 V to 1.3 V	16.2	3.1	10.3	19.8	2.8	8.2	15.8	ns
		V <sub>CCI</sub> = 1.4 V to 1.6 V	15.4	2.8	9.5	18.2	2.5	7.4	13.2	ns
		V <sub>CCI</sub> = 1.65 V to 1.95 V	15.0	2.7	9.1	17.4	2.4	7.0	11.9	ns
		V <sub>CCI</sub> = 2.3 V to 2.7 V	14.7	2.5	8.7	16.9	2.2	6.6	11.1	ns
t <sub>en</sub>	enable time	OE to Y [3]								
		V <sub>CCI</sub> = 0.75 V to 0.85 V	25	3	20	151	3	18	148	ns
		V <sub>CCI</sub> = 1.1 V to 1.3 V	17.9	3.1	11.3	18.9	2.8	9.0	15.5	ns
		V <sub>CCI</sub> = 1.4 V to 1.6 V	16.9	2.8	10.3	17.5	2.5	8.1	13.9	ns
		V <sub>CCI</sub> = 1.65 V to 1.95 V	16.5	2.7	9.9	16.9	2.4	7.6	13.3	ns
		V <sub>CCI</sub> = 2.3 V to 2.7 V	16.0	2.5	9.4	16.4	2.2	7.1	12.7	ns
t <sub>dis</sub>	disable time	OE to Y [4]								
		V <sub>CCI</sub> = 0.75 V to 0.85 V	25	3	20	151	3	20	148	ns
		V <sub>CCI</sub> = 1.1 V to 1.3 V	17.1	3.1	12.0	18.3	2.8	11.3	17.4	ns
		V <sub>CCI</sub> = 1.4 V to 1.6 V	16.1	2.8	11.1	17.4	2.5	10.3	16.0	ns
		V <sub>CCI</sub> = 1.65 V to 1.95 V	15.6	2.7	10.6	16.5	2.4	9.9	15.5	ns
		V <sub>CCI</sub> = 2.3 V to 2.7 V	15.0	2.5	10.0	16.3	2.2	9.4	15.0	ns

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## Dual supply buffer/line driver; 3-state

Symbol	Parameter	Conditions				V <sub>cco</sub>				Unit
			1.2 V	1.2 V 1.5 V ± 0.1 V		1.8 V ± 0.15 V				
			Typ[1]	Min	Typ[1]	Max	Min	Typ <mark>[1]</mark>	Max	
t <sub>t</sub>	transition time	$V_{CCI} = 0.75 V \text{ to } 2.7 V$ [5]	-	1.0	-	-	1.0	-	-	ns

[1] Typical values are measured at nominal supply voltages and  $T_{amb}$  = +25 °C.

[2]  $t_{pd}$  is the same as  $t_{PLH}$  and  $t_{PHL}$ .

[3]  $t_{en}$  is the same as  $t_{PZH}$  and  $t_{PZL}$ .

[4]  $t_{dis}$  is the same as  $t_{PHZ}$  and  $t_{PLZ}$ .

[5]  $t_t$  is the same as  $t_{THL}$  and  $t_{TLH}$ .

### Table 11. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see Fig. 12; for waveform see Fig. 4 and Fig. 5.

Symbol	Parameter	Conditions	V <sub>cco</sub>									Unit
			2.5 V ± 0.2 V			3.	3 V ± 0.3	3 V	5.0 V ± 0.5 V			
			Min	Typ[1]	Мах	Min	Typ[1]	Мах	Min	Typ[1]	Мах	
T <sub>amb</sub> = 2	5 °C	-		1								
t <sub>pd</sub>	propagation	A to Y [2]										
	delay	V <sub>CCI</sub> = 0.75 V to 0.85 V	2	13	57	2	13	65	2	14	77	ns
		V <sub>CCI</sub> = 1.1 V to 1.3 V	2.4	6.5	10.8	2.2	5.9	9.5	2.1	5.6	9.0	ns
		V <sub>CCI</sub> = 1.4 V to 1.6 V	2.1	5.7	9.1	2.0	5.1	8.2	1.9	4.8	7.7	ns
		V <sub>CCI</sub> = 1.65 V to 1.95 V	2.0	5.3	8.7	1.8	4.7	7.7	1.8	4.4	7.3	ns
		V <sub>CCI</sub> = 2.3 V to 2.7 V	1.9	4.9	8.1	1.7	4.3	7.1	1.6	4.0	6.6	ns
t <sub>en</sub>	enable time	OE to Y [3]										
		V <sub>CCI</sub> = 0.75 V to 0.85 V	2	17	72	2	17	80	2	20	92	ns
		V <sub>CCI</sub> = 1.1 V to 1.3 V	2.4	7.2	12.5	2.2	6.7	11.4	2.1	6.5	11.2	ns
		V <sub>CCI</sub> = 1.4 V to 1.6 V	2.1	6.3	11.0	2.0	5.7	10.2	1.9	5.5	9.8	ns
		V <sub>CCI</sub> = 1.65 V to 1.95 V	2.0	5.8	10.5	1.8	5.2	9.8	1.8	5.0	9.2	ns
		V <sub>CCI</sub> = 2.3 V to 2.7 V	1.9	5.3	9.9	1.7	4.7	9.1	1.6	4.5	8.6	ns
t <sub>dis</sub>	disable time	OE to Y [4]										
		V <sub>CCI</sub> = 0.75 V to 0.85 V	2	17	72	2	18	80	2	16	92	ns
		V <sub>CCI</sub> = 1.1 V to 1.3 V	2.4	9.2	14.2	2.2	9.9	15.2	2.1	8.3	13.2	ns
		V <sub>CCI</sub> = 1.4 V to 1.6 V	2.1	8.2	13.1	2.0	9.1	14.1	1.9	7.4	12.1	ns
		V <sub>CCI</sub> = 1.65 V to 1.95 V	2.0	7.8	12.6	1.8	8.6	13.7	1.8	7.0	11.7	ns
		V <sub>CCI</sub> = 2.3 V to 2.7 V	1.9	7.2	12.1	1.7	8.1	13.2	1.6	6.5	11.2	ns

Symbol	Parameter	Conditions	V <sub>cco</sub>									Unit
			2.5 V ± 0.2 V			3.3 V ± 0.3 V			5.0 V ± 0.5 V			
			Min	Typ[1]	Max	Min	Typ[1]	Max	Min	Typ[1]	Мах	
T <sub>amb</sub> = -4	40 °C to +85 °	C				I	1			1		
t <sub>pd</sub>		A to Y [2]										
	delay	V <sub>CCI</sub> = 0.75 V to 0.85 V	2	13	152	2	13	179	2	14	210	ns
		V <sub>CCI</sub> = 1.1 V to 1.3 V	2.4	6.5	10.8	2.2	5.9	9.5	2.1	5.6	9.0	ns
		V <sub>CCI</sub> = 1.4 V to 1.6 V	2.1	5.7	9.1	2.0	5.1	8.2	1.9	4.8	7.7	ns
		V <sub>CCI</sub> = 1.65 V to 1.95 V	2.0	5.3	8.7	1.8	4.7	7.7	1.8	4.4	7.3	ns
		V <sub>CCI</sub> = 2.3 V to 2.7 V	1.9	4.9	8.1	1.7	4.3	7.1	1.6	4.0	6.6	ns
t <sub>en</sub>	enable time	OE to Y [3]										
		V <sub>CCI</sub> = 0.75 V to 0.85 V	2	17	167	2	17	194	2	20	225	ns
		V <sub>CCI</sub> = 1.1 V to 1.3 V	2.4	7.2	12.5	2.2	6.7	11.4	2.1	6.5	11.2	ns
		V <sub>CCI</sub> = 1.4 V to 1.6 V	2.1	6.3	11.0	2.0	5.7	10.2	1.9	5.5	9.8	ns
		V <sub>CCI</sub> = 1.65 V to 1.95 V	2.0	5.8	10.5	1.8	5.2	9.8	1.8	5.0	9.2	ns
		V <sub>CCI</sub> = 2.3 V to 2.7 V	1.9	5.3	9.9	1.7	4.7	9.1	1.6	4.5	8.6	ns
t <sub>dis</sub>	disable time	OE to Y [4]										
		V <sub>CCI</sub> = 0.75 V to 0.85 V	2	17	167	2	18	194	2	16	225	ns
		V <sub>CCI</sub> = 1.1 V to 1.3 V	2.4	9.2	14.2	2.2	9.9	15.2	2.1	8.3	13.2	ns
		V <sub>CCI</sub> = 1.4 V to 1.6 V	2.1	8.2	13.1	2.0	9.1	14.1	1.9	7.4	12.1	ns
		V <sub>CCI</sub> = 1.65 V to 1.95 V	2.0	7.8	12.6	1.8	8.6	13.7	1.8	7.0	11.7	ns
		V <sub>CCI</sub> = 2.3 V to 2.7 V	1.9	7.2	12.1	1.7	8.1	13.2	1.6	6.5	11.2	ns
tt	transition time	V <sub>CCO</sub> = 5.5 V [5]	1.0	-	-	1.0	-	-	1.0	-	-	ns

## Table 12. Typical dynamic characteristics at T<sub>amb</sub> = 25 °C

Voltages are referenced to GND (ground = 0 V); for test circuit see Fig. 12; for waveform see Fig. 4 and Fig. 5.

Symbol	ol Parameter Conditions			V <sub>cco</sub>						
				1.2 V	1.5 V	1.8 V	2.5 V	3.3 V	5.0 V	1
C <sub>PD</sub>	power	$f_i = 1 \text{ MHz}; R_L = \infty \Omega; V_I = 0 \text{ V to } V_{CCI}$	[1]							
	dissipation capacitance	input supply	[2]							
	capacitance	V <sub>CCI</sub> = 0.8 V		0.4	0.4	0.4	0.4	0.4	0.4	pF
		V <sub>CCI</sub> = 1.2 V		0.4	0.4	0.4	0.4	0.4	0.4	pF
		V <sub>CCI</sub> = 1.5 V		0.5	0.5	0.5	0.5	0.5	0.5	pF
		V <sub>CCI</sub> = 1.8 V		0.5	0.5	0.5	0.5	0.5	0.5	pF
		V <sub>CCI</sub> = 2.5 V		0.7	0.7	0.7	0.7	0.7	0.7	pF
		output supply	[3]							
		V <sub>CCI</sub> = 0.8 V		6.7	6.8	6.8	6.9	7.5	9.5	pF
		V <sub>CCI</sub> = 1.2 V		6.8	6.9	7.0	7.0	7.1	7.6	pF
		V <sub>CCI</sub> = 1.5 V		6.9	6.9	6.9	7.0	7.1	7.6	pF
		V <sub>CCI</sub> = 1.8 V		6.9	6.9	6.9	7.0	7.2	7.6	pF
		V <sub>CCI</sub> = 2.5 V		6.9	7.0	7.0	7.0	7.2	7.6	pF
CI	input capacitance	$V_{I} = 0 V \text{ or } V_{CCI}; V_{CCI} = 0 V \text{ to } 2.7 V$		0.6	0.6	0.6	0.6	0.6	0.6	pF
C <sub>O</sub>	output capacitance	V <sub>O</sub> = 0 V; V <sub>CCO</sub> = 0 V		1.8	1.8	1.8	1.8	1.8	1.8	pF

[1]  $C_{PD}$  is used to determine the dynamic power dissipation (P<sub>D</sub> in  $\mu$ W).

[2] Power dissipated from input supply (V<sub>CCI</sub>)

 $P_D = C_{PD} \times V_{CCI}^2 \times f_i \times N$  where:

 $C_{PD}$  = power dissipation capacitance of the input supply.

 $V_{CCI}$  = input supply voltage in V;

 $f_i$  = input frequency in MHz;

N = number of inputs switching;

[3] Power dissipated from output supply ( $V_{CCO}$ )

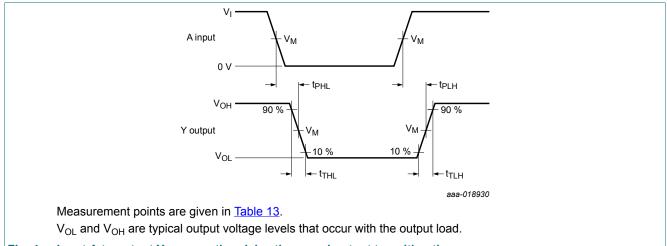
 $P_D = (C_L + C_{PD}) \times V_{CCO}^2 \times f_o$  where:

 $C_L$  = load capacitance in pF;

 $C_{PD}$  = power dissipation capacitance of the output supply.

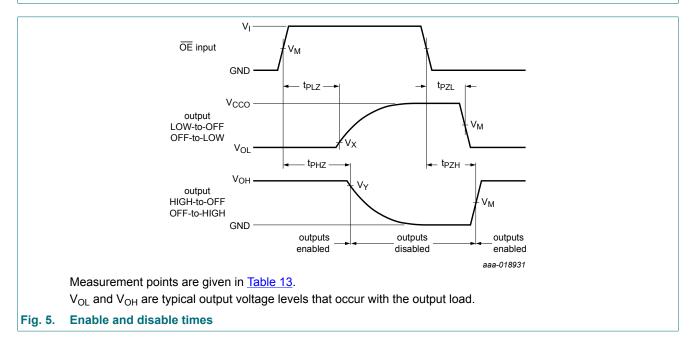
 $V_{CCO}$  = output supply voltage in V;

 $f_o$  = output frequency in MHz;



# 11.1. Waveforms, graphs and test circuit

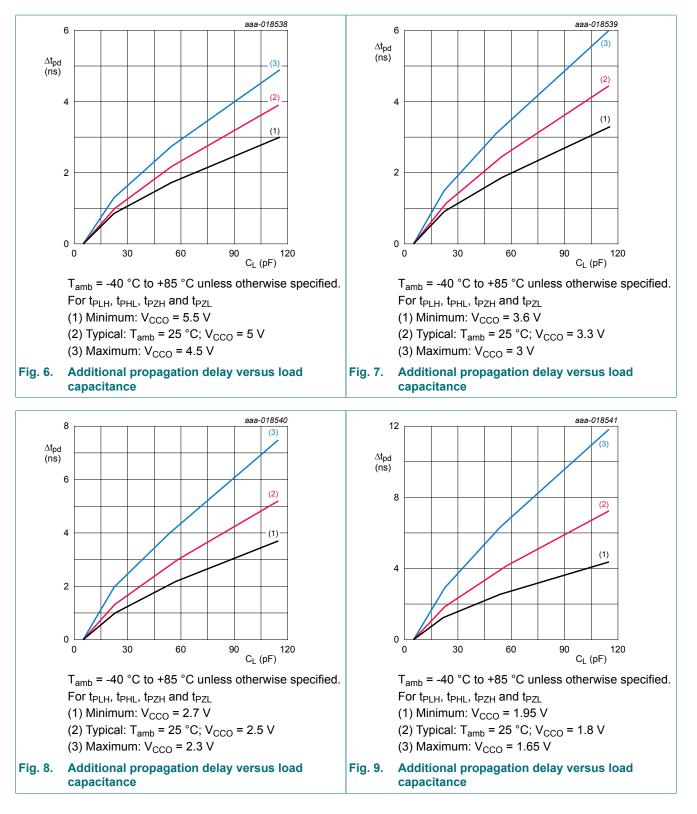
## Fig. 4. Input A to output Y propagation delay times and output transition times



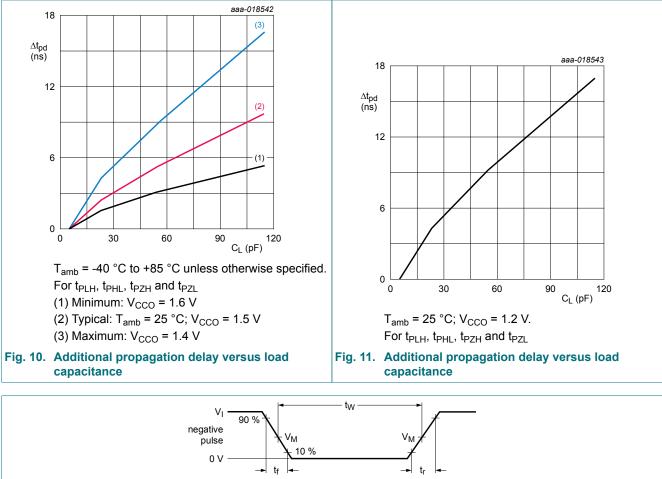
#### Table 13. Measurement points

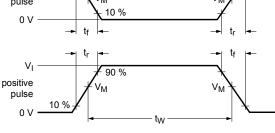
Supply voltage		Output			Input		
V <sub>CCI</sub>	V <sub>cco</sub>	V <sub>M</sub>	V <sub>X</sub>	V <sub>Y</sub>	V <sub>M</sub>	VI	
0.75 V to 2.7 V	1.2 V to 1.6 V	0.5V <sub>CCO</sub>	V <sub>OL</sub> + 0.1 V	V <sub>OH</sub> - 0.1 V	0.5V <sub>CCI</sub>	V <sub>CCI</sub>	
0.75 V to 2.7 V	1.65 V to 2.7 V	0.5V <sub>CCO</sub>	V <sub>OL</sub> + 0.15 V	V <sub>OH</sub> - 0.15 V	0.5V <sub>CCI</sub>	V <sub>CCI</sub>	
0.75 V to 2.7 V	3.0 V to 5.5 V	0.5V <sub>CCO</sub>	V <sub>OL</sub> + 0.3 V	V <sub>OH</sub> - 0.3 V	0.5V <sub>CCI</sub>	V <sub>CCI</sub>	

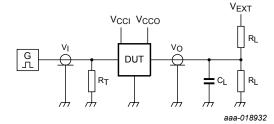
### Dual supply buffer/line driver; 3-state



## Dual supply buffer/line driver; 3-state







Test data is given in Table 14.

Definitions test circuit:

- $R_T$  = termination resistance should be equal to output impedance  $Z_o$  of the pulse generator.
- $C_L$  = load capacitance including jig and probe capacitance.
- R<sub>L</sub> = Load resistance.

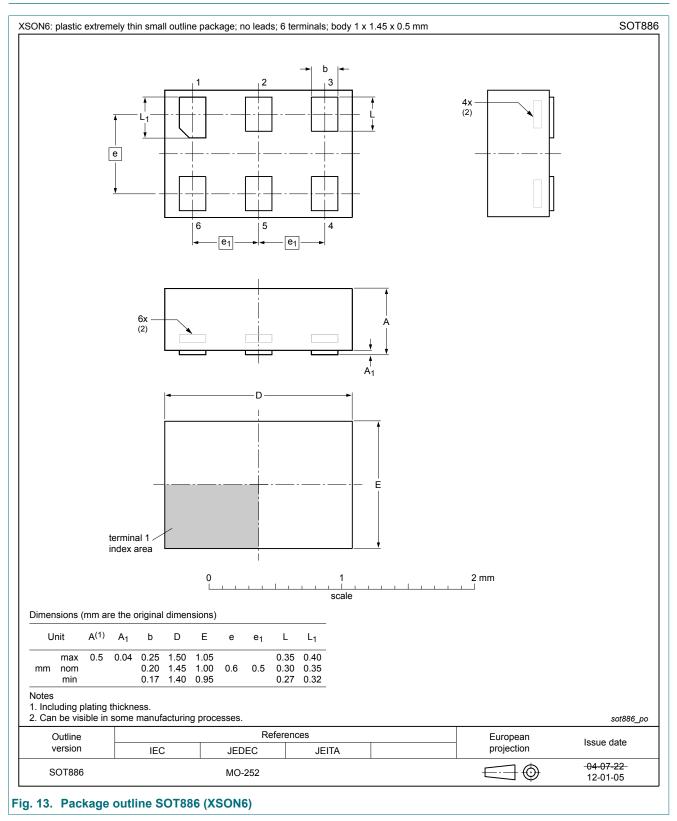
### Fig. 12. Test circuit for measuring switching times

## Table 14. Test data

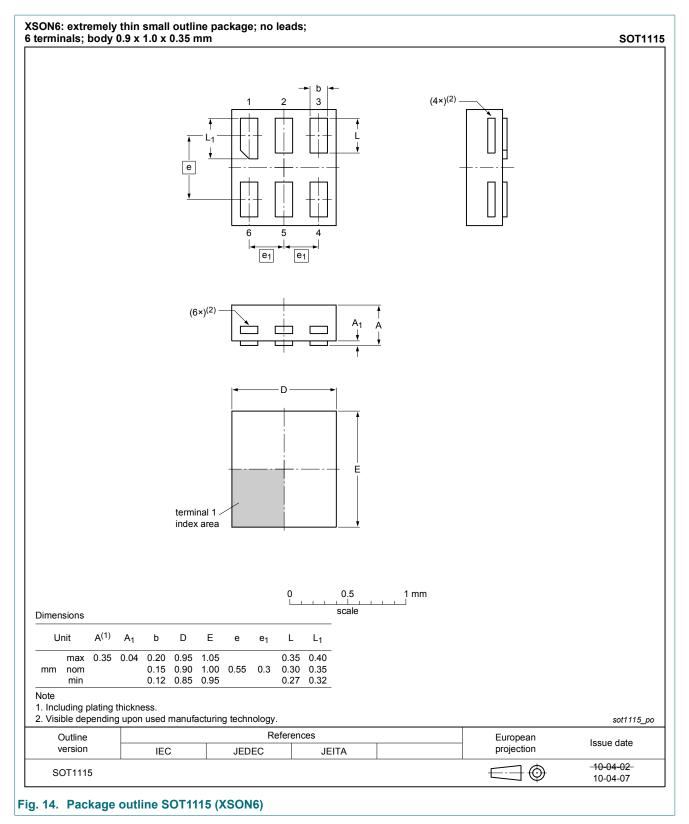
Supply voltage		Load		Input		V <sub>EXT</sub>		
V <sub>CCI</sub>	V <sub>cco</sub>	CL	RL	t <sub>r</sub> , t <sub>f</sub>	VI	t <sub>PLH</sub> , t <sub>PHL</sub>	t <sub>PZH</sub> , t <sub>PHZ</sub>	t <sub>PZL</sub> , t <sub>PLZ</sub>
0.75 V to 2.7 V	1.2 V to 5.5 V	5 pF	10 kΩ	≤3.0 ns	V <sub>CCI</sub>	GND	GND	2V <sub>CCO</sub>

74AXP1T125

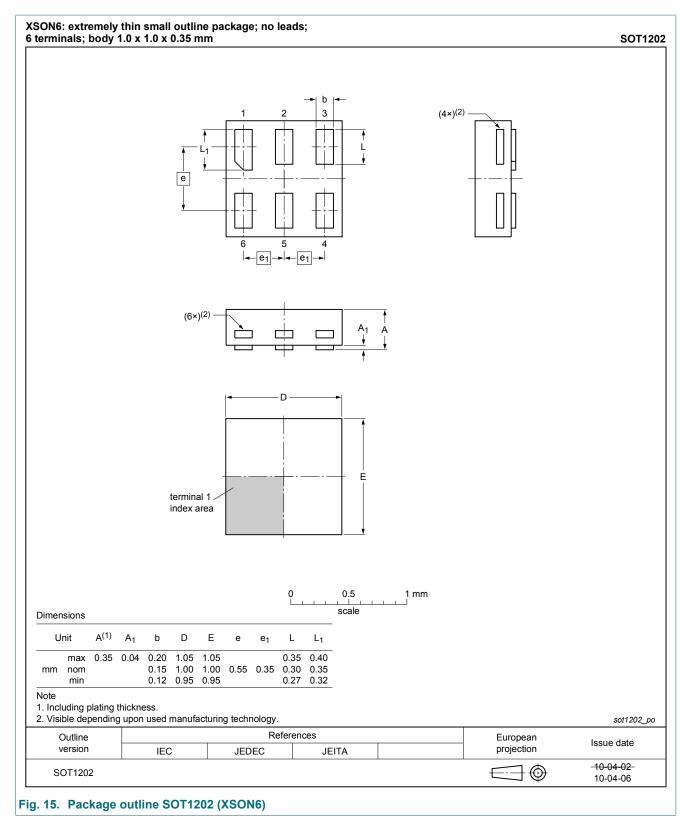
# 12. Package outline



## Dual supply buffer/line driver; 3-state



## Dual supply buffer/line driver; 3-state



# 13. Abbreviations

Table 15. Abbreviations						
Acronym	Description					
CDM	Charged Device Model					
DUT	Device Under Test					
ESD	ElectroStatic Discharge					
HBM	Human Body Model					

# 14. Revision history

## Table 16. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes			
74AXP1T125 v.2	20190322	Product data sheet	-	74AXP1T125 v.1			
Modifications:	<ul> <li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> <li>Type number 74AXP1T125GW (SOT363) removed.</li> </ul>						
74AXP1T125 v.1	20151221	Product data sheet	-	-			

# 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <u>https://www.nexperia.com</u>.

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