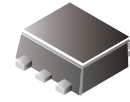


RoHS Device
Halogen Free

Features

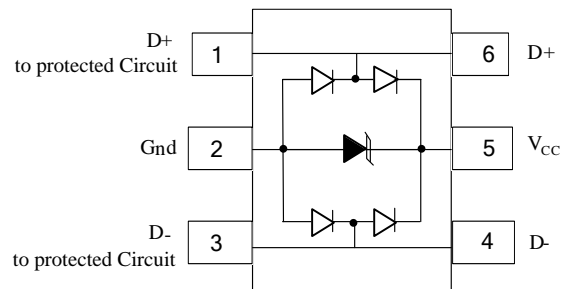
- 100 Watts Peak Power per Line ($t_p = 8/20\mu s$)
- Protects two I/O lines
- Low operating voltage: 5V
- Ultra Low capacitance($< 1.0pF$) for high-speed interfaces
- Solid-state technology



SOT-666

Mechanical Characteristics

- JEDEC SOT-666 package
- Molding compound flammability rating:UL 94V-0
- Marking : Making Code
- Packaging : Tape and Reel per EIA 481



Applications

- FireWire & USB
- Sensitive Analog Inputs
- Portable Electronics
- LAN/WAN equipment
- Video Line Protection
- Microcontroller Input Protection

IEC COMPATIBILITY (EN61000-4)

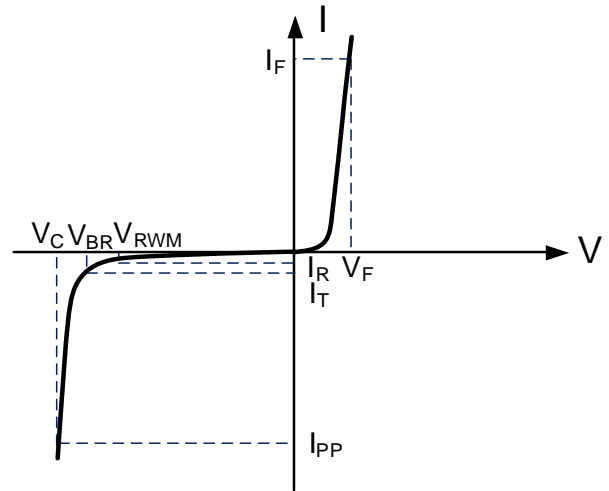
- IEC 61000-4-2 (ESD) $\pm 15kV$ (air), $\pm 8kV$ (contact)
- IEC 61000-4-4 (EFT) 40A (5/50ns)
- IEC 61000-4-5 (Lightning) 4A(8/20US)

Absolute Maximum Rating

Rating	Symbol	Value	Units
Peak Pulse Power ($t_p=8/20\mu s$)	P_{PP}	100	Watts
Peak Pulse Current ($t_p=8/20\mu s$)	I_{PP}	4	A
Lead Soldering Temperature	T_L	260 (10sec)	$^{\circ}C$
Operating Temperature	T_J	-55 to + 125	$^{\circ}C$
Storage Temperature	T_{STG}	-55 to +150	$^{\circ}C$

Electrical Parameters (T=25°C)

Symbol	Parameter
I_{PP}	Maximum Reverse Peak Pulse Current
V_C	Clamping Voltage @ I_{PP}
V_{RWM}	Working Peak Reverse Voltage
I_R	Maximum Reverse Leakage Current @ V_{RWM}
V_{BR}	Breakdown Voltage @ I_T



Electrical Characteristics

Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Units
Reverse Stand-Off Voltage	V_{RWM}				5.0	V
Breakdown Voltage	V_{BR}	$I_T=1mA$	6.0			V
Reverse Leakage Current	I_R	$V_{RWM}=5V, T=25^\circ C$			1.0	μA
Clamping Voltage	V_C	$I_{PP}=1A, t_p=8/20\mu s$		10		V
Clamping Voltage	V_C	$I_{PP}=4A, t_p=8/20\mu s$		25		V
Junction Capacitance	C_j	Between I/O pins and Ground $V_R=0V, f=1MHz$		0.8	1.0	pF
		Between I/O pins $V_R=0V, f=1MHz$		0.4	0.6	pF

Typical Characteristics

Figure 1: Peak Pulse Power Vs Pulse Time

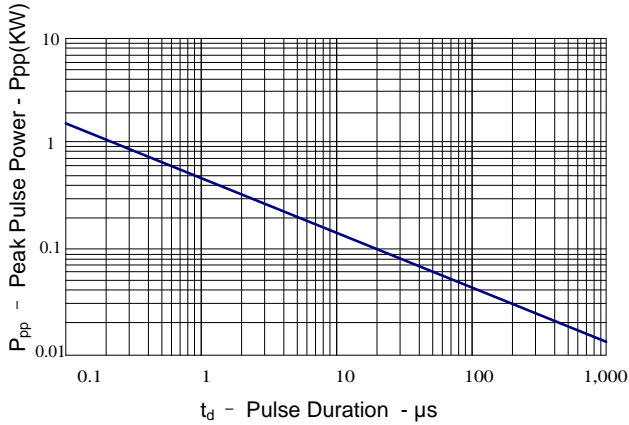


Figure 2: Power Derating Curve

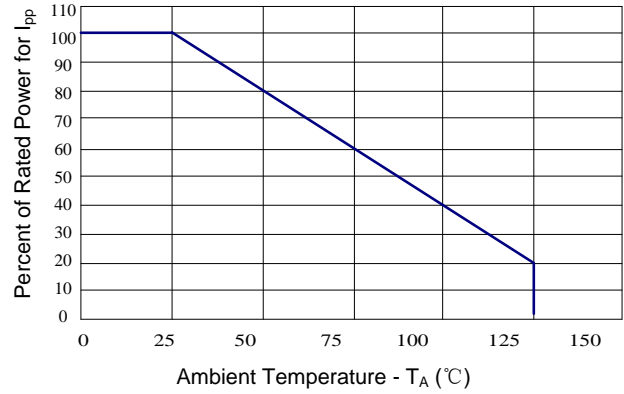


Figure 3: Pulse Waveform

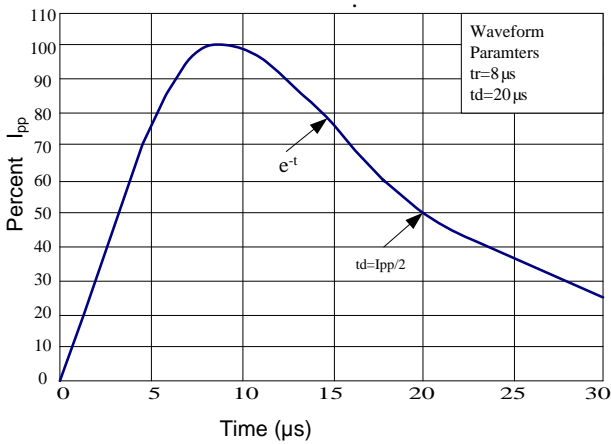


Figure 4: Clamping Voltage vs. Peak Pulse Current

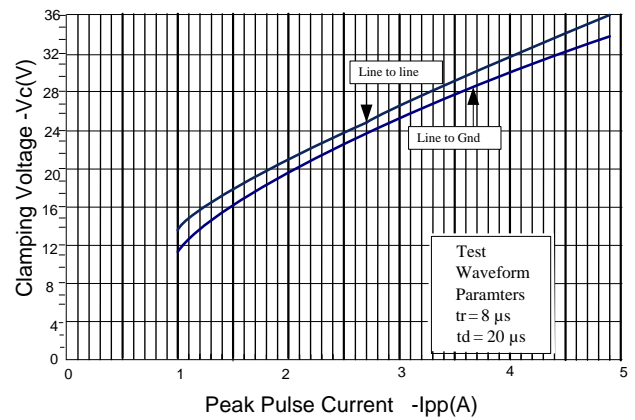


Figure 5: Forward Voltage vs. Forward Current

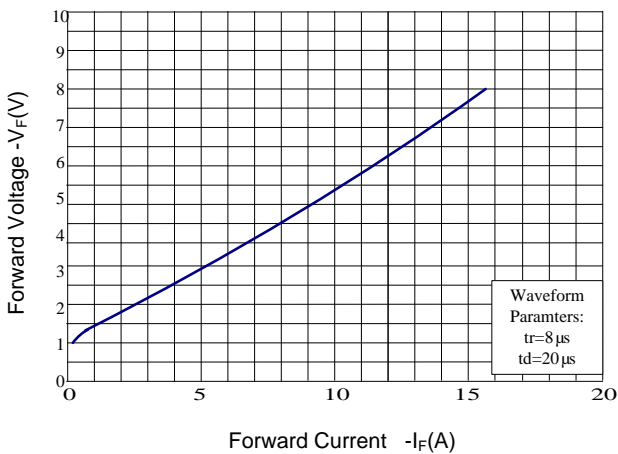
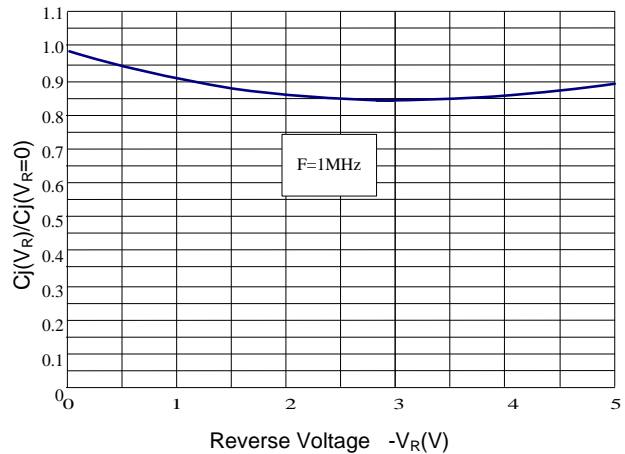


Figure 6: Capacitance vs. Reverse Voltage



Outline Drawing_ SOT-666

