

Photocouplers Photorelay

TLP170GM

1. Applications

- · Security Systems
- · I/O Interface Boards
- Factory Automation (FA)
- · Measuring Instruments
- · Mechanical relay replacements

2. General

The Toshiba TLP170GM consists of an infrared LED optically coupled to a photo-MOSFET in a 4-pin SO6 package, which is suitable for surface mount assembly.

The TLP170GM is suitable for the battery management systems which require space savings.

3. Features

(1) Halogen-free

For details, see "Devices in Halogen-Free Resin Packages" at the end of this datasheet.

- (2) Normally opened (1-Form-A)
- (3) OFF-state output terminal voltage: 350 V (min)
- (4) Trigger LED current: 1 mA (max)
- (5) ON-state current: 110 mA (max)
- (6) ON-state resistance: 35Ω (max, t < 1 s)
- (7) ON-state resistance: 50Ω (max, continuous)
- (8) Isolation voltage: 3750 Vrms (min)
- (9) Safety standards

UL-recognized: UL 1577, File No.E67349

cUL-recognized: CSA Component Acceptance Service No.5A File No.E67349

VDE-approved: EN 60747-5-5 (Note 1)

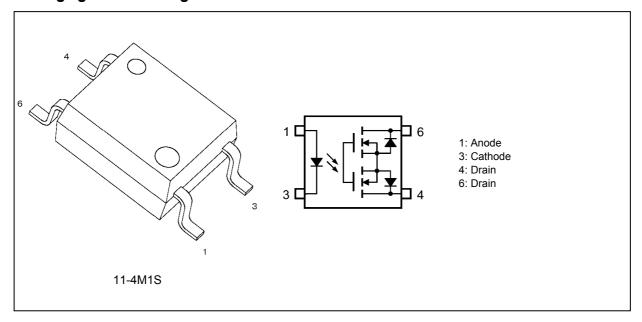
Note 1: When a VDE approved type is needed, please designate the **Option (V4)**.

Table 3.1 Mechanical Parameters

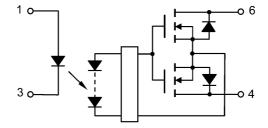
Characteristics	TLP170GM	Unit
Creepage distances	5.0 (min)	mm
Clearance distances	5.0 (min)	
Internal isolation thickness	0.2 (min)	



4. Packaging and Pin Assignment



5. Internal Circuit





6. Absolute Maximum Ratings (Note) (Unless otherwise specified, Ta = 25 °C)

	Characteristics		Symbol	Note	Rating	Unit
LED	Input forward current		I _F		30	mA
	Input forward current derating	(T _a ≥ 25 °C)	$\Delta I_F/\Delta T_a$		-0.3	mA/°C
	Input forward current (pulsed)	(100 μs pulse, 100 pps)	I _{FP}		1	Α
	Input reverse voltage		V_{R}		6	V
	Input power dissipation		P_{D}		50	mW
	Input power dissipation derating	$(T_a \ge 25 ^{\circ}C)$	$\Delta P_D/\Delta T_a$		-0.5	mW/°C
	Junction temperature		Tj		125	°C
Detector	OFF-state output terminal voltage		V_{OFF}		350	V
	ON-state current		I _{ON}		110	mA
	ON-state current derating	$(T_a \ge 25 ^{\circ}C)$	Δl _{ON} /ΔT _a		-1.1	mA/°C
	ON-state current (pulsed)	(t = 100 ms, duty = 1/10)	I _{ONP}		0.33	Α
	Output power dissipation		Po		300	mW
	Output power dissipation derating	$(T_a \ge 25 ^{\circ}C)$	$\Delta P_{O}/\Delta T_{a}$		-3.0	mW/°C
	Junction temperature		Tj		125	°C
Common	Storage temperature		T_{stg}		-55 to 125	
	Operating temperature		T_{opr}		-40 to 85	
	Lead soldering temperature	(10 s)	T _{sol}		260	
	Isolation voltage	(AC, 60 s, R.H. ≤ 60 %)	BV _S	(Note 1)	3750	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: This device is considered as a two-terminal device: Pins 1 and 3 are shorted together, and pins 4 and 6 are shorted together.

7. Recommended Operating Conditions (Note)

Characteristics	Symbol	Note	Min	Тур.	Max	Unit
Supply voltage	V_{DD}		_		280	V
Input forward current	I _F			2	25	mA
ON-state current	I _{ON}				110	
Operating temperature	T _{opr}		-20		85	°C

Note: The recommended operating conditions are given as a design guide necessary to obtain the intended performance of the device. Each parameter is an independent value. When creating a system design using this device, the electrical characteristics specified in this data sheet should also be considered.



8. Electrical Characteristics (Unless otherwise specified, T_a = 25 °C)

	Characteristics	Symbol	Note	Test Condition	Min	Тур.	Max	Unit
LED	Input forward voltage	V _F		I _F = 10 mA	1.1	1.27	1.4	V
	Input reverse current	I _R		V _R = 5 V			10	μА
	Input capacitance	Ct		V = 0 V, f = 1 MHz		30		pF
Detector	OFF-state current	I _{OFF}	·	V _{OFF} = 350 V	_	0.001	1	μА
	Output capacitance	C _{OFF}		V = 0 V, f = 1 MHz		30	_	pF

9. Coupled Electrical Characteristics (Unless otherwise specified, Ta = 25 °C)

Characteristics	Symbol	Note	Test Condition	Min	Тур.	Max	Unit
Trigger LED current	I _{FT}		I _{ON} = 110 mA	_	0.2	1	mA
Return LED current	I _{FC}		I _{OFF} = 100 μA	0.01	0.2	_	mA
ON-state resistance	R _{ON}		I _{ON} = 110 mA, I _F = 2 mA, t < 1 s	_	22	35	Ω
			I _{ON} = 110 mA, I _F = 2 mA	_	28	50	

10. Isolation Characteristics (Unless otherwise specified, T_a = 25 °C)

Characteristics	Symbol	Note	Test Condition	Min	Тур.	Max	Unit
Total capacitance (input to output)	Cs	(Note 1)	V _S = 0 V, f = 1 MHz	_	8.0		pF
Isolation resistance	R _S	(Note 1)	V_S = 500 V, R.H. \leq 60 %	5 × 10 ¹⁰	1014	_	Ω
Isolation voltage	BVS	(Note 1)	AC, 60 s	3750	_		Vrms

Note 1: This device is considered as a two-terminal device: Pins 1 and 3 are shorted together, and pins 4 and 6 are shorted together.

11. Switching Characteristics (Unless otherwise specified, Ta = 25 °C)

Characteristics	Symbol	Note	Test Condition	Min	Тур.	Max	Unit
Turn-on time	t _{ON}		See Fig. 11.1	_	1	2	ms
Turn-off time	t _{OFF}		$R_L = 200 \Omega$, $V_{DD} = 20 V$, $I_F = 2 mA$	_	0.5	1	

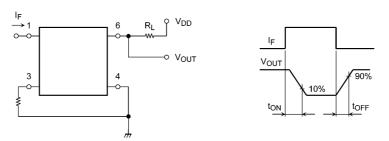


Fig. 11.1 Switching Time Test Circuit and Waveform



12. Characteristics Curves and Circuit Connections

12.1. Characteristics Curves (Note)

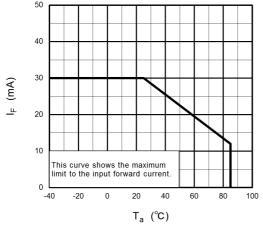


Fig. 12.1.1 I_F - T_a

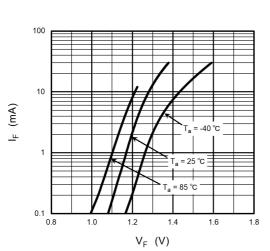


Fig. 12.1.3 I_F - V_F

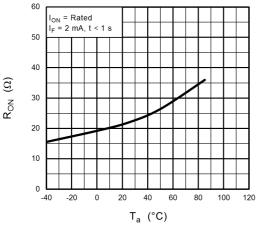


Fig. 12.1.5 R_{ON} - T_a

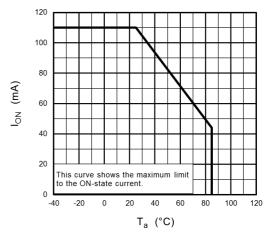


Fig. 12.1.2 I_{ON} - T_a

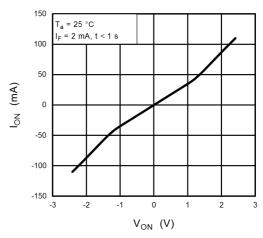


Fig. 12.1.4 I_{ON} - V_{ON}

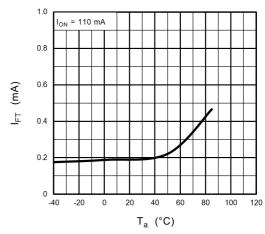


Fig. 12.1.6 I_{FT} - T_a



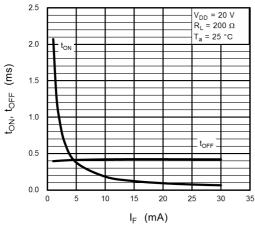


Fig. 12.1.7 t_{ON}, t_{OFF} - I_F

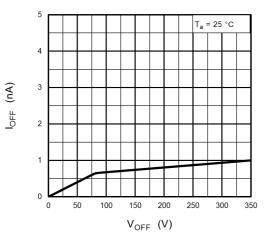


Fig. 12.1.9 I_{OFF} - V_{OFF}

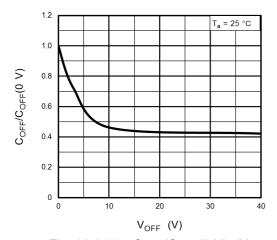


Fig. 12.1.11 C_{OFF}/C_{OFF}(0 V) - V_{OFF}

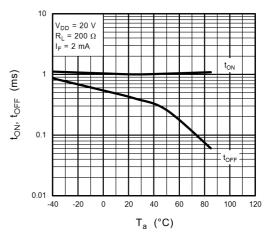


Fig. 12.1.8 t_{ON}, t_{OFF} - T_a

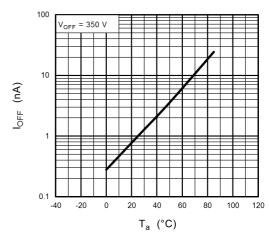


Fig. 12.1.10 I_{OFF} - T_a

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



13. Soldering and Storage

13.1. Precautions for Soldering

The soldering temperature should be controlled as closely as possible to the conditions shown below, irrespective of whether a soldering iron or a reflow soldering method is used.

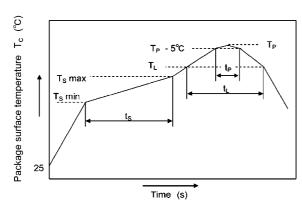
· When using soldering reflow.

The soldering temperature profile is based on the package surface temperature.

(See the figure shown below, which is based on the package surface temperature.)

Reflow soldering must be performed once or twice.

The mounting should be completed with the interval from the first to the last mountings being 2 weeks.



	Symbol	Min	Max	Unit
Preheat temperature	Ts	150	200	°C
Preheat time	ts	60	120	s
Ramp-up rate (T _L to T _P)			3	°C/s
Liquidus temperature	TL	217		°C
Time above T _L	t∟	60	150	s
Peak temperature	T _P		260	°C
Time during which T_c is between ($T_P - 5$) and T_P	t _P		30	s
Ramp-down rate (T _P to T _L)			6	°C/s

An Example of a Temperature Profile When Lead(Pb)-Free Solder Is Used

· When using soldering flow

Preheat the device at a temperature of 150 $^{\circ}\text{C}$ (package surface temperature) for 60 to 120 seconds.

Mounting condition of 260 $^{\circ}$ C within 10 seconds is recommended.

Flow soldering must be performed once.

· When using soldering Iron

Complete soldering within 10 seconds for lead temperature not exceeding 260 °C or within 3 seconds not exceeding 350 °C

Heating by soldering iron must be done only once per lead.

13.2. Precautions for General Storage

- · Avoid storage locations where devices may be exposed to moisture or direct sunlight.
- Follow the precautions printed on the packing label of the device for transportation and storage.
- Keep the storage location temperature and humidity within a range of 5 °C to 35 °C and 45 % to 75 %, respectively.
- Do not store the products in locations with poisonous gases (especially corrosive gases) or in dusty conditions.
- Store the products in locations with minimal temperature fluctuations. Rapid temperature changes during storage can cause condensation, resulting in lead oxidation or corrosion, which will deteriorate the solderability of the leads.
- · When restoring devices after removal from their packing, use anti-static containers.
- · Do not allow loads to be applied directly to devices while they are in storage.
- If devices have been stored for more than two years under normal storage conditions, it is recommended that you check the leads for ease of soldering prior to use.

Rev.1.0



14. Ordering Information (Example of Item Name)

Item Name	Packaging (Note 1)	VDE Ootion	Packing (MOQ)
TLP170GM(E	SMD		Magazine (125 pcs)
TLP170GM(TPL,E	SMD		Tape and reel (3000 pcs)
TLP170GM(TPR,E	SMD		Tape and reel (3000 pcs)
TLP170GM(V4,E	SMD	EN 60747-5-5	Magazine (125 pcs)
TLP170GM(V4TPL,E	SMD	EN 60747-5-5	Tape and reel (3000 pcs)
TLP170GM(V4TPR,E	SMD	EN 60747-5-5	Tape and reel (3000 pcs)

Note 1: SMD: Surface Mount Device

15. Devices in Halogen-Free Resin Packages

· This product is Halogen-Free

Toshiba Electronic Devices & Storage Corporation ("Toshiba") defines a "Halogen-Free resin semiconductor product" as a semiconductor product in which:

- (1) the encapsulating resins do not contain any of the following elements: bromine (Br), chlorine (Cl) and antimony (Sb), respectively, in an amount exceeding 0.09 weight percent, and do not contain chlorine and bromine in an aggregate amount exceeding 0.15 weight percent of the encapsulating resins, and/or
- (2) the resin portion(s) in printed circuit boards do not contain any of the following elements: bromine, chlorine and antimony, respectively, in an amount exceeding 0.09 weight percent, and do not contain chlorine and bromine in an aggregate amount exceeding 0.15 weight percent of the each resin portion(s) in printed circuit boards.

For avoidance of doubt, "Halogen-Free resin semiconductor product" does not mean, and Toshiba does not make any warranty of any kind, that said semiconductor product is entirely free of antimony or of any of the following elements of the halogen family: bromine, chlorine, iodine (I), fluorine (F) and astatine (At).

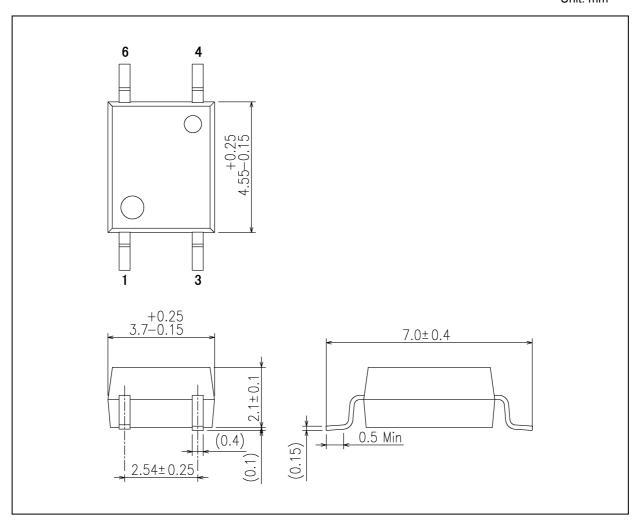
In addition, a Halogen-Free resin semiconductor product may contain antimony and/or any of the elements of the halogen family as mentioned in the above paragraph in one or more portion(s) of the semiconductor product other than the encapsulating resins and the resin portion(s) in printed circuit boards.

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Package Dimensions

Unit: mm



Weight: 0.1 g (typ.)

	Package Name(s)
TOSHIBA: 11-4M1S	



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