

RoHS

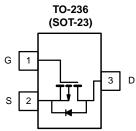
COMPLIANT

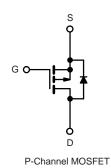
## P-Channel 60 V (D-S) MOSFET

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	<b>R<sub>DS(on)</sub> (</b> Ω <b>)</b>	V <sub>GS(th)</sub> (V)	I <sub>D</sub> (mA)			
- 60	3 at $V_{GS}$ = - 10 V	- 1 to - 3	-500			

#### **FEATURES**

- Halogen-free According to IEC 61249-2-21 • Definition
- TrenchFET<sup>®</sup> Power MOSFET •
- High-Side Switching •
- Low On-Resistance: 3  $\Omega$
- Low Threshold: 2 V (typ.)
- Fast Swtiching Speed: 20 ns (typ.)
- Low Input Capacitance: 20 pF (typ.) •
- Compliant to RoHS Directive 2002/95/EC



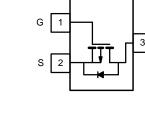


<b>ABSOLUTE MAXIMUM RATINGS</b> $T_A = 25 \text{ °C}$ , unless otherwise noted					
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage		V <sub>DS</sub>	- 60		
Gate-Source Voltage		V <sub>GS</sub>	± 20	V	
	T <sub>A</sub> = 25 °C	I <sub>D</sub>	- 500		
Continuous Drain Current <sup>a</sup>	T <sub>A</sub> = 100 °C		- 350	mA	
Pulsed Drain Current <sup>b</sup>		I <sub>DM</sub>	-1500		
	T <sub>A</sub> = 25 °C	D	460	m)\//	
Power Dissipation <sup>a</sup>	T <sub>A</sub> = 100 °C	PD	240	mW	
Maximum Junction-to-Ambient <sup>a</sup>		R <sub>thJA</sub>	350	°C/W	
Operating Junction and Storage Temperature Range		T <sub>J,</sub> T <sub>stg</sub>	- 55 to 150	°C	

Notes:

a. Surface mounted on FR4 board.

b. Pulse width limited by maximum junction temperature.



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<b>SPECIFICATIONS</b> $T_A = 25 \text{ °C}$ , unless otherwise noted							
			Limits				
Parameter	Symbol	Test Conditions	Min.	Typ. <sup>a</sup>	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS}$ = 0 V, $I_D$ = - 10 $\mu$ A	- 60			v	
Gate-Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = -250 \ \mu A$	- 1		- 3	v	
		$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 10	μA	
Gate-Body Leakage		$V_{DS} = 0 V, V_{GS} = \pm 10 V$			± 200		
Gale-Douy Leakage	I <sub>GSS</sub>	$V_{DS}$ = 0 V, $V_{GS}$ = ± 10 V, $T_{J}$ = 85 °C			± 500		
		$V_{DS} = 0 V$ , $V_{GS} = \pm 5 V$			± 100	nA	
Zara Cata Valtara Drain Current		$V_{DS} = -60 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	/		- 25	1	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS}$ = - 60 V, $V_{GS}$ = 0 V, $T_{J}$ = 85 °C			- 250		
On Otata Duain Ourrant		V <sub>GS</sub> = - 10 V, V <sub>DS</sub> = - 4.5 V	- 50				
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>GS</sub> = - 10 V, V <sub>DS</sub> = - 10 V	- 600			mA	
	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 25 mA	4				
Drain-Source On-Resistance <sup>a</sup>		V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 100 mA		3		Ω	
		V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 100 mA, T <sub>J</sub> =125 °C			9	1	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 10 V, I <sub>D</sub> = - 100 mA	80			mS	
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> = - 100 mA, V <sub>GS</sub> = 0 V			- 1.4	V	
Dynamic							
Total Gate Charge	Qg			2.0		nC	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = -30 \text{ V}, V_{GS} = -15 \text{ V}$ $I_{D} \cong -100 \text{ mA}$		1.2			
Gate-Drain Charge	Q <sub>gd</sub>			0.8			
Input Capacitance	C <sub>iss</sub>			23		pF	
Output Capacitance	C <sub>oss</sub>	$V_{DS} = -25 V, V_{GS} = 0 V$ f = 1 MHz		10			
Reverse Transfer Capacitance	C <sub>rss</sub>			5		]	
Switching <sup>b</sup>		·					
Turn-On Time	t <sub>d(on)</sub>	V <sub>DD</sub> = - 25 V, R <sub>L</sub> = 150 Ω		20		ns	
Turn-Off Time	t <sub>d(off)</sub>	$I_D \cong$ - 200 mA, $V_{GEN}$ = - 10 V, $R_g$ = 10 $\Omega$		35			

Notes:

a. Pulse test: PW  $\leq$  300  $\mu s$  duty cycle  $\leq$  2 %.

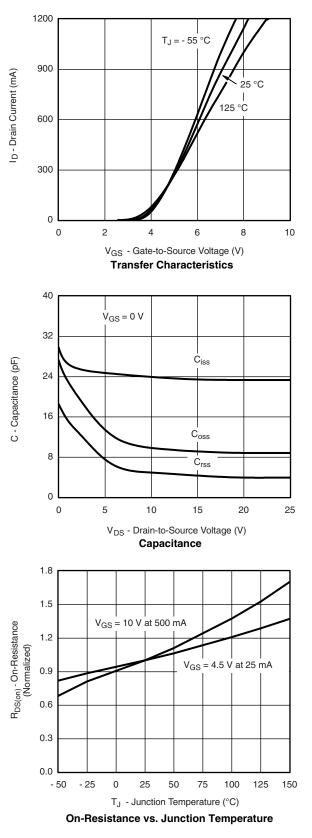
b. Switching time is essentially independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



#### 1.0 $V_{GS} = 10.V$ 5 V 0.8 6 V I<sub>D</sub> - Drain Current (A) 0.6 4 V 0.4 3 V 0.2 2 V 0.0 0 1 2 3 4 5 V<sub>DS</sub> - Drain-to-Source Voltage (V) **Output Characteristics** 15 V<sub>GS</sub> = 4.5 V 12 $R_{DS(on)}$ - On-Resistance ( $\Omega$ ) 9 $V_{GS} = 5 V$ 6 V<sub>GS</sub> = 10 V 3 0 0 200 400 600 800 1000 I<sub>D</sub> - Drain Current (mA) **On-Resistance vs. Drain Current** 15 I<sub>D</sub> = 500 mA V<sub>GS</sub> - Gate-to-Source Voltage (V) 12 $V_{DS} = 30 V$ V<sub>DS</sub> = 48 V 9 6 3

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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0

0.0

0.3

0.6

0.9

Qg - Total Gate Charge (nC)

Gate Charge

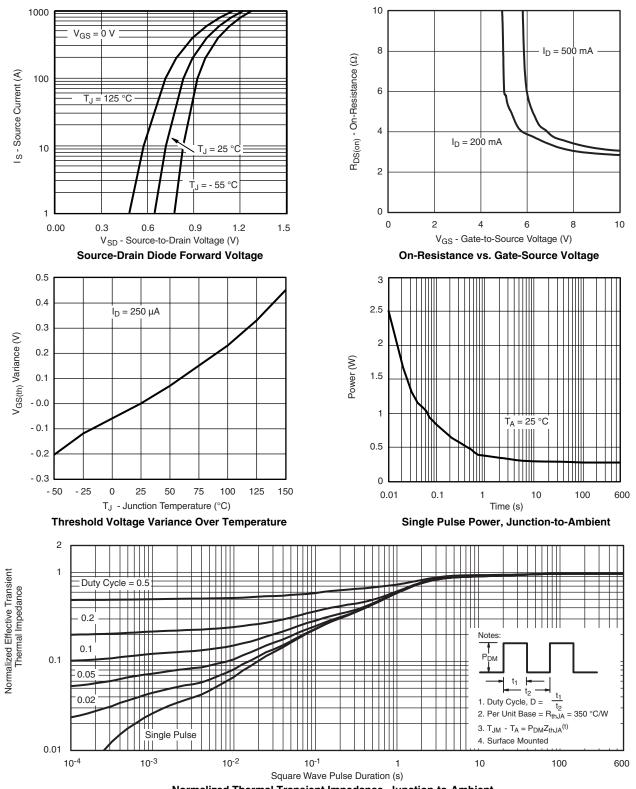
1.2

1.5

1.8



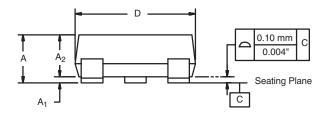
#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





### SOT-23 (TO-236): 3-LEAD



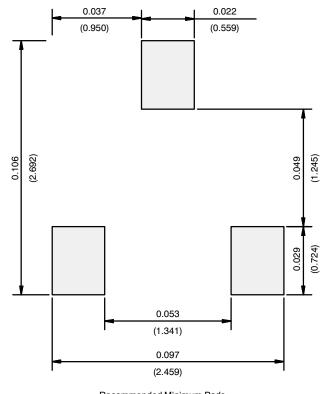




Dim	MILLIMETERS		INCHES		
	Min	Мах	Min	Мах	
Α	0.89	1.12	0.035	0.044	
A <sub>1</sub>	0.01	0.10	0.0004	0.004	
A <sub>2</sub>	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
C	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E <sub>1</sub>	1.20	1.40	0.047	0.055	
е	0.95 BSC		0.0374 Ref		
e <sub>1</sub>	1.90 BSC		0.0748 Ref		
L	0.40	0.60	0.016	0.024	
L <sub>1</sub>	0.64 Ref		0.025 Ref		
S	0.50 Ref		0.020 Ref		
q	3°	8°	3°	8°	
ECN: S-03946-Rev. K, 09- DWG: 5479	Jul-01	•	·		



#### **RECOMMENDED MINIMUM PADS FOR SOT-23**



Recommended Minimum Pads Dimensions in Inches/(mm)



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