

General Description

The WSD4050DN is the highest performance trench N-ch MOSFETs with extreme high cell density, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

The WSD4050DN meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

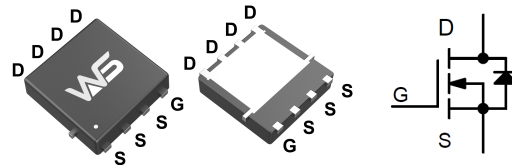
Product Summary

BVDSS	RDSON	ID
40V	7.4mΩ	50A

Applications

- High Frequency Point-of-Load Synchronous Buck Converter for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- Load Switch

DFN3.3X3.3-EP Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	40	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D@T_C=25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^G$	50	A
$I_D@T_C=100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^G$	30	A
$I_{DM}@T_C=25^\circ C$	Pulsed Drain Current ^C	105	A
EAS	Avalanche Energy, Single Pulse (L=0.3mH)	60	mJ
I_{AS}	Avalanche Current	20	A
$P_D@T_A=25^\circ C$	Total Power Dissipation ^A	5.0	W
$P_D@T_A=70^\circ C$	Total Power Dissipation ^A	3.2	W
T_J T_{STG}	Storage and Junction Temperature Range	-55 to 150	$^\circ C$

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ^A	---	60	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction-Case ^A	---	4.6	$^\circ C/W$

Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	40	---	---	V
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V, I _D =7A	---	7.4	9.5	mΩ
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =4.5V, I _D =5A	---	10	12	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	1.0	1.5	2.0	V
ΔV _{GS(th)}	V _{GS(th)} Temperature Coefficient		---	-6.	---	mV/°C
I _{DSS}	Drain-Source Leakage Current	V _{DS} =32V, V _{GS} =0V, T _J =25°C	---	-	2	uA
		V _{DS} =32V, V _{GS} =0V, T _J =55°C	---	-	10	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V	---	-	±100	nA
g _{fs}	Forward Transconductance	V _{DS} =5V, I _D =20A	---	70	---	S
R _g	Gate Resistance	V _{DS} =0V, V _{GS} =0V, f=1MHz	---	1.8	2.7	Ω
Q _g	Total Gate Charge (10V)	V _{DS} =20V, V _{GS} =10V, I _{DS} =20A	---	22	45	nC
Q _{gs}	Gate-Source Charge		---	5.5	7.5	
Q _{gd}	Gate-Drain Charge		---	3.0	5.1	
T _{d(on)}	Turn-On Delay Time	V _{DS} =20V, R _L =1Ω, V _{GS} =10V, R _G =3Ω.	---	7.5	---	ns
T _r	Rise Time		---	2.0	---	
T _{d(off)}	Turn-Off Delay Time		---	23	---	
T _f	Fall Time		---	3.0	---	
C _{iss}	Input Capacitance	V _{DS} =20V, V _{GS} =0V, f=1MHz	---	1284	---	pF
C _{oss}	Output Capacitance		---	145	---	
C _{rss}	Reverse Transfer Capacitance		---	55	---	

A. The value of R_{θJA} is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with T_A=25° C. The Power dissipation P_{DSM} is based on R_{θJA} ≤ 10s and the maximum allowed junction temperature of 150° C. The value in any given application depends on the user's specific board design.

B. The power dissipation P_D is based on T_{J(MAX)}=150° C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.

C. Single pulse width limited by junction temperature T_{J(MAX)}=150° C.

D. The R_{θJA} is the sum of the thermal impedance from junction to case R_{θJC} and case to ambient.

E. The static characteristics in Figures 1 to 6 are obtained using <300μs pulses, duty cycle 0.5% max.

F. These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsink, assuming a maximum junction temperature of T_{J(MAX)}=150° C. The SOA curve provides a single pulse rating.

G. The maximum current rating is package limited.

H. These tests are performed with the device mounted on 1 in² FR-4 board with 2oz. Copper, in a still air environment with T_A=25° C.

Typical Operating Characteristics

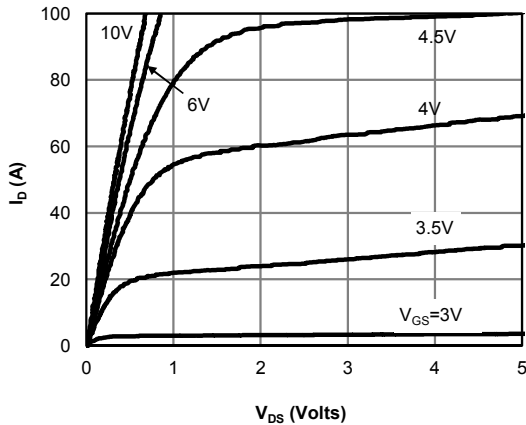


Figure 1: On-Region Characteristics (Note E)

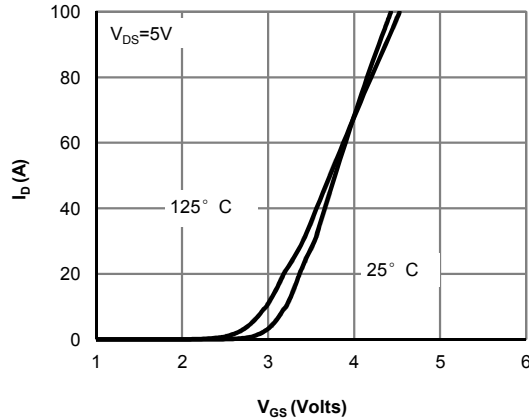


Figure 2: Transfer Characteristics (Note E)

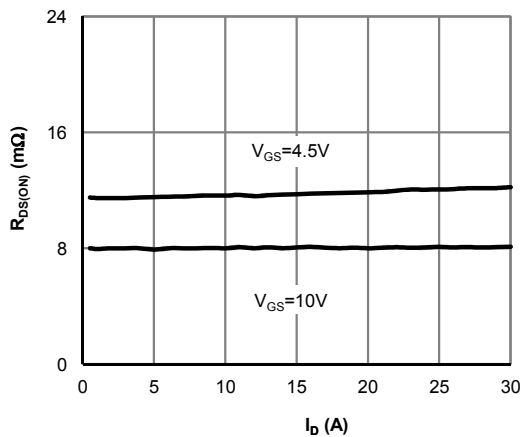


Figure 3: On-Resistance vs. Drain Current and Gate Voltage (Note E)

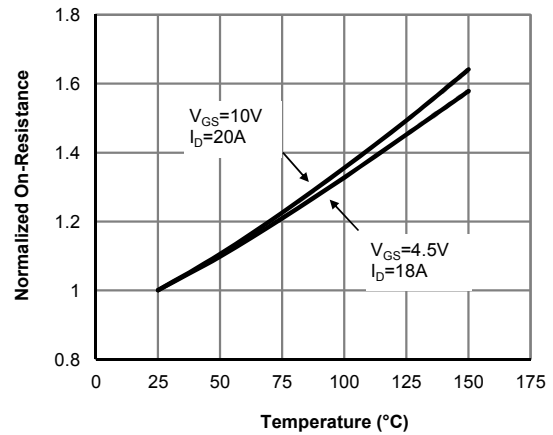


Figure 4: On-Resistance vs. Junction Temperature (Note E)

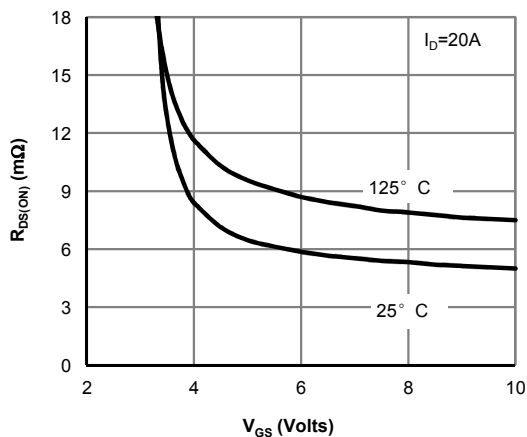


Figure 5: On-Resistance vs. Gate-Source Voltage (Note E)

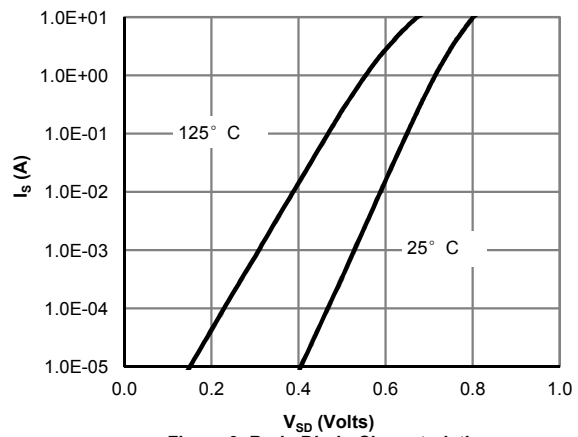


Figure 6: Body-Diode Characteristics (Note E)

Typical Operating Characteristics (Cont.)

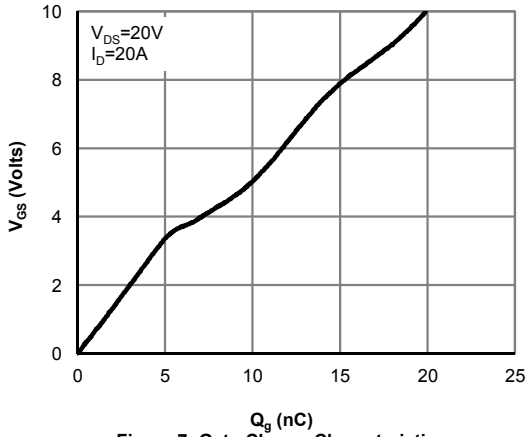


Figure 7: Gate-Charge Characteristics

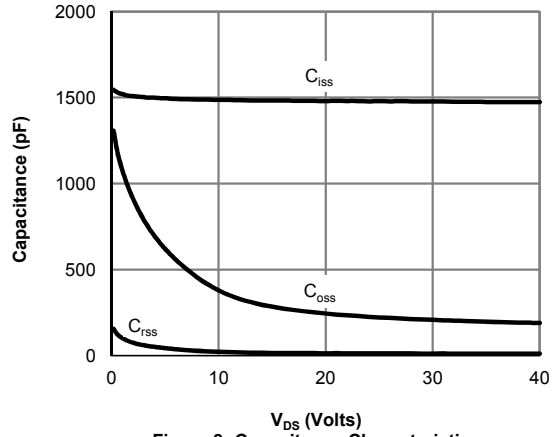


Figure 8: Capacitance Characteristics

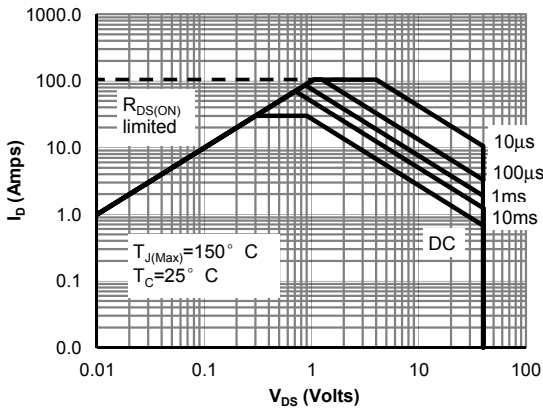


Figure 9: Maximum Forward Biased Safe Operating Area (Note F)

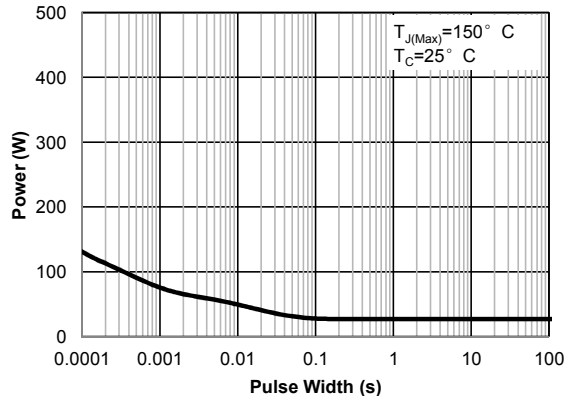


Figure 10: Single Pulse Power Rating Junction-to-Case (Note F)

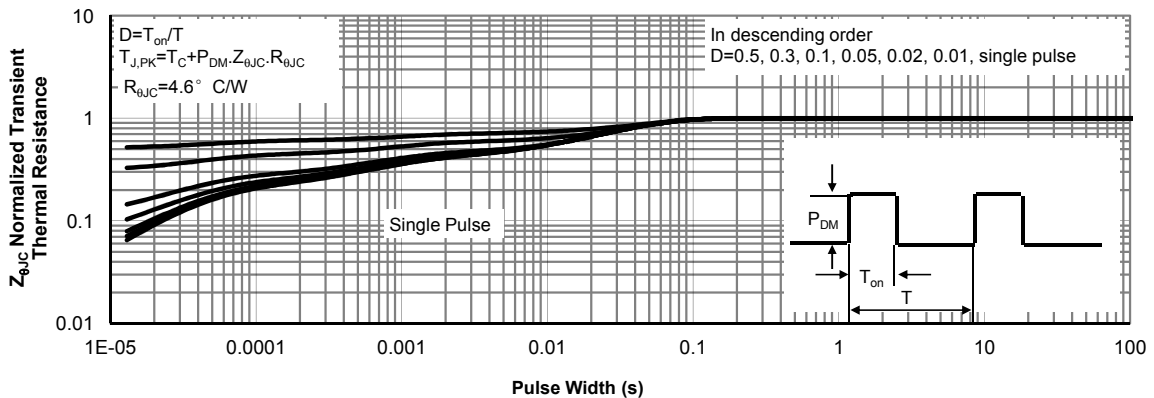


Figure 11: Normalized Maximum Transient Thermal Impedance (Note F)

Typical Operating Characteristics (Cont.)

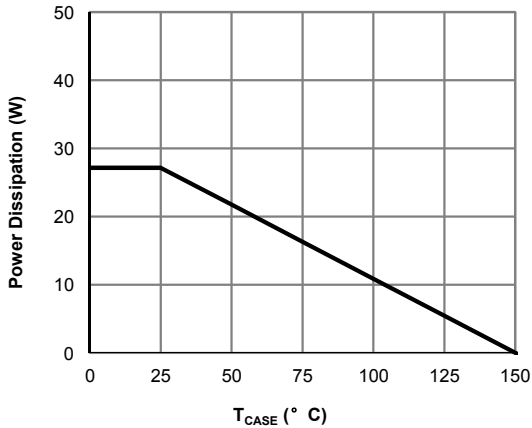


Figure 12: Power De-rating (Note F)

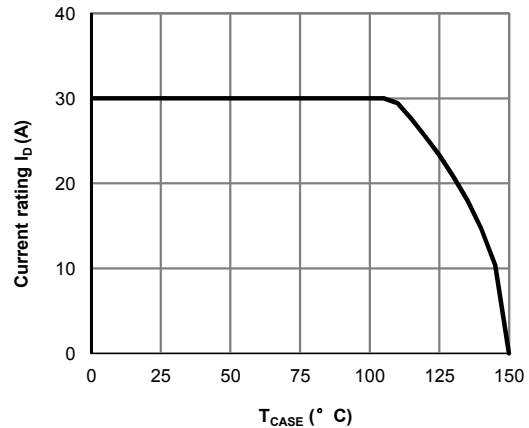


Figure 13: Current De-rating (Note F)

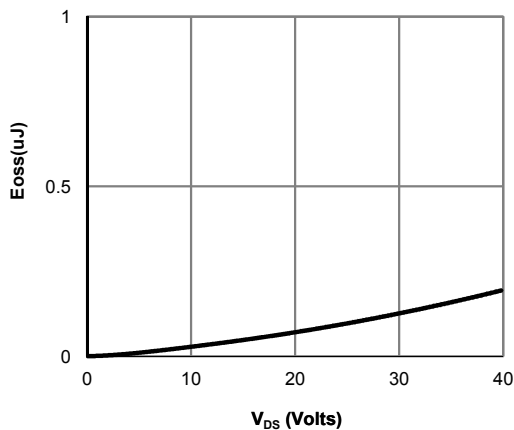


Figure 14: Coss stored Energy

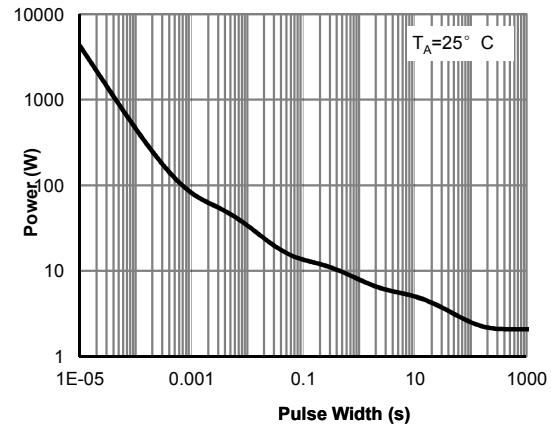


Figure 15: Single Pulse Power Rating Junction-to-Ambient (Note H)

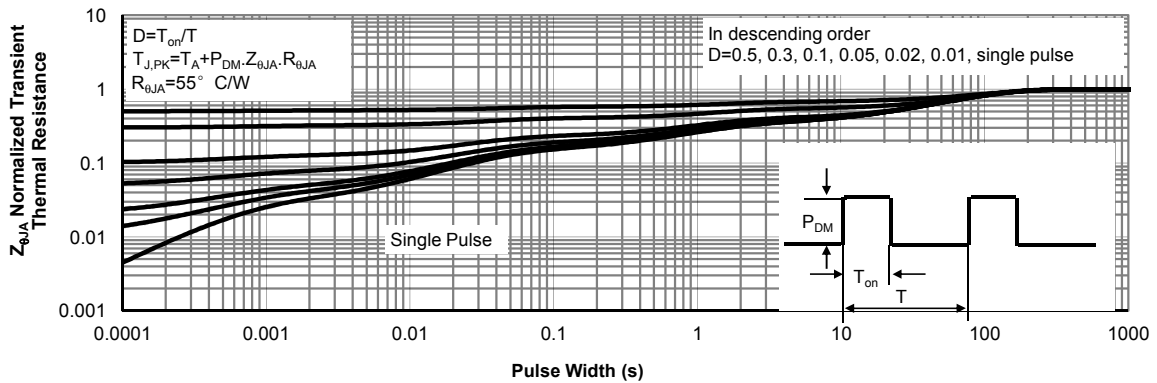


Figure 16: Normalized Maximum Transient Thermal Impedance (Note H)



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